



ED-CM4IO COMPUTER

AN INDUSTRIAL EMBEDDED COMPUTER BASED ON RASPBERRY PI CM4

Shanghai EDA Technology Co.,Ltd 2023-02-07



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Release Note

Date	Version	Description	Note
2023-02-07	V1.0	Initial version	



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1 Product Overview

ED-CM4IO Computer is a commercial industrial computer based on Compute Module 4 IO Board and CM4 module.

1.1 Target Application

- Industrial application
- Advertising display
- Intelligent manufacture
- Maker develop

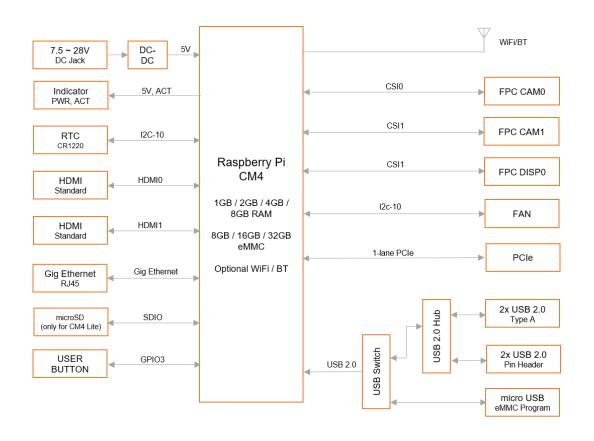
1.2 Specifications and Parameters

Function	Parameters
CPU	Broadcom BCM2711 4 core, ARM Cortex-A72(ARM v8), 1.5GHz, 64bit CPU
Memory	1GB / 2GB / 4GB / 8GB option
eMMC	0GB / 8GB / 16GB / 32GB option
SD card	micro SD card, support CM4 Litewithout eMMC
Ethernet	1x Gigabit Ethernet
WiFi / Bluetooth	2.4G / 5.8G Dual band WiFi, bluetooth5.0
HDMI	2x standard HDMI
DSI	2x DSI
Camera	2x CSI
USB Host	2x USB 2.0 Type A, 2x USB 2.0 Host Pin Header extended, 1x USB micro-B for eMMC burning
PCle	1-lane PCIe 2.0, Highest support 5Gbps
40-Pin GPIO	Raspberry Pi 40-Pin GPIO HAT extended
Real time clock	1x RTC
One-button on-off	Software on/off based on GPIO
Fan	1x adjustable speed fan control interface
DC power supply output	5V@1A, 12V@1A,
LED indicator	red(power indicator), green(system state indicator)
Power input	7.5V-28V



Function	Parameters	
Dimensions	180(length) x 120(wide) x 36(high) mm	
Case	Full Metal Shell	
Antenna accessory Support optional WiFi/BT external antenna, which has passed wireless authentication together with Raspberry Pi CM4, and optional 4G external antenna.		
Operation system	Compatible with official Raspberry Pi OS, provides BSP software support package, and supports online installation and update of APT.	

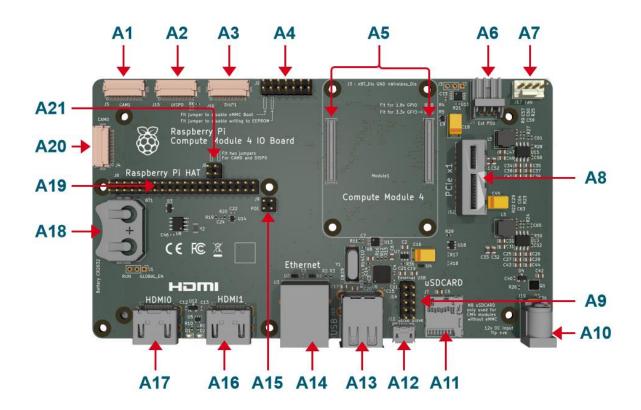
1.3 System Diagram



ED-CM4IO Computer



1.4 Functional Layout



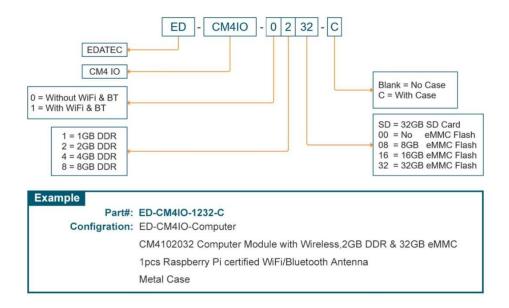
No.	Function	No.	Function
A1	CAM1 port	A13	2× USB port
A2	DISP0 port	A14	Ethernet RJ45 port
A3	DISP1 port	A15	POE port
A4	CM4 Config Pin Header	A16	HDMI1 port
A5	CM4 socket	A17	HDMI0 port
A6	External power output port	A18	RTC battery socket
A7	Fan control port	A19	40 Pin Header
A8	PCIe port	A20	CAM0 port
A9	2× USB Pin Header	A21	I2C-0 connect Pin Header
A10	DC power socket		
A11	Micro SD slot		
A12	Micro USB port		

1.5 Packing List

- 1x CM4 IO Computer host
- 1x 2.4GHz/5GHz WiFi/BT antenna

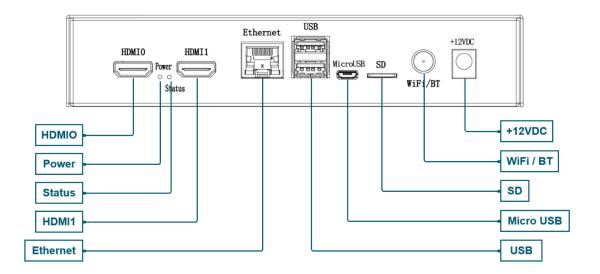


1.6 Order Code

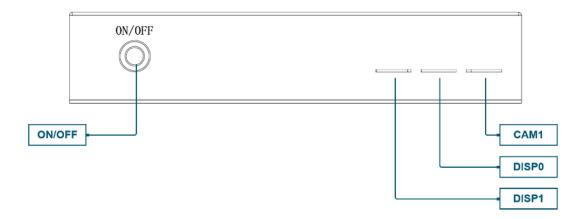


2 Product Appearance and Structure

2.1 Product Appearance







2.2 Product Photo



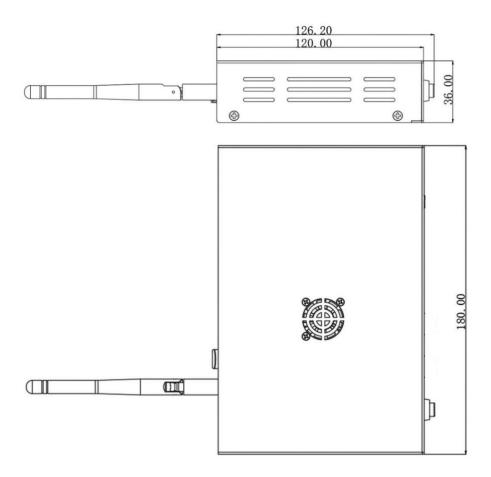


EDA Technology Co.,LTD- Electronics Development Accelerator



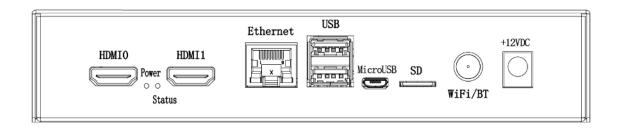
2.3 Dimensions

unit: mm, tolerance: ± 0.1 mm



3 Interfaces and Connectors

2.4 Front Panel





2.4.1 Power Input

The standard input power supply of ED-CM4IO Computer is 12V, the maximum power input range is +7.5V~28V, and the screen printing of the shell is +12VDC, which is a standard 5.5*2.0 DC Jack power socket.

2.4.2 Indicator Light

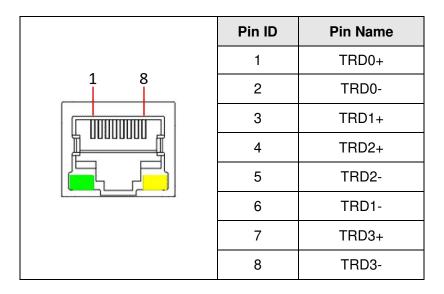
ED-CM4IO Computer has two lights, the red LED is the power indicator and the green LED is the status indicator.

2.4.3 HDMI

ED-CM4IO Computer has two standard HDMI Type A interfaces, the screen names are HDMI0 and HDMI1 respectively, and the two HDMI interfaces are identical, so it can work by directly connecting the HDMI display screen.

2.4.4 Gigabit Ethernet

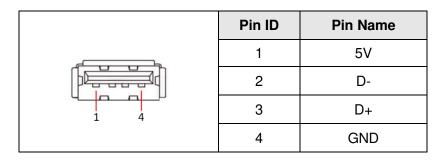
There is an adaptive 10/100/1000Mbsp Ethernet interface on ED-CM4IO Computer, and the screen printing logo is Ethernet. It is recommended to use Cat6 (Category 6) network cable to cooperate with it.



2.4.5 USB 2.0

There are two USB 2.0 Type A interfaces on the ED-CM4IO Computer, which can transmit data up to 480 Mbit/s. If we use our factory image, the USB Host function is available. If we use the official image, we





2.4.6 micro-USB

The ED-CM4IO Computer also has a MicroUSB, the screen name of which is micro USB, which is located on the right side of the USB socket and is mainly used to burn the eMMC of the CM4 core board.

2.4.7 micro-SD Card Slot

There is a micro SD card slot on ED-CM4IO Computer, which is used together with CM4 Lite.

Only when you choose CM4 Lite (without eMMC) core module, you need to use the micro SD card for main file system storage. If you choose CM4 module with eMMC, this micro SD card slot is not available, and it shares an MMC port with eMMC.

2.4.8 Antennas

The ED-CM4IO Computer panel has a standard SMA head antenna interface corresponding to the WiFi/BT antenna.

2.5 Rear Panel





2.5.1 Power ON/OFF Button

ED-CM4IO Computer has an on-off button, and the screen printing on the panel is "ON/OFF", which realizes the one-button on-off function.

One-key on-off is a combination of Bootloader of Raspberry Pi and GPIO of 40PIN to realize on-off by software, which is different from the traditional power on-off or on-off by hardware.

One-button power on/off uses GPIO3 on the 40-pin socket. If you want to realize the one-button power on/off function, this pin should be configured as ordinary GPIO function, and can no longer be defined as SCL1 of I2C. Please remap I2C function to other pins.

NOTE:To realize the one-button on-off function, it is necessary to install the factory image or the BSP package provided by us.

2.5.2 DISP Slot

The back panel of ED-CM4IO Computer has two DISP slots, which are used to lead out the FPC cable, and the silk-screen is DISP1 and DISP0 respectively.

2.5.3 **CAM Slot**

There are two CAM slots on the back panel and side of ED-CM4IO Computer, which are used to lead out the FPC cable, and the silk-screen is CAM1 and CAM0 respectively.

3 Internal interface

3.1 RTC

The ED-CM4IO Computer is integrated with RTC. For the version sold in China, CR2032 button cell (RTC backup power supply) will be installed by default. In this way, the system can be guaranteed to have an uninterrupted and reliable clock, which is not affected by factors such as equipment power down. RTC clock chip is mounted on i2c-10 bus, and the device address is 0x51.

3.2 MIPI DSI

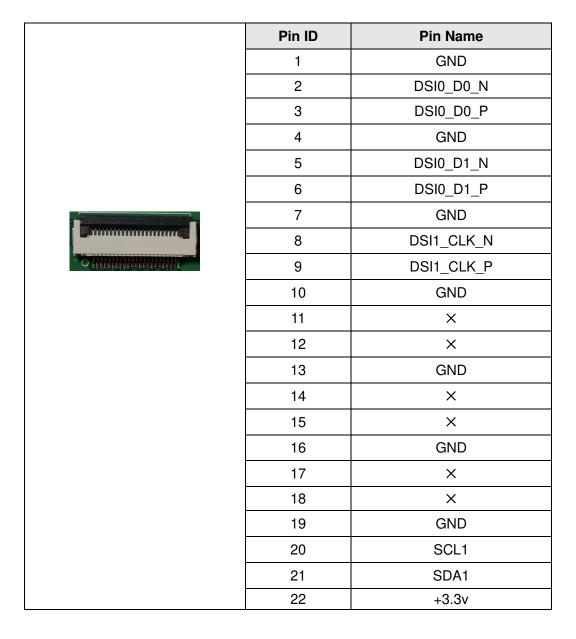
The ED-CM4IO Computer has two MIPI DSI interfaces, and the screen printing name is DISP1/DISP0, both of which use 22-pin connectors with a spacing of 0.5 mm. DISP0 has 2 channels, and DISP1 has 4



channels. When using DSI0 interface, it is necessary to install two jumper caps on J6 to connect the internal I2C bus to the connector.

3.2.1 DISP0

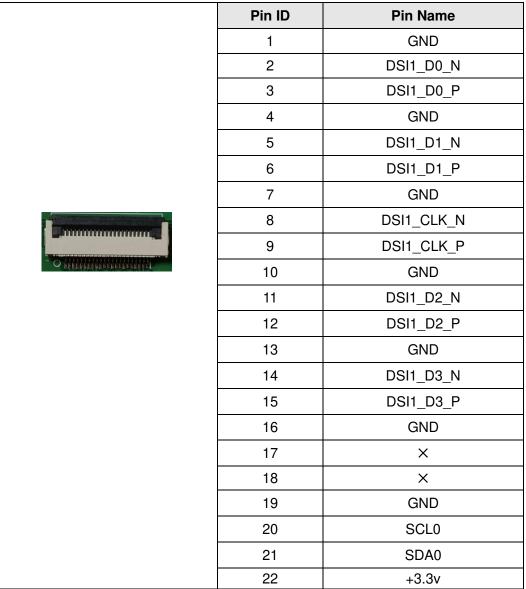
The corresponding definition of DISP0 pin is as follows.



3.2.2 DISP1

The corresponding definition of DISP1 pin is as follows.





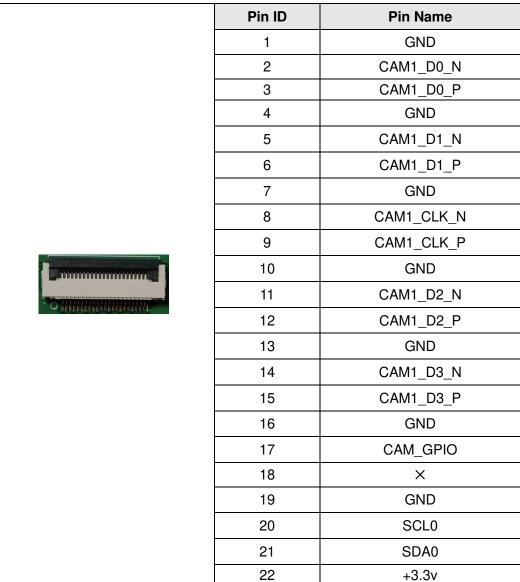
3.3 MIPI CSI

There are two CSI camera interfaces on ED-CM4IO Computer, and the screen printing marks are CAM0 and CAM1 respectively, both of which use 22-pin connectors with a spacing of 0.5 mm. CAM0 has 2 channels, and CAM1 has 4 channels. When using the CAM0 interface, it is necessary to install two jumper caps on J6 to connect the internal I2C bus to the connector.

3.3.1 CAM0

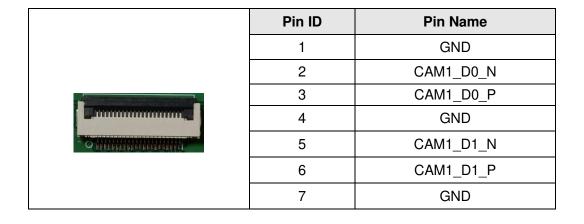
The corresponding definition of CAM0 pin is as follows.





3.3.2 CAM1

The corresponding definition of CAM1 pin is as follows.





8	CAM1_CLK_N
9	CAM1_CLK_P
10	GND
11	CAM1_D2_N
12	CAM1_D2_P
13	GND
14	CAM1_D3_N
15	CAM1_D3_P
16	GND
17	CAM_GPIO
18	X
19	GND
20	SCL0
21	SDA0
22	+3.3v

3.4 I2C-0 Connect Pin Header

J6 Pin Header of ED-CM4IO Computer is used to connect the ID_SC pin and ID_SD pin of CM4 with I2C bus pins of CAM0 and DSI0 interfaces. When using the CAM0 or DSI0 interface, it is necessary to install two jumper caps on J6 to connect the internal I2C bus to the CAM0 or DSI0 connector.

#	Signal	CM4 Pinout
1	SCL	ID_SC
2	SDA	ID_SD

3.5 CM4 Config Pin Header

J2 Pin Header of ED-CM4IO Computer is used for the startup configuration of CM4, and pins 1 and 2 are used to configure the prohibition of starting from eMMC. When writing eMMC of CM4, pins 1 and 2 of J2 need to be shorted to prohibit starting from eMMC and enable starting from USB rpiboot. Shorting pins 3 and 4 of J2 will prohibit writing to EEPROM on board CM4.

Pin	Function
1	GND
2	nRPIBOOT
3	GND



NOTE: No need to use other pins.

3.6 40-PIN Raspberry Pi HAT GPIO

ED-CM4IO Computer has a pin arrangement with a distance of 2X20P of 2.54mm, which leads out the GPIO of CM4.

Pin	Name	I/O	Function
1	3V3	0	
2	5V	I/O	
3	PIN3	I/O	I2C1_SDA
4	5V	I/O	
5	PIN5	I/O	I2C1_SCL
6	GND		
7	PIN7	I/O	GPIO4
8	PIN8	I/O	TXD0
9	GND		
10	PIN10	I/O	RXD0
11	PIN11	I/O	GPIO17
12	PIN12	I/O	GPIO18
13	PIN13	I/O	GPIO27
14	GND		
15	PIN15	I/O	GPIO22
16	PIN16	I/O	GPIO23
17	3V3	0	
18	PIN18	I/O	GPIO24



Pin	Name	I/O	Function
19	PIN19	I/O	GPIO10
20	GND		
21	PIN21	I/O	GPIO9
22	PIN22	I/O	GPIO25
23	PIN23	I/O	GPIO11
24	PIN24	I/O	GPIO8
25	GND		
26	PIN26	I/O	GPIO7
27	PIN27	I/O	ID_SD
28	PIN28	I/O	ID_SC
29	PIN29	I/O	GPIO5
30	GND		
31	PIN31	I/O	GPIO6
32	PIN32	I/O	GPIO12
33	PIN33	I/O	GPIO13
34	GND		
35	PIN35	I/O	GPIO19
36	PIN36	I/O	GPIO16
37	PIN37	I/O	GPIO26
38	PIN38	I/O	GPIO20
39	GND		
40	PIN40	I/O	GPIO21



3.7 PoE Pin Header

The Gigabit network port of ED-CM4IO Computer supports POE power supply, and the POE power supply terminal is led out through 1x2 2.54mm Pin Header, which needs to be equipped with official PoE HAT module to realize POE power supply to ED-CM4IO Computer.

Pin definition of POE Pin Header is as follows

Pin	Function
1	VC1-
2	VC1+
3	VC2-
4	VC2+

3.8 PCle

ED-CM4IO Computer has a standard PCIe Gen2 interface, supports 1 PCIe channel, and the speed is up to 5Gbps.



Pin ID	Pin Name	Pin ID	Pin Name
B1	+12V	A1	NC
B2	+12V	A2	+12V
В3	+12V	А3	+12V
B4	GND	A4	GND
B5	NC	A 5	+3.3V
В6	NC	A6	+3.3V
В7	GND	A7	NC
B8	+3.3V	A8	+3.3V
В9	+3.3V	A9	+3.3V
B10	+3.3V	A10	+3.3V
B11	NC	A11	PCIE_nRST
B12	PCIE_CLK_nREQ	A12	GND
B13	GND	A13	PCIE_CLK_P
B14	PCIE_TX_N	A14	PCIE_CLK_N



B15	PCIE_TX_P	A15	GND
B16	GND	A16	PCIE_RX_N
B17	NC	A17	PCIE_RX_P
B18	GND	A18	GND

3.9 USB 2.0 Pin Header

There are also two USB 2.0 Host on ED-CM4IO Computer, which are led out by 2x5 2.54mm Pin Header, and the interface is J14. Customers can expand USB Device devices according to their own applications.

USB Pin Header's pin defined as follows:

Pin	Function	Pin	Function
1	+5V	6	USB4_P
2	+5V	7	GND
3	USB3_N	8	GND
4	UBS4_N	9	NC
5	USB3_P	10	NC

3.10 External Power Output

J20 socket can provide +5V and +12V power, with one pin being +12V, four pins being +5V, and pins 2 and 3 being grounded.

J20 pin defined as follows:

Pin	Function	
1	+12V	
2	GND	
3	GND	
4	+5V	

3.11 Fan Control Pin Header

J17 socket of ED-CM4IO Computer is a fan control interface, which supports PWM output and fan speed control.

J17 pin defined is as follows:



Pin	Function	
1	GND	
2	+12V	
3	Speed input	
4	PWM output	

4 Wireless Communication

4.1 WiFi

ED-CM4IO Computer supports 2.4G/5G dual-band WiFi.

2.4G band

Parameter	Feature
frequency range	802.11b/g/n(HT20): 2412-2472MHz
	802.11n(HT40): 2422-2462MHz
modulation system	802.11b:DSSS
	802.11g/n:OFDM
Frequency Step	5M

5G band

Parameter	Feature
frequency range	802.11a/n/ac:
	5150-5350MHz
	5470-5725MHz
	5725-5850MHz
modulation system	BPSK
Frequency step	5M

4.2 Bluetooth

ED-CM4IO Computer supports Bluetooth 5.0.

Parameter	Feature
frequency range	2402-2480MHz
modulation system	GFSK,DPSK
Frequency step	2M

4.3 Antenna



4.3.1 WiFi / BT antenna

Parameter	Feature
Antenna type	External antenna
frequency band	2400-2500MHz, 5150-5850 MHz
antenna gain	2 dBi
impedance	50 OHM

5 Electrical Characteristics

5.1 Electrical parameters

Parameters	Minimum	Typical	Max	Unit
System power input	7.5	12	28	V
Working temperature	-20	25	85	°C
Storage temperature	-25	25	85	°C
Working environment humidity	20		90	%

6 FAQ

6.1 Can micro SD card be used for system startup?

The micro SD card slot is only used with CM4 Lite for primary file system storage. If you choose CM4 module with eMMC, this micro SD card slot is not available because it shares an MMC interface with eMMC.

7 Product Change Record

8 About us

8.1 About EDATEC



EDATEC, located in Shanghai, is one of Raspberry Pi's global design partners. Our vision is to provide hardware solutions for Internet of Things, industrial control, automation, green energy and artificial intelligence based on Raspberry Pi technology platform.

We provide standard hardware solutions, customized design and manufacturing services to speed up the development and time to market of electronic products.

8.2 Contact us

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