TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (L^2 - π -MOSIV)

2SJ304

DC-DC Converter, Relay Drive and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance $: RDS(ON) = 80 \text{ m}\Omega \text{ (typ.)}$

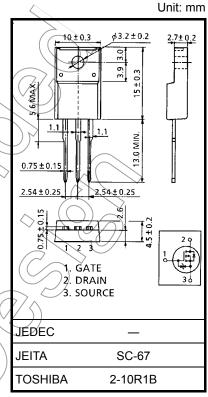
• High forward transfer admittance $|Y_{fs}| = 8.0 \text{ S (typ.)}$

• Low leakage current $: I_{DSS} = -100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = -60 \,\text{V})$

• Enhancement mode : $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-60	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	-60	> (
Gate-source voltage		V _{GSS} 〈	±20	X
Drain current	DC (Note 1)	ID	- 14	*
	Pulse(Note 1)	IDP	-56	
Drain power dissipation (Tc = 25°C)		PD	40	\ W
Channel temperature		(T _{ch}))	150	°C
Storage temperature ra	ange	Tstg	-55 to 150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th} (ch-c)	3.125	°C/W
Thermal resistance, channel to ambient	R _{th} (ch-a)	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device.

Please handle with caution.

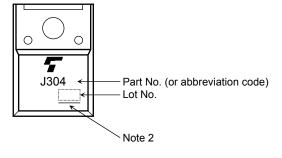
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	_	_	-100	μΑ
Drain-source br	reakdown	V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V	-60	-	_	V
Gate threshold	voltage	V_{th}	V _{DS} = -10 V, I _D = -1 mA	(20.8) /~	-2.0	V
Davis ON i-t	D= 0 (0) 11	V _{GS} = -4 V, I _D = -5 A	<u> </u>	130	190	mΩ	
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = -10 V, I _D = -7 A	$\bigcirc)$	80 120	LII77	
Forward transfe	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -7 A	5.0	8.0	_	S
Input capacitano	ce	C _{iss}		_	1200	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = −10 V, V _{GS} = 0 V, f = 1 MHz	_	220	_	pF
Output capacitance		Coss			550	\rightarrow	
Switching time	Rise time	t _r	V _{GS} OV ID TO VOUT		20	> _	
	Turn-on time	t _{on}	$-10V$ $R_{L}=$ 4.3Ω		> 30	_	ns
	Fall time	t _f	V _{DD} = -30W		25	-	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$	_	100	_	
Total gate charg plus gate-drain	je (Gate-source)	Q_{g} (_	45	_	
Gate-source charge		Qgs	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -14 \text{ A}$	_	30	_	nC
Gate-drain ("mi	ller") charge	Q _{gd}		_	15	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics Symbol Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	_	-	-14	Α
Pulse drain reverse current (Note 1)	-	-	-56	Α
Forward voltage (diode) V _{DSF} I _{DR} = -14 A, V _{GS} = 0 V		_	1.7	V
Reverse recovery time trr IDR = -14 A, VGS = 0 V	I	110	1	ns
Reverse recovery charge Q _{rr} di _{DR} / dt = 50 A / μs	_	0.18	_	μC

Marking

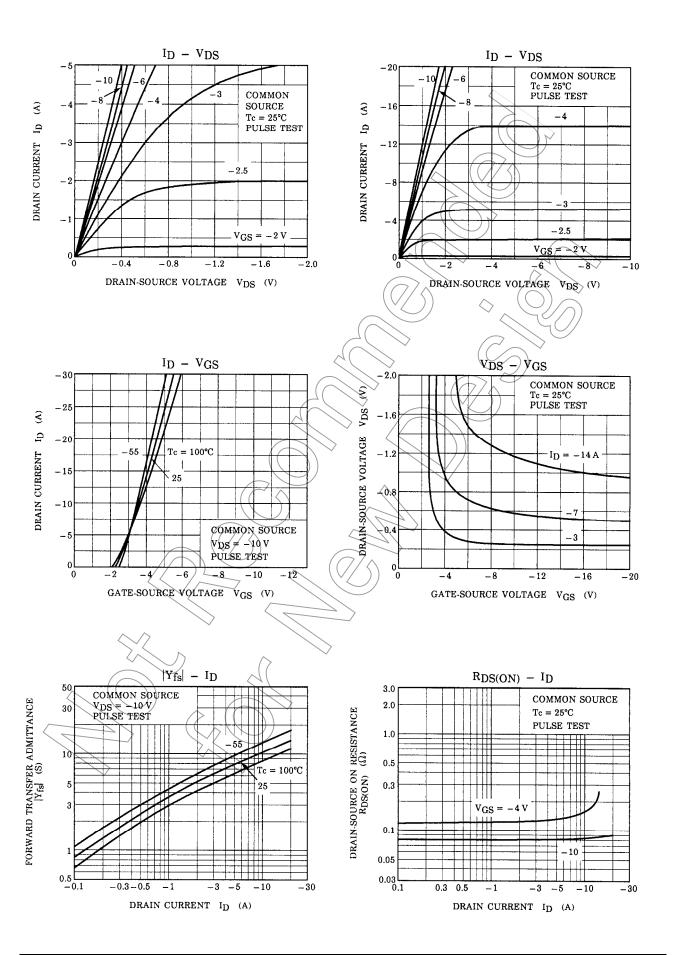


Note 2: A line under a Lot No. identifies the indication of product Labels.

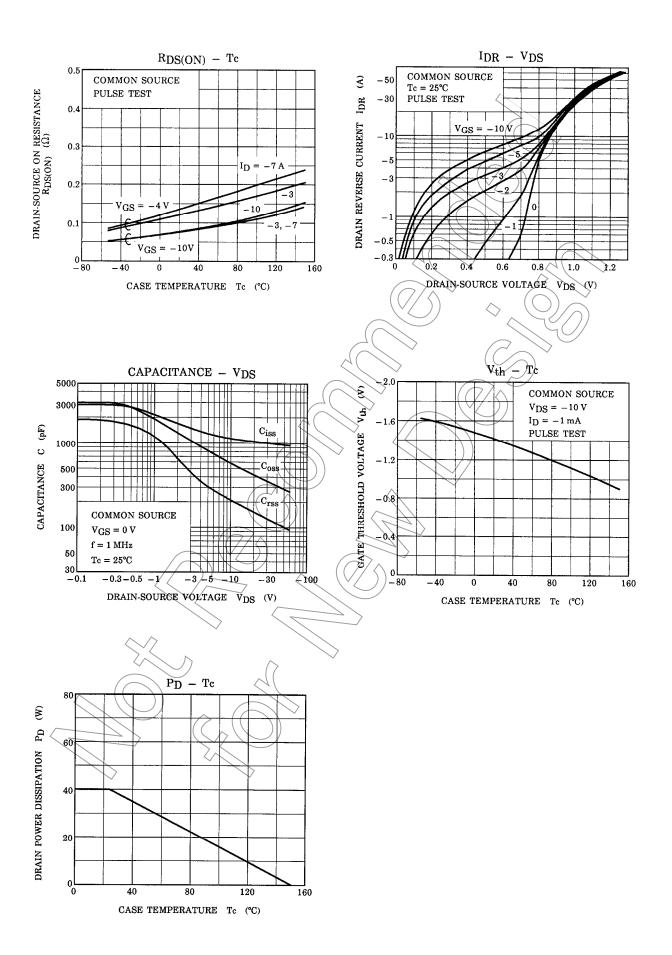
Not underlined: [[Pb]]/INCLUDES > MCV

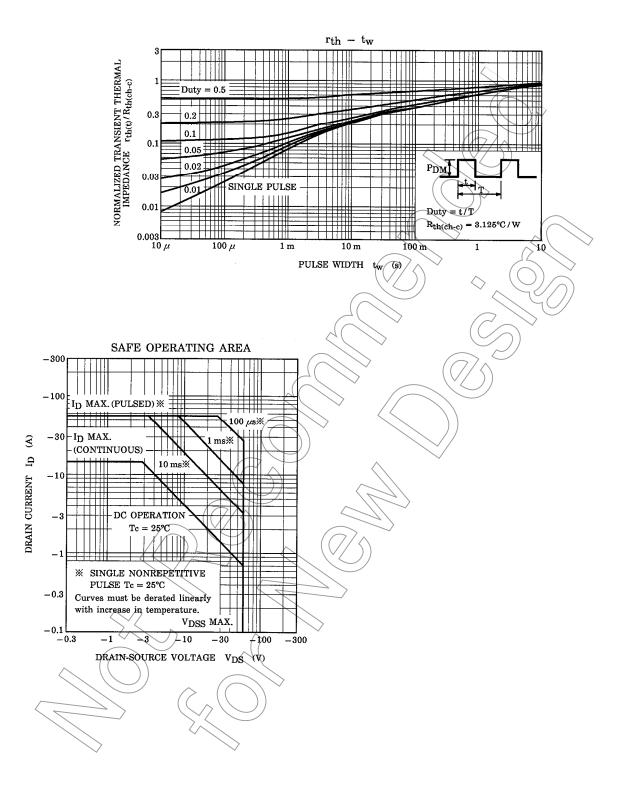
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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