



100V 175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BVDSS	R _{DS(ON)} Max	I _D Max T _C = +25°C	
100V	17.4mΩ @ V _{GS} = 10V	59A	
100 V	$30.3 \text{m}\Omega$ @ V _{GS} = 4.5V	45A	

Features and Benefits

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production— Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)}—Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH10H017LPDQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

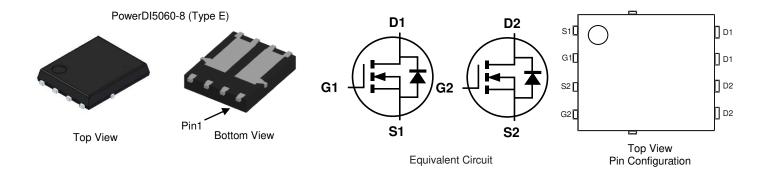
Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Synchronous Rectifier
- DC-DC Converters
- Primary Side Switching

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

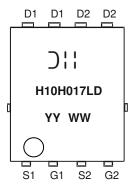
Part Number	Case	Packaging
DMTH10H017LPDQ-13	PowerDI5060-8 (Type E)	2500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



⊃¦¦= Manufacturer's Marking H10H017LD = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 19 = 2019) WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	100	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current, $V_{GS} = 10V$ (Note 7) $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$		l _D	59 42	Α
TA = +25°C Continuous Drain Current, VGS = 10V (Note 6) $T_A = +85°C$ $T_A = +100°C$		lσ	13 10 9	А
Maximum Body Diode Forward Current (Note 6)	ls	60	Α	
Pulsed Drain Current (10µs Pulse, T _C = +25°C, Package Limited)	I _{DM}	236	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Tc = +25°C, Package	Ism	236	Α	
Avalanche Current, L = 3mH (Note 8)	IAS	10	Α	
Avalanche Energy, L = 3mH (Note 8)	Eas	150	mJ	
Avalanche Current, L = 1mH (Note 8)	I _{AS}	10	Α	
Avalanche Energy, L = 1mH (Note 8)	Eas	50	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)		Rөja	100	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 6)		Rөja	56	°C/W
Total Power Dissipation	T _C = +25°C	P_{D}	93	W
Thermal Resistance, Junction to Case (Note 7)	<u>.</u>	Rejc	1.6	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Thermal resistance from junction to solder point (on the exposed drain pin).
- 8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25$ °C.



Electrical Characteristics (@Tc = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	•			•			
Drain-Source Breakdown Voltage	BVDSS	100	_	_	V	V _{GS} = 0V, I _D = 1mA	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 80V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	13.7	17.4	mΩ	V _{GS} = 10V, I _D = 17A	
Static Diain-Source On-Nesistance	R _{DS(ON)}	_	23.8	30.3	11122	$V_{GS} = 4.5V, I_D = 10A$	
Diode Forward Voltage	V _{SD}	_	0.8	1.3	V	V _G S = 0V, I _S = 17A	
DYNAMIC CHARACTERISTICS (Note 10)	•		•		•		
Input Capacitance	Ciss	_	1986	_		V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	333	_	pF		
Reverse Transfer Capacitance	Crss	_	20	_		1 – 1101112	
Gate Resistance	Rg	_	1.17	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (VGS = 4.5V)	Qg	_	14.4	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	28.6	_	nC	V _{DS} = 50V, I _D = 20A	
Gate-Source Charge	Qgs	_	5.2	_	110		
Gate-Drain Charge	Qgd	_	8.2	<u> </u>			
Turn-On Delay Time	t _{D(ON)}	_	9.8	<u> </u>		V _{DD} = 50V, V _{GS} = 10V,	
Turn-On Rise Time	t _R	_	16.3	<u> </u>			
Turn-Off Delay Time	tD(OFF)		32.6	_	ns	$R_G = 11\Omega, I_D = 20A$	
Turn-Off Fall Time	tF	_	21.6	_			
Body Diode Reverse Recovery Time	t _{RR}		40.6	_	ns	I _F = 17A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Qrr		58.1	_	nC	I _F = 17A, di/dt = 100A/μs	

Notes:

^{9.} Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing.



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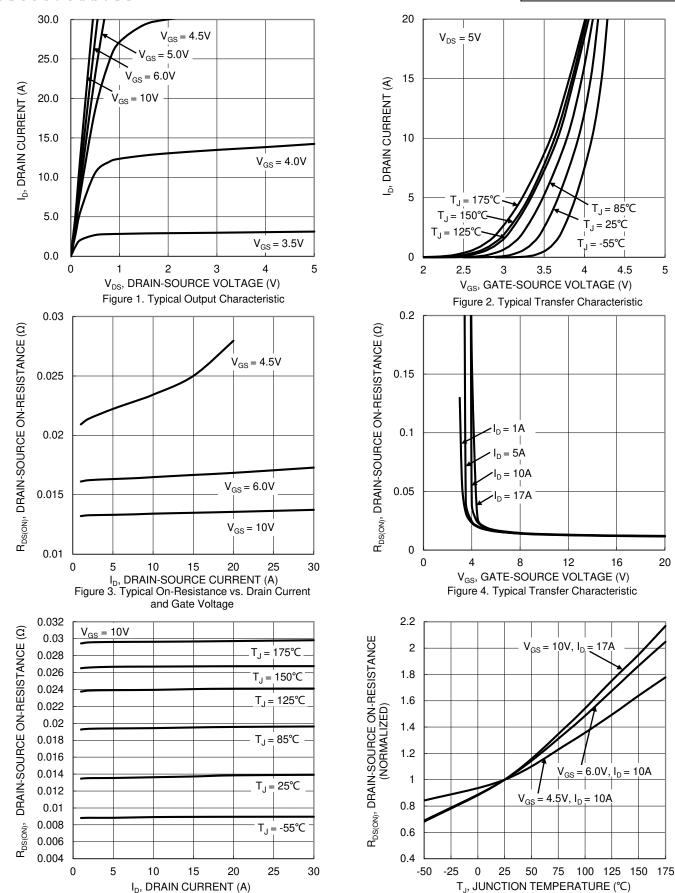


Figure 5. Typical On-Resistance vs. Drain Current

and Temperature

Figure 6. On-Resistance Variation with Temperature





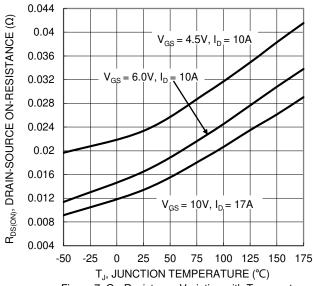


Figure 7. On-Resistance Variation with Temperature

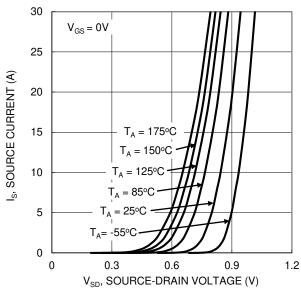


Figure 9. Diode Forward Voltage vs. Current 10 8 6 $V_{GS}(V)$ 4 $V_{DS} = 50V, I_{D} = 20A$ 2 0 25 0 5 10 15 20 30 Q_q (nC)

Figure 11. Gate Charge

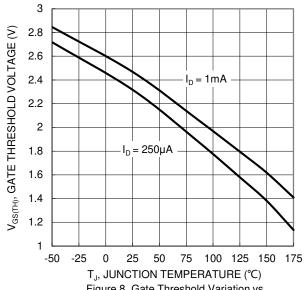


Figure 8. Gate Threshold Variation vs. JunctionTemperature

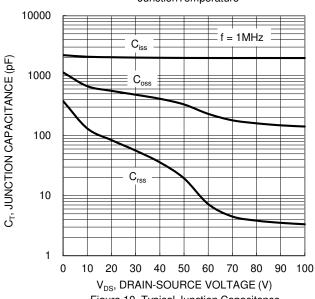


Figure 10. Typical Junction Capacitance

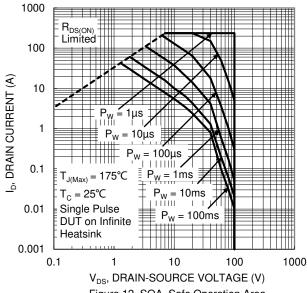


Figure 12. SOA, Safe Operation Area



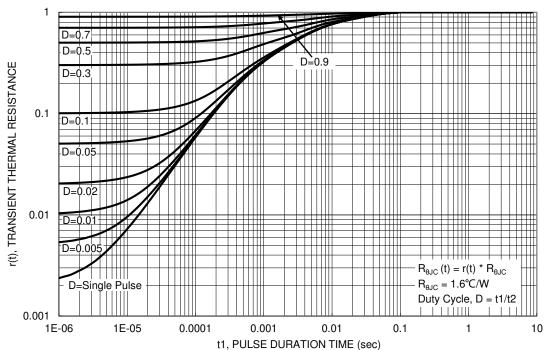


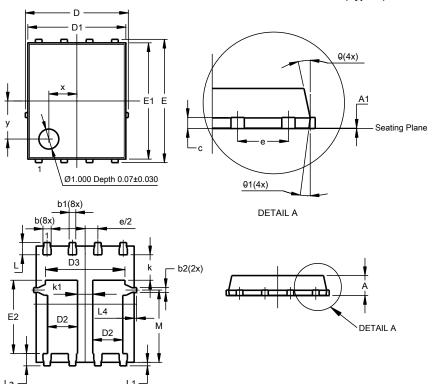
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type E)

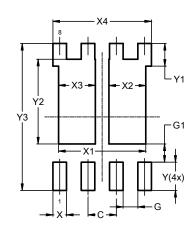


PowerDI5060-8 (Type E)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A 1	0	0.05	0.02		
b	0.33	0.51	0.41		
b1	0.300	0.366	0.333		
b2	0.20	0.35	0.25		
С	0.23	0.33	0.277		
D	5	.15 BS0	\sim		
D1	4.85	4.95	4.90		
D2	1.40	1.60	1.50		
D3			3.98		
Е		.15 BS0			
E1	5.75	5.85	5.80		
E2	3.56	3.76	3.66		
е	1	.27BSC			
k		_	1.27		
k1	0.56	_	_		
L	0.51	0.71	0.61		
La	0.51	0.71	0.61		
L1	0.05	0.20	0.175		
L4	_	_	0.125		
M	3.50	3.71	3.605		
X		_	1.400		
у	_	_	1.900		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type E)



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	3.910		
X2	1.650		
Х3	1.650		
X4	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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