



# PMEG4010CEH-Q

40 V, 1 A very low VF Schottky barrier rectifier

26 September 2022

Product data sheet

## 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection encapsulated in a small SOD123F Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Forward current:  $I_F \leq 1$  A
- Reverse voltage:  $V_R \leq 40$  V
- Very low forward voltage
- Small and flat lead SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications



## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_F$	forward current	$T_{sp} \leq 55$ °C	-	-	1	A
$V_R$	reverse voltage		-	-	40	V
$V_F$	forward voltage	$I_F = 1$ A; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.02$ ; pulsed; $T_{amb} = 25$ °C	-	490	570	mV
$I_R$	reverse current	$V_R = 40$ V; $T_{amb} = 25$ °C	-	6	50	$\mu$ A

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 SOD123F	 sym001
2	A	anode		

[1] The marking bar indicates the cathode.

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PMEG4010CEH-Q</a>	SOD123F	plastic, surface-mounted package; 2 leads; 2.6 mm x 1.6 mm x 1.1 mm body	<a href="#">SOD123F</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010CEH-Q	C9

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage			-	40	V
$I_F$	forward current	$T_{sp} \leq 55\text{ °C}$		-	1	A
$I_{FRM}$	repetitive peak forward current	$t_p \leq 1\text{ ms}$ ; $\delta \leq 0.25$		-	7	A
$I_{FSM}$	non-repetitive peak forward current	$t_p = 8\text{ ms}$ ; square wave; $T_{j(\text{init})} = 25\text{ °C}$		-	9	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	375	mW
			[2]	-	830	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-65	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	330	K/W
			[1] [3]	-	-	150	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	60	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

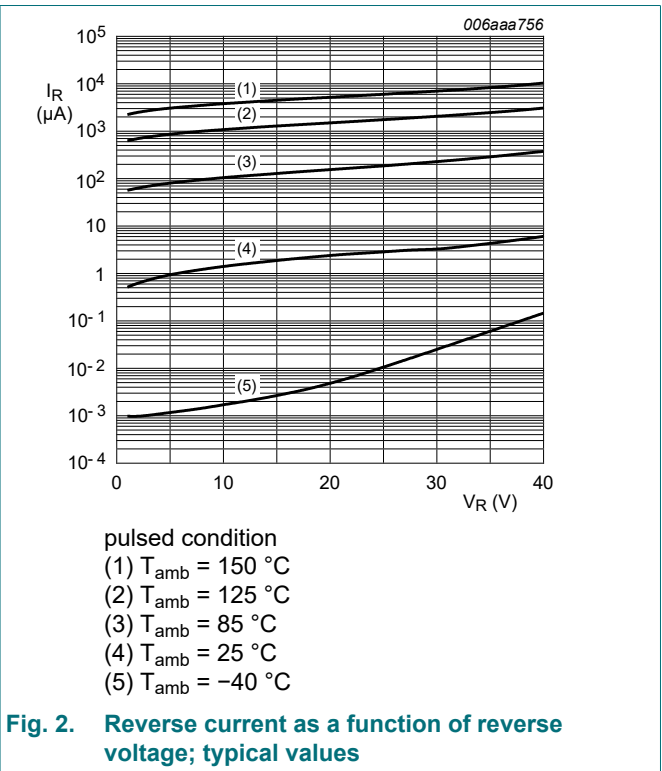
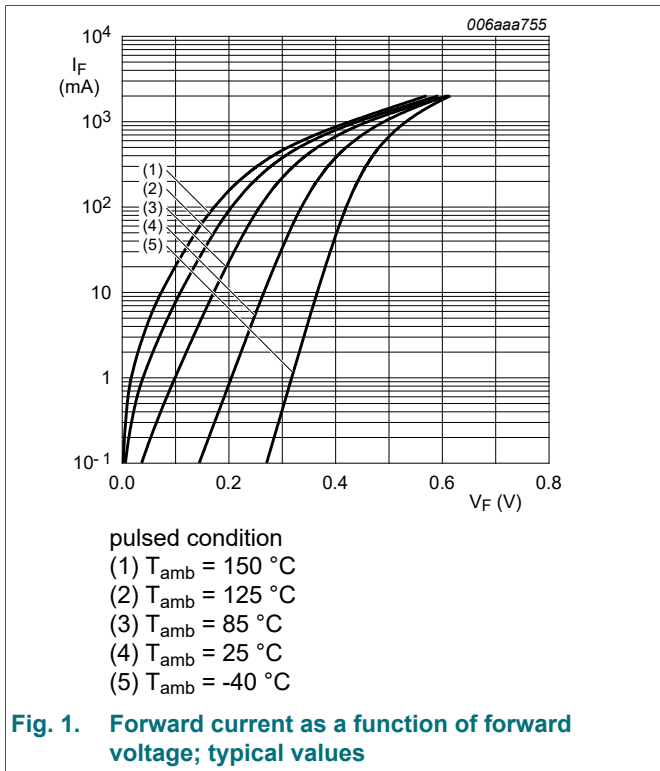
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

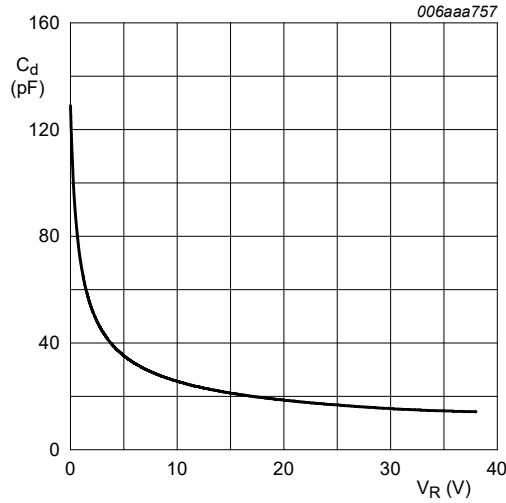
[4] Soldering point of cathode tab.

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	210	240	mV
		I <sub>F</sub> = 10 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	270	310	mV
		I <sub>F</sub> = 100 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	340	390	mV
		I <sub>F</sub> = 500 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	420	490	mV
		I <sub>F</sub> = 700 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	450	520	mV
		I <sub>F</sub> = 1 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; pulsed; T <sub>amb</sub> = 25 °C	-	490	570	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	0.8	-	μA
		V <sub>R</sub> = 10 V; T <sub>amb</sub> = 25 °C	-	1.1	-	μA
		V <sub>R</sub> = 40 V; T <sub>amb</sub> = 25 °C	-	6	50	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	69	77	pF

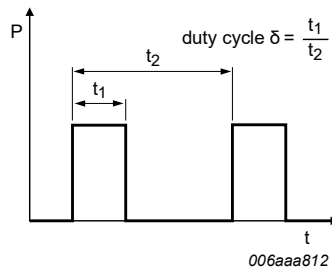




$f = 1 \text{ MHz}; T_{\text{amb}} = 25 \text{ }^\circ\text{C}$

**Fig. 3. Diode capacitance as a function of reverse voltage; typical values**

## 11. Test information



**Fig. 4. Duty cycle definition**

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

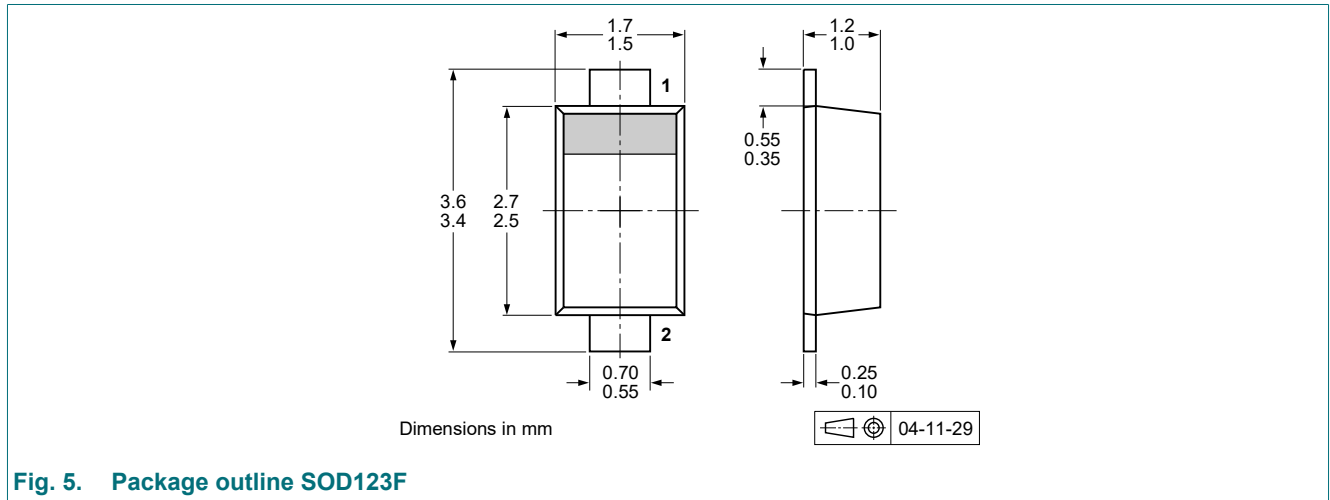


Fig. 5. Package outline SOD123F

## 13. Soldering

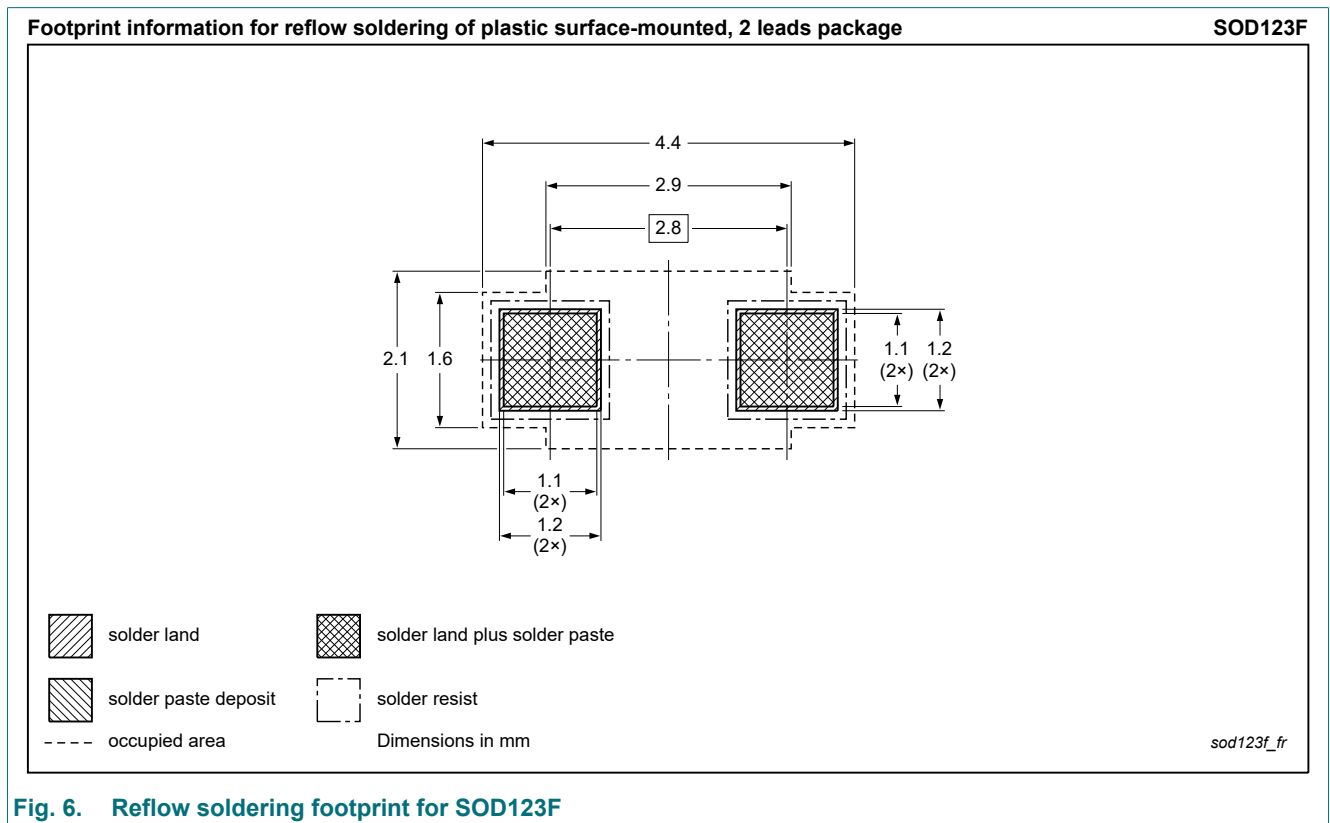


Fig. 6. Reflow soldering footprint for SOD123F

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4010CEH-Q v.1	20220926	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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## Contents

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1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	1
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	2
9. Thermal characteristics.....	2
10. Characteristics.....	3
11. Test information.....	4
12. Package outline.....	5
13. Soldering.....	5
14. Revision history.....	6
15. Legal information.....	7

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