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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK3229

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1095-0200 (Previous: ADE-208-766)

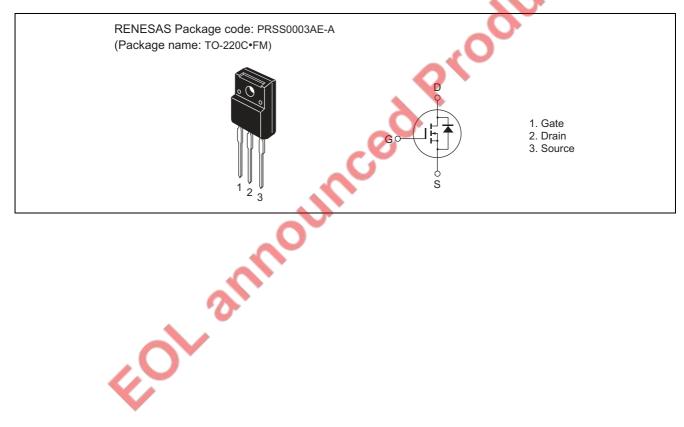
Rev.2.00

Sep 07, 2005

Features

- Low on-resistance $R_{DS (on)} = 6 \text{ m}\Omega \text{ typ.}$
- Low drive current
- 4 V gate drive device can be driven from 5 V source

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	80	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	60	Α
Drain peak current	I _{D (pulse)} Note 1	240	A
Body-drain diode reverse drain current	I _{DR}	60	A
Avalanche current	I _{AP} Note 3	50	A
Avalanche energy	E _{AR} Note 3	181	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \infty$, duty cycle $\le 1\%$

2. Value at Tc = 25°C

3. Value at Tch \leq 25°C, Rg \geq 50 Ω

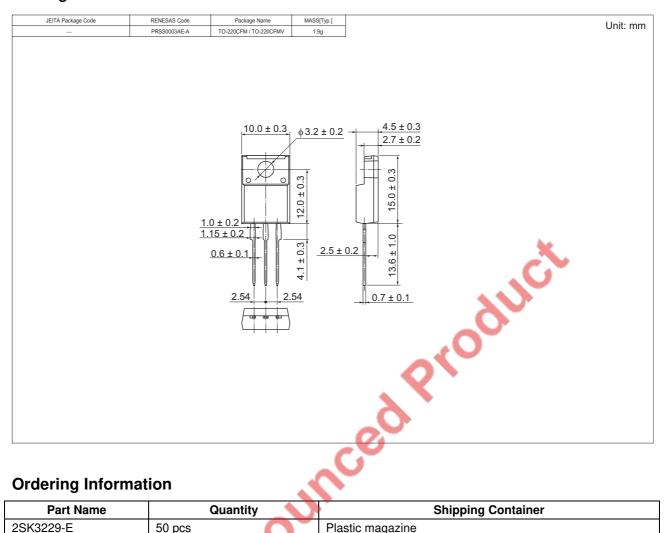
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	80	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I_{GSS}	_	_	±0.1	∝A	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	_	1	10	≪A	$V_{DS}=80\ V,\ V_{GS}=0$
Gate to source cutoff voltage	V _{GS (off)}	1.0	-	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS (on)}	_	6.0	7.5	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
	R _{DS (on)}	1	8.0	12	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	50	85	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	9700	_	рF	$I_D = 10 \text{ V}$
Output capacitance	Coss	_	1250	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	290	_	рF	f = 1 MHz
Total gate charge	Qg	_	150	_	nC	$V_{DD} = 25 \text{ V}$
Gate to source charge	Qgs	_	30	_	nC	$V_{GS} = 25 \text{ V}$
Gate to drain charge	Qgd	_	30	_	nC	$I_D = 60 \text{ A}$
Turn-on delay time	t _{d (on)}	_	80	_	ns	$I_D = 30 \text{ A}$
Rise time	t _r	_	280	_	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	t _{d (off)}	_	780	_	ns	$R_L = 1 \Omega$
Fall time	t _f	_	340	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_F = 60 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	_	80	_	ns	$I_F = 60 \text{ A}, V_{GS} = 0$
Ť						di _F /dt = 50 A/∝s

Note: 4. Pulse test

Package Dimensions



Ordering Information

Part Name	Quantity		Shipping Container
2SK3229-E	50 pcs	1	Plastic magazine

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