

# 800MHz to 2.7GHz RF Measuring Receiver

#### **FEATURES**

RF Frequency Range: 800MHz to 2.7GHz

■ Ultra Wide Dynamic Range: 75dB at 900MHz

Wide Power Supply Range: 2.7V to 5.25V

Low Supply Current: 14.7mA at 3V

8-Lead MS0P Package

#### **APPLICATIONS**

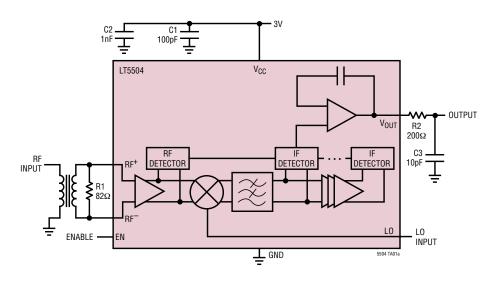
- RSSI Measurements
- Receive AGC
- Transmit Power Control
- ASK and Envelope Demodulation
- GSM/TDMA/CDMA/WCDMA

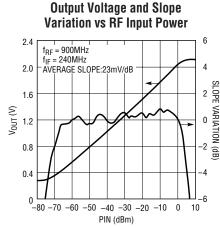
#### DESCRIPTION

The LT®5504 is an 800MHz to 2700MHz monolithic integrated measuring receiver, capable of detecting a wide dynamic range RF signal from -75dBm to +5dBm. The logarithm of the RF signal is precisely converted into a linear DC voltage. The LT5504 consists of RF/IF limiters, an LO buffer amplifier, a limiting mixer, a 3rd-order 450MHz integrated low pass filter, RF/IF detectors and an output interface. The ultrawide dynamic range is achieved by simultaneously measuring the RF signal and a down-converted IF signal obtained using the on-chip mixer and an external local oscillator. The RF- and IF-detected signals are summed to generate an accurate linear DC voltage proportional to the input RF voltage (or power) in dB. The output is buffered with a low output impedance driver.

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#### TYPICAL APPLICATION





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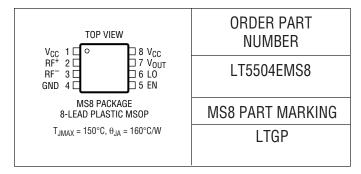
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## **ABSOLUTE MAXIMUM RATINGS**

#### (Note 1)

Power Supply Voltage	5.5V
V <sub>OUT</sub> , EN	0,V <sub>CC</sub>
LO Input Power	6dBm
RF Input Power Differential (50Ω, 5.5V)	. 24dBm
RF Input Power Single-Ended ( $50\Omega$ , 5.5V)	. 18dBm
Operating Ambient Temperature40°C	to 85°C
Storage Temperature Range65°C to	to 150°C
Lead Temperature (Soldering, 10 sec)	300°C

### PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

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SYMBOL	PARAMETER	CONDITIONS	MIN TYP MAX	UNITS
RF Input		·	·	
f <sub>RF</sub>	Frequency Range		800 to 2700	
	Input Impedance	(Note 6)		
	DC Voltage	Internally Biased	1.7	V
LO Input		·		
f <sub>L0</sub>	Frequency Range		850 to 3100	MHz
	Input Return Loss	Internally Matched to 50Ω	14	dB
	DC Voltage	Internally Biased	0.82	V
$\overline{P_{L0}}$	LO Power		−16 to −8	dBm
	LO to RF Leakage	900MHz 1.9GHz 2.5GHz	-50 -45 -40	dBc dBc dBc
IF Frequency				
f <sub>IF</sub>	Frequency		50 to 450	MHz
Output Voltag	je at f <sub>RF</sub> = 900MHz, f <sub>LO</sub> = 1140MHz	·		
	Linear Dynamic Range (Note 4)		66 75	dB
	Output Voltage	Input = -70dBm Input = -20dBm Input = 0dBm	0.4 1.6 2.1	V V V
	Average Slope	Input from -50dBm to -20dBm	16 23	mV/dB
Output Voltag	je at f <sub>RF</sub> = 1900MHz, f <sub>LO</sub> = 2140MHz			
	Linear Dynamic Range (Note 4)		60 72	dB
	Output Voltage	Input = -70dBm Input = -20dBm Input = 0dBm	0.35 1.52 1.9	V V V
	Average Slope	Input from -50dBm to -20dBm	16 23	mV/dB

# **ELECTRICAL CHARACTERISTICS** $T_A = 25^{\circ}C$ . $V_{CC} = 3V$ , $P_{L0} = -10 dBm$ , unless otherwise noted. (Notes 2, 3)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Output Voltag	e at f <sub>RF</sub> = 2500MHz, f <sub>LO</sub> = 2260MHz		'			
	Linear Dynamic Range (Note 4)		58	70		dB
	Output Voltage	Input = -70dBm Input = -20dBm Input = 0dBm		0.3 1.45 1.8		V V
	Average Slope	Input from -50dBm to -20dBm	16	23		mV/dB
Output Interfa	ce	·	'			
	Current Drive Capability			400		μΑ
	Output Noise Spectral Density	At 100KHz At 10MHz		3.9 0.32		μV/√Hz μV/√Hz
	Output Response Time (Note 5)	RF Input Pin from No Signal to OdBm	200			ns
Power Up/Dov	wn					
t <sub>ON</sub> Turn ON Tin	Turn ON Time (Note 5)			400		ns
	Turn OFF Time (Note 5)			4		μS
	Input Resistance			30		kΩ
	Enable Turn ON Voltage (Note 7)		0.6 • V <sub>CC</sub>			V
	Disable Turn OFF Voltage (Note 7)		0.4 • V <sub>CC</sub>		V	
Power Supply	,		•			
$\overline{V_{CC}}$	Supply Voltage		2.7		5.25	V
I <sub>CC</sub>	Supply Current			14.7	22	mA
	Shutdown Current				30	μА

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: Tests are performed as shown in the configuration of Figure 5.

**Note 3:** Specifications over the  $-40^{\circ}$ C to  $85^{\circ}$ C temperature range are guaranteed by design, characterization and correlation with statistical process controls.

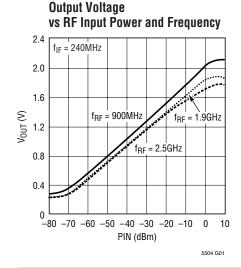
**Note 4:** The Linear Dynamic Range is defined as the range over which the output slope is at least 50% of the average slope from -50dBm to -20dBm.

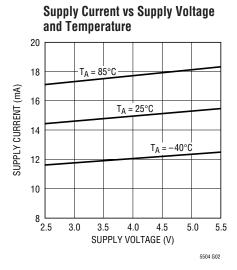
Note 5: The output voltage is settled to the full specification within 1dB.

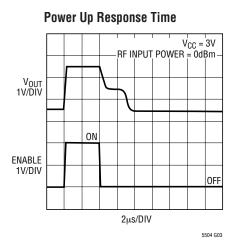
Note 6: Refer to Figure 1 and Applications Information.

Note 7: Refer to Pin Functions description.

### TYPICAL PERFORMANCE CHARACTERISTICS







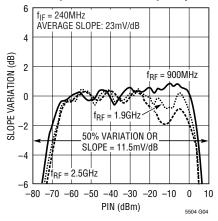
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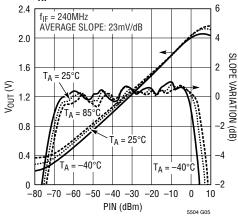
#### TYPICAL PERFORMANCE CHARACTERISTICS

 $(V_{CC} = 3V \text{ unless otherwise noted}).$ 

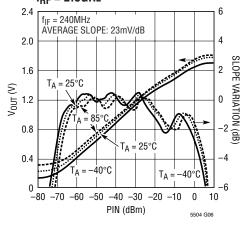
Output Slope Variation vs RF Input Power and Frequency



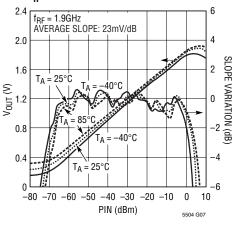
Output Voltage and Slope Variation vs RF Input Power and Temperature,  $f_{RF} = 900 \mbox{MHz}$ 



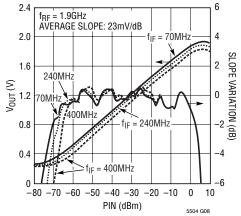
Output Voltage and Slope Variation vs RF Input Power and Temperature,  $f_{RF} = 2.5 \mbox{GHz}$ 



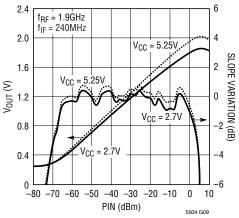
Output Voltage and Slope Variation vs RF Input Power and Temperature,  $f_{\text{IF}} = 240 \text{MHz}$ 



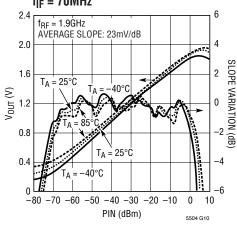
Output Voltage and Slope Variation vs RF Input Power and IF Frequency



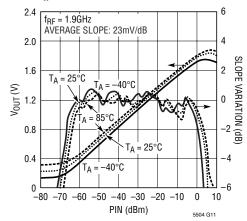
Output Voltage and Slope Variation vs RF Input Power and Supply Voltage



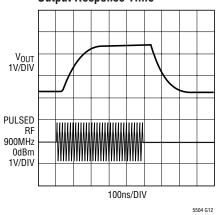
Output Voltage and Slope Variation vs RF Input Power and Temperature,  $f_{\text{IF}} = 70 \text{MHz}$ 



Output Voltage and Slope Variation vs RF Input Power and Temperature,  $f_{\text{IF}} = 400 \text{MHz}$ 



**Output Response Time** 



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#### PIN FUNCTIONS

**V<sub>CC</sub>** (**Pins 1, 8**): Power Supply Pins. These pins must be tied together at the part as close as possible, and should be decoupled using 1000pF capacitors.

RF+ (Pin 2): Positive RF Input Pin.

**RF**<sup>-</sup> (**Pin 3**): Negative RF Input Pin.

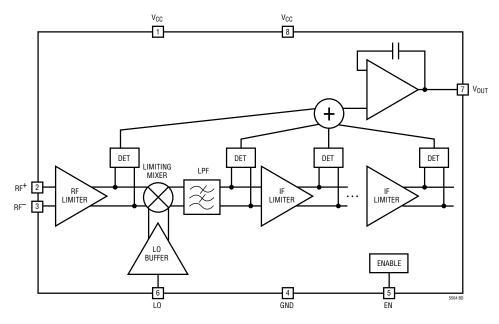
GND (Pin 4): Ground Pin.

**EN (Pin 5):** Enable Pin. The on/off threshold voltage is about  $V_{CC}/2$ . When the input voltage is higher than  $0.6 \cdot V_{CC}$ , the circuit is completely turned on. When the input voltage is less than  $0.4 \cdot V_{CC}$ , the circuit is turned off.

LO (Pin 6): Local Oscillator Input Pin.

Vout (Pin 7): Output Pin.

### **BLOCK DIAGRAM**



### APPLICATIONS INFORMATION

The LT5504 consists of the following sections: RF/IF limiters, limiting mixer, RF/IF detectors, LO buffer amplifier, 3rd-order integrated low pass filter (LPF), output interface and bias circuitry.

An RF signal ranging from 800MHz to 2.7GHz is detected by the RF and IF detectors using a proprietary technique. The down-converted IF signal is band limited by the onchip LPF, reducing broadband noise, and thus an ultrawide dynamic range signal can be measured. The RF measuring receiver is essentially a logarithmic voltage detector. The measured output voltage is directly proportional to the RF signal voltage. An internal temperature compensation circuit results in a highly temperature-stable output voltage.

#### **RF** Limiter

The differential input impedance of the RF limiter is shown in Figure 1. A 1:1 input transformer can be used to achieve  $50\Omega$  broadband matching with an  $82\Omega$  shunt resistor (R1) at the inputs as shown in Figure 5.

The 1:1 RF input transformer can also be replaced with a narrow band single-ended-to-differential conversion circuit using three discrete elements as shown in Figure 2. Their nominal values are listed in Table 1. Due to the parasitics of the PCB, these values may require adjustment.



#### APPLICATIONS INFORMATION

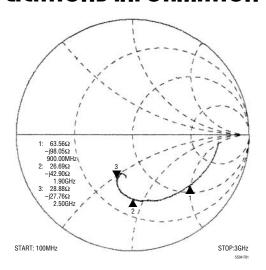


Figure 1. Differential RF Input Impedance

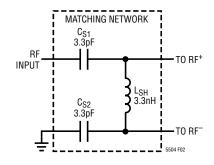


Figure 2. RF Input Matching Network at 1900MHz

Figure 3 shows the output voltage vs RF input power response for these two input terminations. The voltage gain of the single-ended-to-differential conversion circuit is:

$$GAIN = 20 \bullet LOG \sqrt{\frac{R_{IN}}{50}} = 3dB,$$

where  $R_{\text{IN}}$  = 100 $\Omega$  is the narrow band input impedance.

Thus, the output voltage curve in this case is shifted to the left by about 3dB.

Table 1. The Component Values of Matching Network  $L_{SH},\,C_{S1}$  and  $C_{S2}$ 

f <sub>IF</sub> (MHz)	L <sub>SH</sub> (nH)	$C_{S1}/C_{S2}(pF)$
900	12.0	3.9
1900	3.3	3.3
2500	2.7	2.2
2700	2.4	1.5

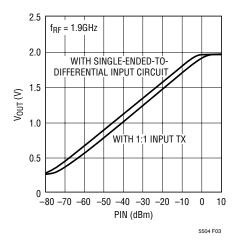


Figure 3. The Output Voltage vs RF Input Power

#### **Limiting Mixer and LPF**

The amplified RF signal is down-converted using the limiting mixer and LO signal. The resulting signal is filtered by the 3rd-order, 450MHz, integrated low pass filter (LPF). Only the desired IF signal is passed to the IF limiters for further detection. Any other mixing products, including LO feedthrough, are much reduced to maximize sensitivity. The receiver's sensitivity is thus defined by the LPF bandwidth.

#### **IF Limiter**

The IF signal is then amplified through the multiple limiter stages for further signal detection. All DC offsets, including LO signal self-mixing, are eliminated by an internal DC offset cancellation circuit. Nevertheless, care should be taken in component placement and in PCB layout to minimize LO coupling to the RF port.

#### **Output Interface**

The output interface of the LT5504 is shown in Figure 4. The output currents from the RF and IF detectors are summed and converted into an output voltage,  $V_{OUT}$ . The maximum charging current available to the output load is about  $400\mu A$ . An internal compensation capacitor  $C_C$  is used to guarantee stable operation for a large capacitive output load. The slew rate is  $80V/\mu s$  and the small signal output bandwidth is approximately 5MHz when the output is resistively terminated. When the output is loaded with a large capacitor  $C_I$ , the slew rate is limited

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#### APPLICATIONS INFORMATION

to  $400\mu A/C_L$ . For example, the slew rate is reduced to  $4V/\mu S$  when  $C_L = 100 pF$ .

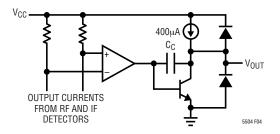


Figure 4. Simplified Circuit Schematic of the Output Interface

#### **Applications**

The LT5504 can be used as a self-standing signal strength-measuring receiver (RSSI) for a wide range of input signals from -75dBm to +5dBm, for frequencies from 800MHz to 2.7GHz.

The LT5504 can be used as a demodulator for AM and ASK modulated signals with data rates up to 5MHz. Depending on specific application needs, the RSSI output can be split into two branches, providing AC coupled data output, and DC coupled, RSSI output for signal strength measurements and AGC. Refer to Figure 5.

The LT5504 can also be used as a wide range RF power detector for transmit power control.

### TYPICAL APPLICATIONS

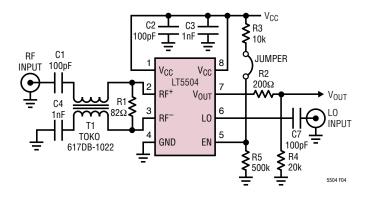


Figure 5. LT5504 Evaluation Board Circuit Schematic

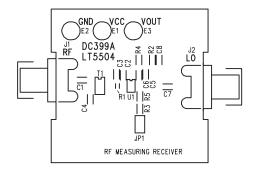


Figure 6.Component Side Silkscreen of Evaluation Board

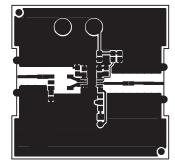


Figure 7. Component Side Layout of Evaluation Board



Figure 8.Bottom Side Silkscreen of Evaluation Board

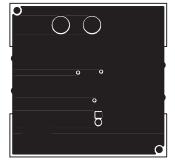
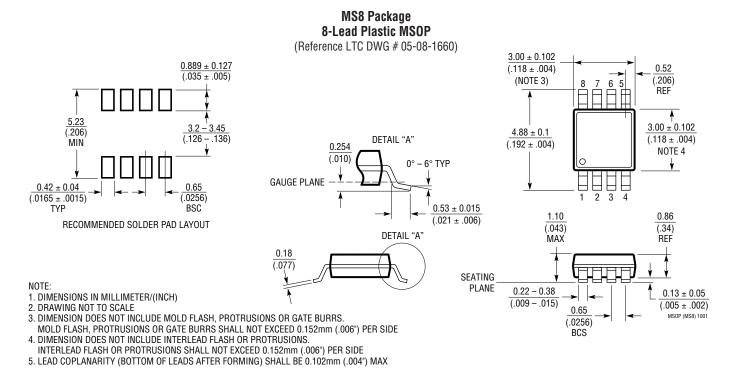


Figure 9. Bottom Side Layout of Evaluation Board



#### PACKAGE DESCRIPTION



### **RELATED PARTS**

PART NUMBER	DESCRIPTION	COMMENTS
LT5500	Receiver Front End	Dual LNA Gain Settling 13.5dB/ $-$ 14dB at 2.5GHz, Double Balanced Mixer, 1.8V $\leq$ V <sub>SUPPLY</sub> $\leq$ 5.25V
LT5502	400MHz Quadrature Demodulator with RSSI	1.8V to 5.25V Supply, 70MHz to 400MHz IF, 84dB Limiting Gain, 90dB RSSI Range
LT5503	1.2GHz to 2.7GHz Direct IQ Modulator and Upconverting Mixer	1.8V to 5.25V Supply, Four-Step RF Power Control, 120MHz Modulation Bandwidth
LTC5505	300MHz to 3.5GHz RF Power Detector	>40dB Dynamic Range, Temperature Compensated, 2.7V to 6V Supply
LT5506	500MHz Quadrature IF Demodulator with VGA	1.8V to 5.25V Supply, 40MHz to 500MHz IF, -4dB to 57dB Linear Power Gain
LTC5507	100kHz to 1GHz RF Power Detector	48dB Dynamic Range, Temperature Compensated, 2.7V to 6V Supply
LTC5508	300MHz to 7GHz RF Power Detector	44dB Dynamic Range, Temperature Compensated, SC70 Package
LTC5509	300MHz to 3GHz RF Power Detector	36dB Dynamic Range, SC70 Package
LT5511	High Signal Level Upconverting Mixer	RF Output to 3GHz, 17dBm IIP3, Integrated LO Buffer
LT5512	High Signal Level Downconverting Mixer	DC-3GHz, 20dBm IIP3, Integrated LO Buffer
LT5515	1.5GHz to 2.5GHz Direct Conversion Demodulator	20dBm IIP3, Integrated LO Quadrature Generator
LT5516	0.8GHz to 1.5GHz Direct Conversion Quadrature Demodulator	21.5dBm IIP3, Integrated LO Quadrature Generator
LT5522	600MHz to 2.7GHz High Signal Level Mixer	25dBm IIP3 at 900MHz, 21.5dBm IIP3 at 1.9GHz, Matched $50\Omega$ RF and LO Ports, Integrated LO Buffer
LTC5532	300MHz to 7GHz Precision RF Power Detector	Precision V <sub>OUT</sub> Offset Control, Adjustable Gain and Offset Voltage