2SC5592

Silicon NPN epitaxial planar type

For DC-DC converter

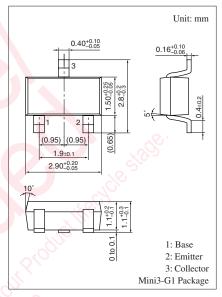
For various driver circuits

Features

- \bullet Low collector-emitter saturation voltage $V_{CE(sat)}$
- High-speed switching
- Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$

	- a			
Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	15	V	
Collector-emitter voltage (Base open)	V _{CEO}	15	v	
Emitter-base voltage (Collector open)	V _{EBO}	5	v	
Collector current	I _C	2.5	А	
Peak collector current	I _{CP}	10	А	
Collector power dissipation *	P _C	600	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	
			5	



Marking Symbol: 2T

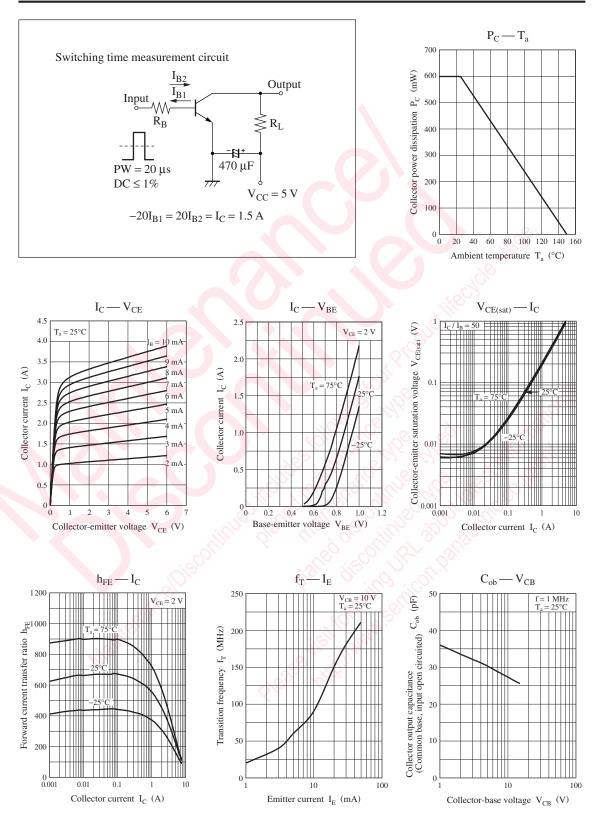
Note) *: Measure on the ceramic substrate at 15 mm × 15 mm × 0.6 mm

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu A, I_{\rm E} = 0$	15	5		V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	15			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu A, I_{\rm C} = 0$	•5			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 10 \text{ V}, I_E = 0$			0.1	μΑ
Forward current transfer ratio *	h _{FE1}	$V_{CE} = 2 V, I_C = 100 mA$	400		1 0 0 0	_
	h _{FE2}	$V_{CE} = 2 V, I_C = 2.5 A$	280			
Collector-emitter saturation voltage *	V _{CE(sat)}	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 10 \text{ mA}$		110		mV
		$I_{\rm C} = 2.5 \text{ A}, I_{\rm B} = 50 \text{ mA}$		220	320	
Transition frequency	f _T	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = -50 \text{ mA}, \text{ f} = 200 \text{ MHz}$		180		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30		pF
Turn-on time	t _{on}	Refer to the switching time measurement circuit		30		ns
Storage time	t _{stg}			100		ns
Fall time	t _f			10		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Pulse measurement (≤ 1 ms)

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