



P-Channel 20-V (D-S) MOSFET

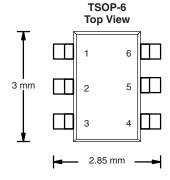
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)		
- 20	0.115 at V _{GS} = - 4.5 V	- 2.8	3.2 nC		
	0.205 at V _{GS} = - 2.5 V	- 2.1	3.2110		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- · PWM Optimized
- 100 % R_q Tested
- Compliant to RoHS Directive 2002/95/EC

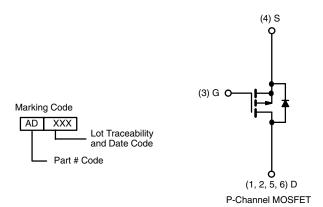






Ordering Information: Si3451DV-T1-E3 (Lead (Pb)-free)

Si3451DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V_{DS}	- 20	V	
Gate-Source Voltage		V_{GS}	± 12	v	
	T _C = 25 °C	I _D	- 2.8		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		- 2.3		
Continuous Diain Current (1) = 130 C)	T _A = 25 °C		- 2.6 ^{b, c}		
	T _A = 70 °C		- 2.1 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 10		
Continuous Source-Drain Diode Current	T _C = 25 °C	_	- 1.76		
	T _A = 25 °C	I _S	- 1.04 ^{b, c}		
Maximum Power Dissipation	T _C = 25 °C		2.1		
	T _C = 70 °C	P _D	1.3	w	
	T _A = 25 °C		1.25 ^{b, c}	VV	
	T _A = 70 °C]	0.8 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R_{thJA}	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	70	85		

Notes:

- a. Based on T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- $c. \quad t=5 \ s.$
- d. Maximum under Steady State conditions is 120 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•	l		
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 16.7		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1D = - 250 μΑ		2.1			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.6		- 1.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 10			Α	
Drain-Source On-State Resistance ^a	D	V _{GS} = - 4.5 V, I _D = - 2.6 A		0.092	0.115	Ω	
	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 1.9 A		0.164	0.205		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 2.6 A		5.5		S	
Dynamic ^b	•					•	
Input Capacitance	C _{iss}			250		pF	
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		80			
Reverse Transfer Capacitance	C _{rss}			55			
Total Gate Charge	Q_{g}	$V_{DS} = -10 \text{ V}, V_{GS} = -5.0 \text{ V}, I_{D} = -2.6 \text{ A}$		3.4	5.1		
Total date charge				3.2	5	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.6 \text{ A}$		0.5			
Gate-Drain Charge	Q_{gd}			1.4			
Gate Resistance	R_g	f = 1 MHz		8.5	13	Ω	
Turn-On Delay Time	t _{d(on)}			9	14		
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 4.76 \Omega$		30	45	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.1 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		32	48		
Fall Time	t _f			16	24		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 1.7	A	
Pulse Diode Forward Current ^a	I _{SM}				- 10		
Body Diode Voltage	V_{SD}	I _S = - 2.1 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			17	26	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 0.1 A dl/dt 100 A/vo T 05 °C		5	6	nC	
Reverse Recovery Fall Time	t _a	$I_F = -2.1 \text{ A, dI/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$		14			
everse Recovery Rise Time t _b				3		ns	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$

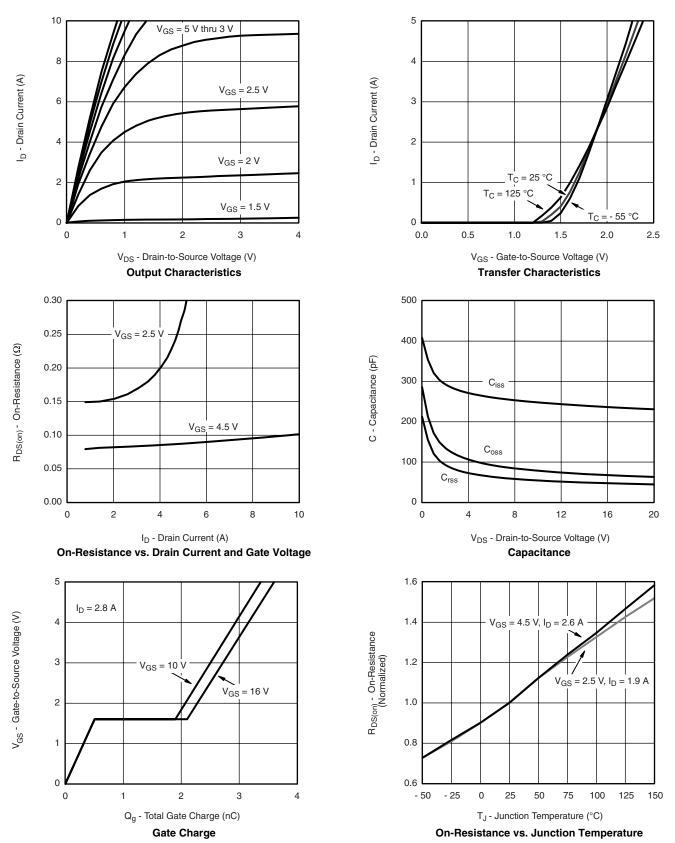
b. Guaranteed by design, not subject to production testing.





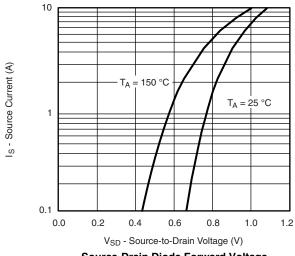


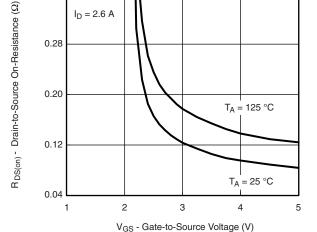
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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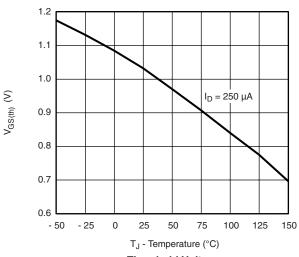


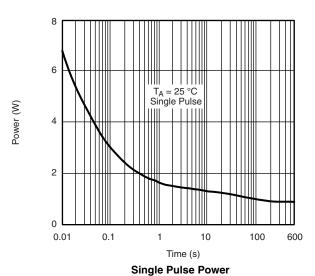


0.36

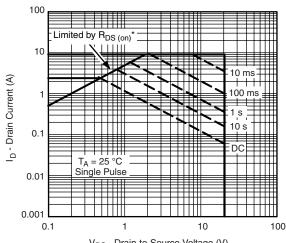
Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage





Threshold Voltage



 V_{DS} - Drain-to-Source Voltage (V) * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

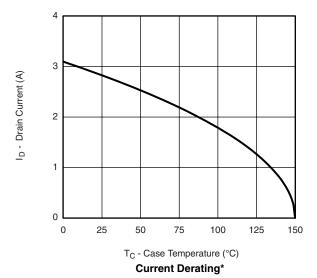
Safe Operating Area

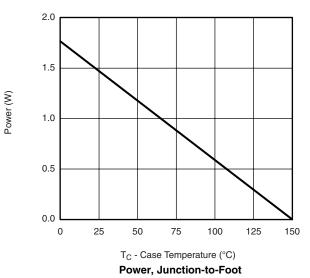




Si3451DV Vishay Siliconix

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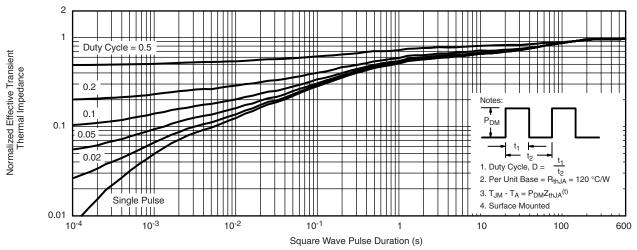


 $^{^{\}star}$ The power dissipation P_D is based on T_{J(max)} = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package

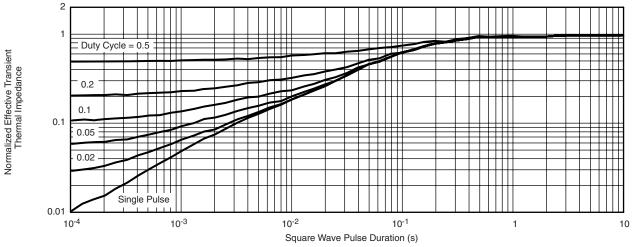
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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