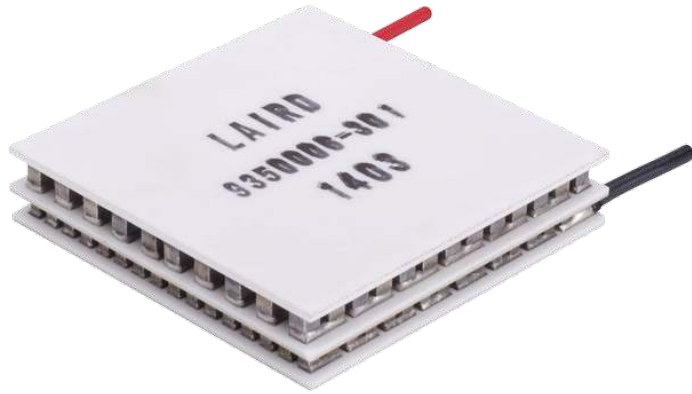


Multistage MS Series Thermoelectric Cooler

The MS2-192-14-20-11-18-11-RT-W8 multistage thermoelectric cooler is able to reach colder temperatures than single stage thermoelectric coolers. It has a maximum Qc of 38 Watts when ΔT = 0 and a maximum ΔT of 90 °C at Qc = 0.

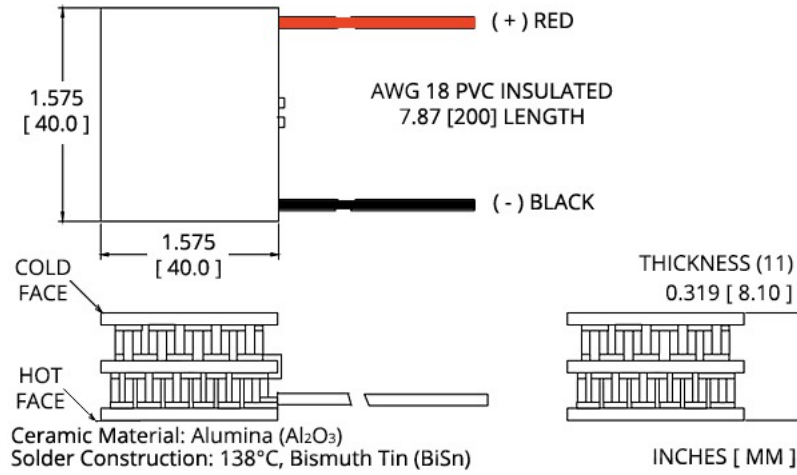


Features

- High temperature differential
- Precise temperature control
- Reliable solid-state operation
- Environmentally-friendly
- DC operation
- RoHS-compliant

Applications

- Thermoelectric Cooling for CMOS Sensors
- Heads-Up Displays, Imaging Sensors

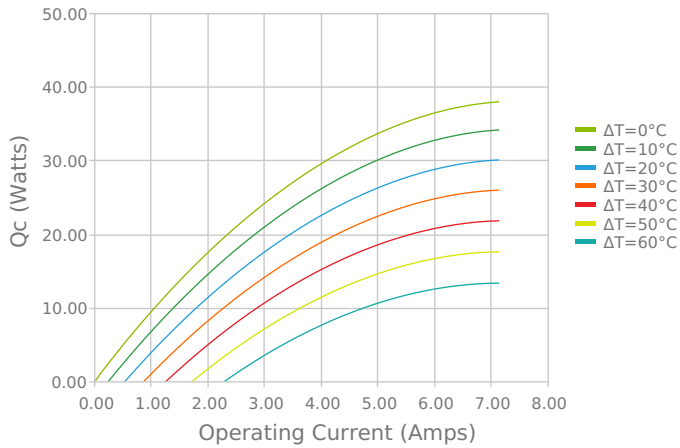


Note: Allow 0.020 in [0.5 mm] around perimeter of the thermoelectric cooler and lead wire attachment to accommodate sealant

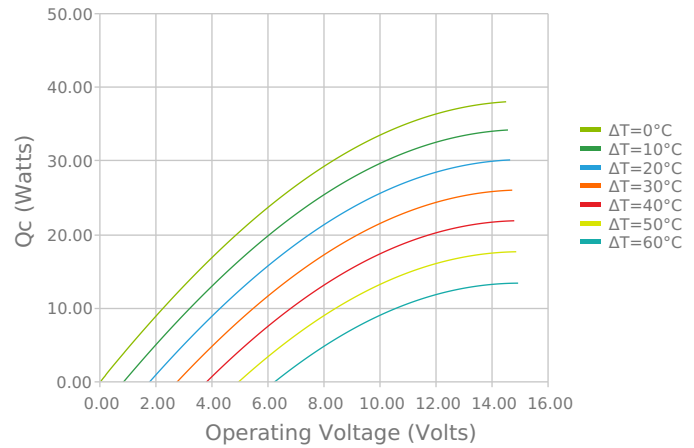
ELECTRICAL AND THERMAL PERFORMANCE

For maximum performance, be sure to orient the CONTROL side of the TEC against the application to be managed and the HEATSINK side against the heat sink or other heat rejection method. The CONTROL side is always opposite the side with lead attachments. Lead attachment is a passive heat loss and less impactful if located on the side that attaches to the heat exchanger.

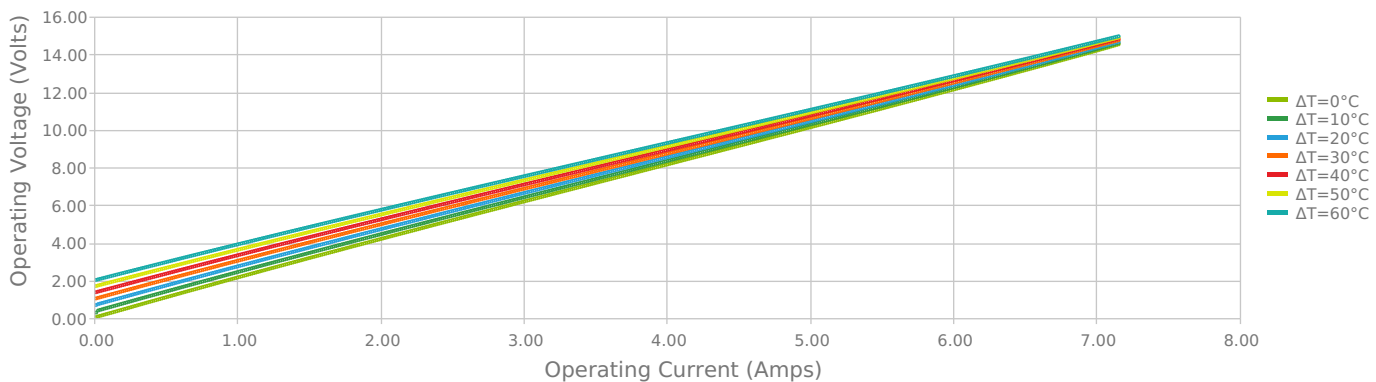
Heat Pumped at Cold Side
 Thot = 27 °C



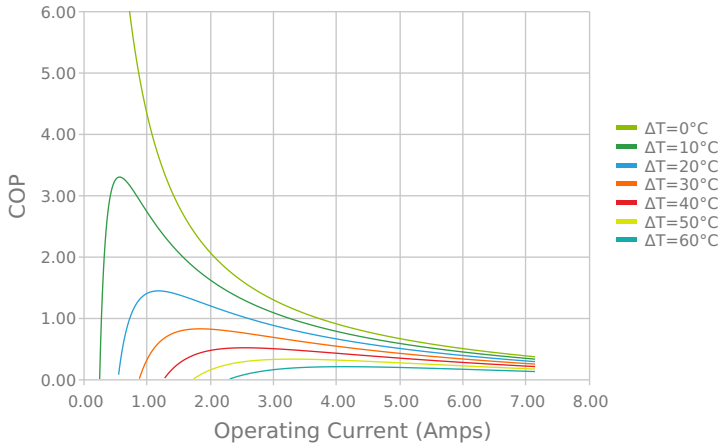
Heat Pumped at Cold Side
 Thot = 27 °C



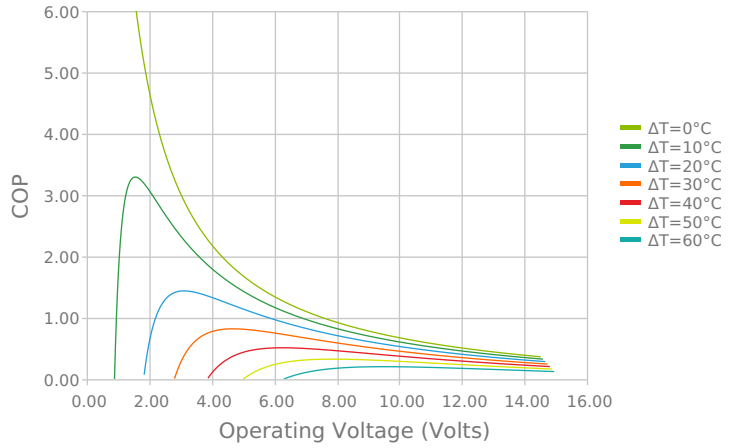
Current vs Voltage (I vs V)
 Thot = 27 °C



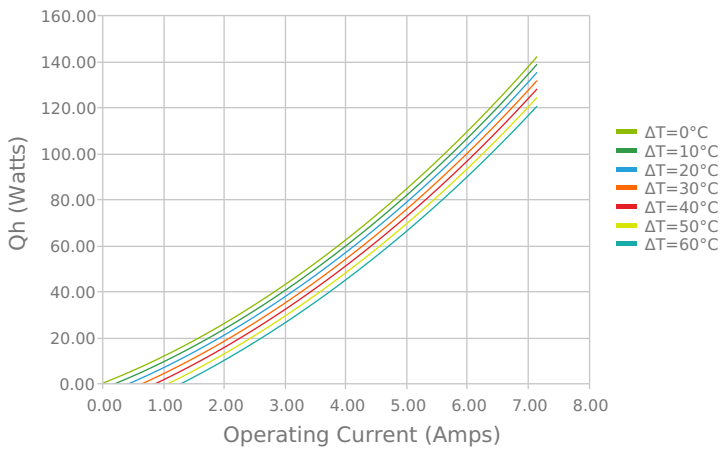
Coefficient of Performance (COP = Qc/Pin)
Thot = 27 °C



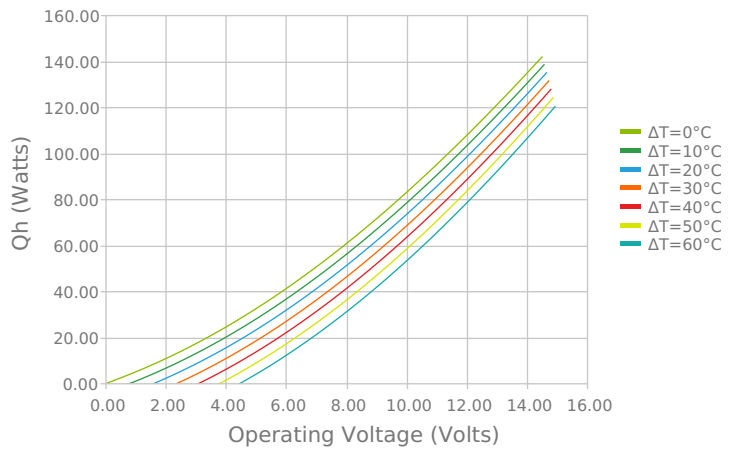
Coefficient of Performance (COP = Qc/Pin)
Thot = 27 °C



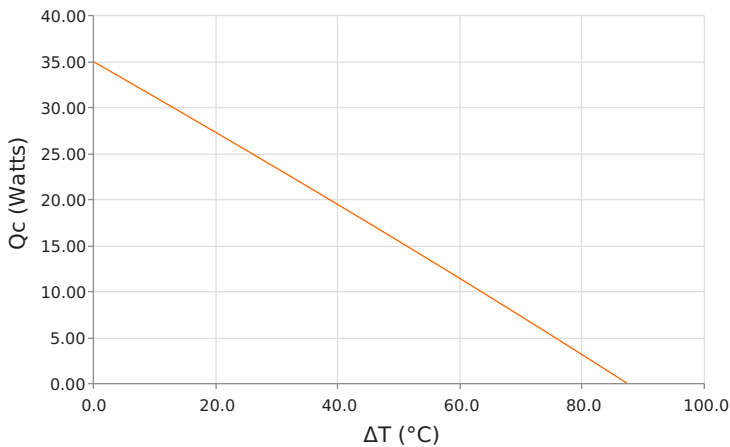
Total Heat Dissipated at Hot Side (Qh=Qc+Pin)
Thot = 27 °C



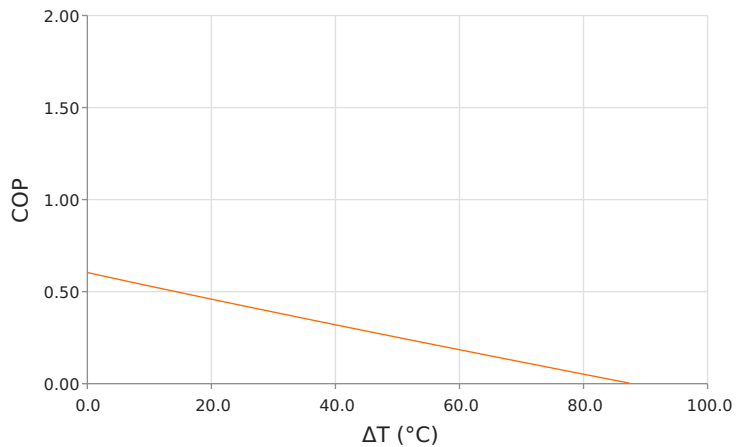
Total Heat Dissipated at Hot Side (Qh=Qc+Pin)
Thot = 27 °C



Heat Pumped at Cold Side (Qc)
Thot = 27 °C | Current = 5.4 Amps



Coefficient of Performance (COP = Qc/Pin)
Thot = 27 °C | Current = 5.4 Amps



SPECIFICATIONS*

Hot Side Temperature	27.0 °C
Qcmax ($\Delta T = 0$)	38.0 Watts
ΔT_{max} ($Q_c = 0$)	90.0 °C
I_{max} (I @ ΔT_{max})	6.9 Amps
V_{max} (V @ ΔT_{max})	14.8 Volts
Module Resistance	2.14 Ohms
Max Operating Temperature	80 °C
Weight	60.0 gram(s)

* Specifications reflect thermoelectric coefficients updated March 2020

FINISHING OPTIONS

Suffix	Thickness	Flatness / Parallelism	Hot Face	Cold Face	Lead Length
11	8.100 ±0.203 mm 0.319 ± 0.008 in	0.025 mm / 0.203 mm 0.001 in / 0.008 in	Lapped	Lapped	199.9 mm 7.87 in

SEALING OPTIONS

Suffix	Sealant	Color	Temp Range	Description
RT	RTV	Translucent or White	-60 to 204°C	Non-corrosive, silicone adhesive

NOTES

1. Max operating temperature: 80°C
2. Do not exceed I_{max} or V_{max} when operating module
3. Reference assembly guidelines for recommended installation
4. Solder tinning also available on metallized ceramics

Any information furnished by Laird and its agents, whether in specifications, data sheets, product catalogues or otherwise, is believed to be (but is not warranted as being) accurate and reliable, is provided for information only and does not form part of any contract with Laird. All specifications are subject to change without notice. Laird assumes no responsibility and disclaims all liability for losses or damages resulting from use of or reliance on this information. All Laird products are sold subject to the Laird Terms and Conditions of sale (including Laird's limited warranty) in effect from time to time, a copy of which will be furnished upon request.

© Copyright 2019-2022 Laird Thermal Systems, Inc. All rights reserved. Laird™, the Laird Ring Logo, and Laird Thermal Systems™ are trademarks or registered trademarks of Laird Limited or its subsidiaries.

Revision: 00 Date: 06-01-2022

Print Date: 06-14-2022