

BD243/A/B/C

Medium Power Linear and Switching Applications

· Complement to BD244, BD244A, BD244B and BD244C respectively



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage		
	: BD243	45	V
	: BD243A	60	V
	: BD243B	80	V
	: BD243C	100	V
V _{CEO}	Collector-Emitter Voltage		
	: BD243	45	V
	: BD243A	60	V
	: BD243B	80	V
	: BD243C	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	6	Α
I _{CP}	*Collector Current (Pulse)	10	Α
I _B	Base Current	2	Α
P _C	Collector Dissipation (T _C =25°C)	65	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage					
	: BD243	$I_{C}=30$ mA, $I_{B}=0$	45			V
	: BD243A		60			V
	: BD243B		80			V
	: BD243C		100			V
I _{CEO}	Collector Cut-off Current : BD243/243A	$V_{CE} = 30V, I_{B} = 0$			0.7	mA
	: BD243B/243C	$V_{CE} = 60V, I_{B} = 0$			0.7	mA
I _{CES}	Collector Cut-off Current : BD243	$V_{CE} = 45V, V_{BE} = 0$			0.4	mA
	: BD243A	$V_{CE} = 60V, V_{BE} = 0$			0.4	mA
	: BD243B	$V_{CE} = 80V, V_{BE} = 0$			0.4	mA
	: BD243C	$V_{CE} = 100V, V_{BE} = 0$			0.4	mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1	mA
h _{FE}	*DC Current Gain	$V_{CE} = 4V, I_{C} = 0.3A$	30			
		$V_{CE} = 4V, I_{C} = 3A$	15			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_C = 6A, I_B = 1A$			1.5	V
V _{RF} (on)	*Base-Emitter ON Voltage	$V_{CF} = 4V, I_{C} = 6A$			2	V

©2000 Fairchild Semiconductor International Rev. A, February 2000

Typical Characteristics

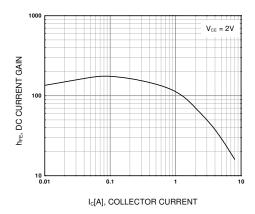


Figure 1. DC current Gain

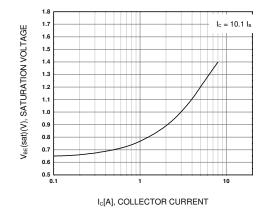


Figure 2. Base-Emitter Saturation Voltage

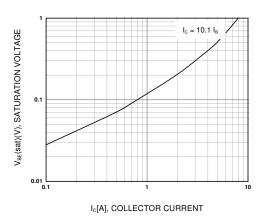
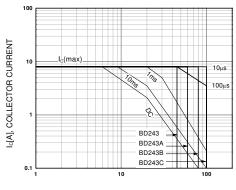


Figure 3. Collector-Emitter Saturation Voltage



 $V_{\text{CE}}[V]$, COLLECTOR-EMITTER VOLTAGE

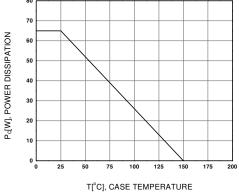


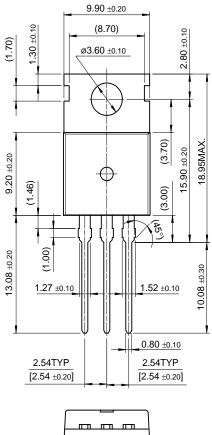
Figure 5. Power Derating

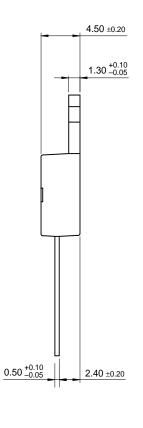
Figure 4. Safe Operating Area

©2000 Fairchild Semiconductor International Rev. A, February 2000

Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FACT™ QFET™ FACT Quiet Series™ QS™

FAST® Quiet Series TM SuperSOT TM -3 SuperSOT TM -6

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

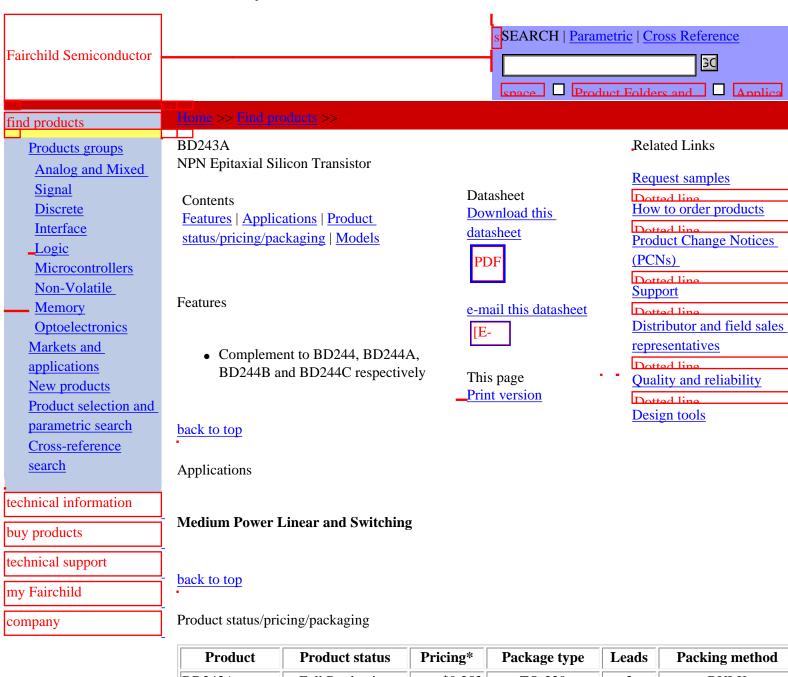
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2000 Fairchild Semiconductor International Rev. E



Product	Product status	Pricing*	Package type	Leads	Packing method
BD243A	Full Production	\$0.353	TO-220	3	BULK
BD243ATU	Full Production	\$0.353	TO-220	3	RAIL

^{* 1,000} piece Budgetary Pricing

back to top

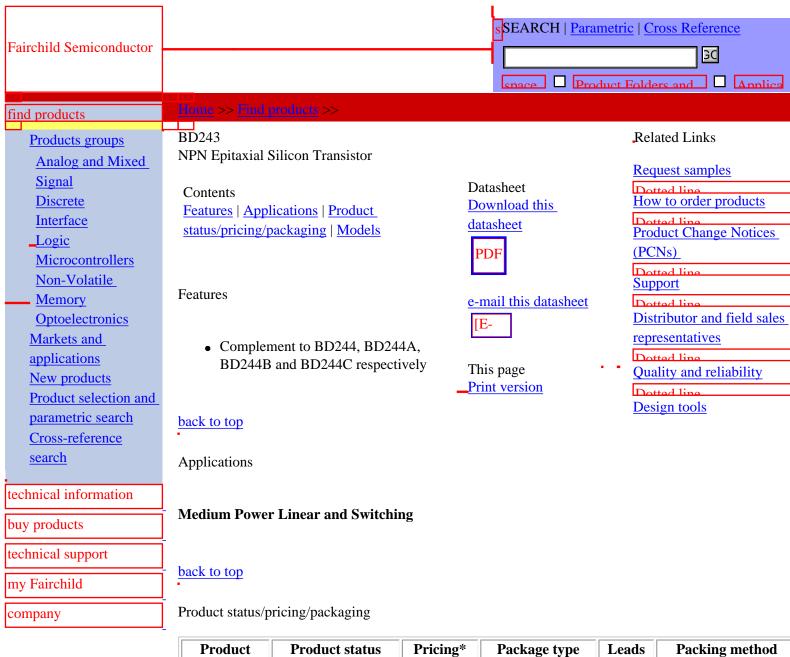
Models

Package & leads	Condition	Temperature range	Software version	Revision date
PSPICE				
TO-220-3	Electrical/Thermal	-25°C to 100°C	9	Nov 24, 2000

back to top

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor



Product	Product status	Pricing*	Package type	Leads	Packing method
BD243TU	Full Production	\$0.353	TO-220	3	RAIL

^{* 1,000} piece Budgetary Pricing

back to top

Models

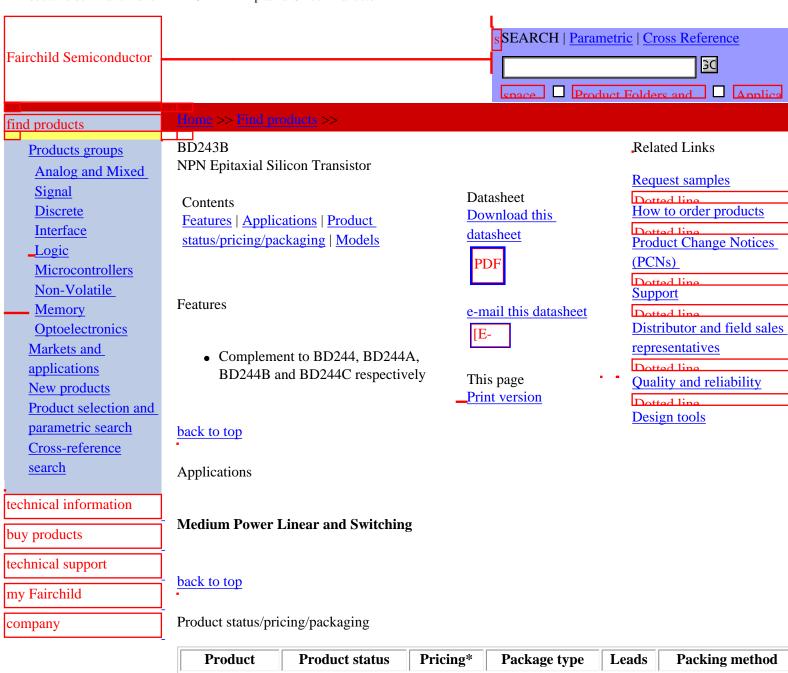
Package & leads	ckage & leads Condition Te		Software version	Revision date
PSPICE				
TO-220-3	Electrical/Thermal	-25°C to 100°C	9	Nov 24, 2000

back to top

Product Folder - Fairchild P/N BD243 - NPN Epitaxial Silicon Transistor

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor



Product	Product status	Pricing*	Package type	Leads	Packing method
BD243B	Full Production	\$0.353	TO-220	3	BULK
BD243BTU	Full Production	\$0.353	TO-220	3	RAIL

^{* 1,000} piece Budgetary Pricing

back to top

Models

Package & leads	Condition	Temperature range	Software version	Revision date
PSPICE				
TO-220-3	Electrical/Thermal	-25°C to 100°C	9	Nov 24, 2000

back to top

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor