



N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
30	0.057 at V _{GS} = 10 V	5.6 ^a	5.5		
	0.082 at V _{GS} = 4.5 V	4.7	5.5		

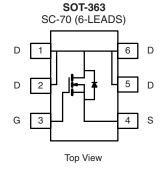
FEATURES

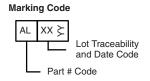
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_q and UIS Tested
- Compliant to RoHS Directive 2002/95/EC





Load Switch for Portable Devices





Ordering Information: Si1472DH-T1-E3 (Lead (Pb-free)

Si1472DH-T1-GE3 (Lead (Pb-free and Halogen-free)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		5.6	A	
Continuous Dusin Courset /T 450 90\8	T _C = 70 °C		4.5		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	4.2 ^{b, c}		
	T _A = 70 °C		3.4 ^{b, c}		
Pulsed Drain Current		I _{DM}	15		
Avalanche Current	1 0.1 mll	I _{AS}	10		
Repetitive Avalanche Energy	tive Avalanche Energy L = 0.1 mH		5	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C		2.3		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.3 ^{b, c}	Α	
	T _C = 25 °C		2.8	w	
Maximum Power Dissipation ^a	T _C = 70 °C	ь 🗔	1.8		
	T _A = 25 °C	P _D	1.5 ^{b, c}		
	T _A = 70 °C		1.0 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stq}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	60	80	°C/W	
Maximum Junction-to-Foot (Drain)	Steady	R _{thJF}	34	45		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 125 °C/W.

Si1472DH

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•	ı	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			25.15		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		5.6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oata Walkana Barin O		V _{DS} = 30 V, V _{GS} = 0 V			1	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 \text{ °C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	15			Α	
Durin Orania Or Olata Baristana a		V _{GS} = 10 V, I _D = 4.2 A		0.046	0.057	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 3.5 \text{ A}$		0.065	0.082		
Forward Transconductance	9 _{fs}	V _{DS} = 15 V, I _D = 4.2 A		8.5		S	
Dynamic ^b			•		•		
Input Capacitance	C _{iss}			380			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		75		pF	
Reverse Transfer Capacitance	C _{rss}			45			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 4.2 \text{ A}$		7	11	- nC	
				3.3	5		
Gate-Source Charge	Q_{gs}	$V_{DS} = 24V$, $V_{GS} = 4.5 V$, $I_D = 4.2 A$		1.2			
Gate-Drain Charge	Q_{gd}			1.0			
Gate Resistance	R_{g}	f = 1 MHz		7.1	10.6	Ω	
Turn-On Delay Time	t _{d(on)}			7.0	11	ns	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_1 = 4.4 \Omega$		56	84		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 3.4 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		18	27		
Fall Time	t _f			5.5	9		
Turn-On Delay Time	t _{d(on)}			15	23	- ns	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_L = 5.4 \Omega$		95	143		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 2.8 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		12	18		
Fall Time	t _f			7	11		
Drain-Source Body Diode Characterist	tics			1	•	<u> </u>	
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			2.3	_	
Pulse Diode Forward Current ^a	I _{SM}				15	A	
Body Diode Voltage	V _{SD}	I _S = 1.8 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			12.3	19	nC	
Body Diode Reverse Recovery Charge Q _{rr}		1 004 41/15 4004/		5	7.5		
Reverse Recovery Fall Time	t _a			7.6		ns	
Reverse Recovery Rise Time	t _b			4.7		1	

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

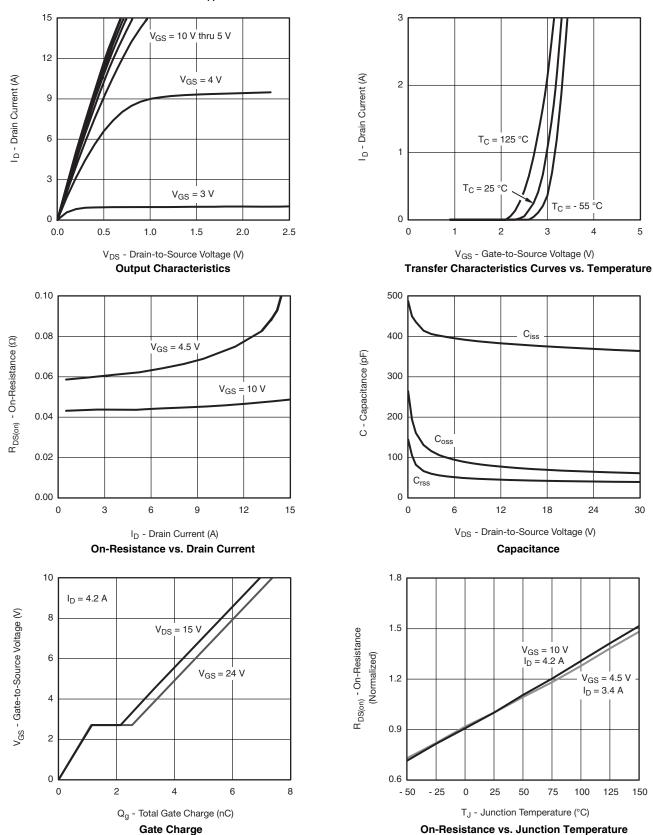
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







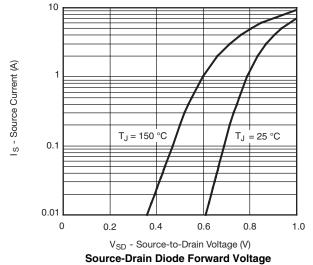
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted

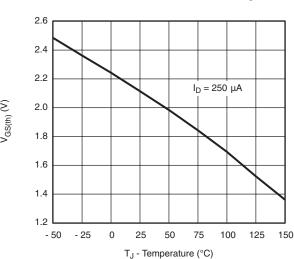


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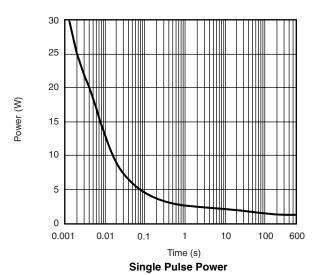


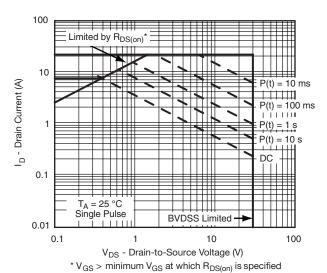


Threshold Voltage

0.10 $I_D = 4.2 \text{ A}$ $I_D = 4.2 \text{ A}$ $I_A = 125 \,^{\circ}\text{C}$ 0.04 $I_A = 25 \,^{\circ}\text{C}$ 0.04 $I_A = 25 \,^{\circ}\text{C}$ 0.05 $I_A = 25 \,^{\circ}\text{C}$ 0.07 $I_A = 125 \,^{\circ}\text{C}$ 0.09 $I_A = 25 \,^{\circ}\text{C}$ 0.09

R_{DS(on)} vs. V_{GS} vs. Temperature





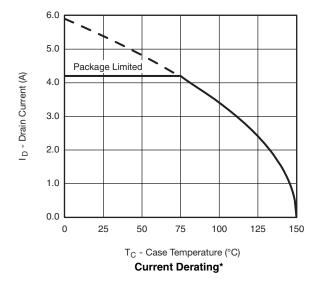
Safe Operating Area, Junction-to-Ambient

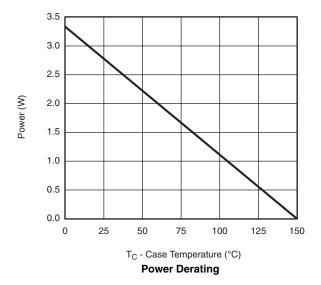




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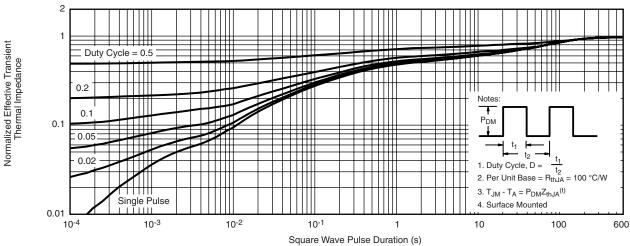


^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

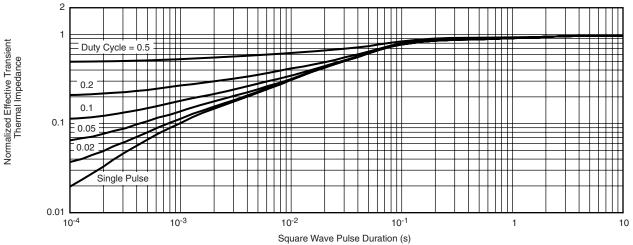
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Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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