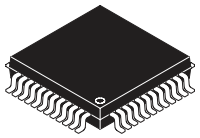
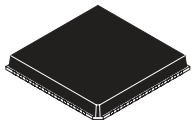
TEXAS INSTRUMENTS





SLES019 − DECEMBER 2001

CCD SIGNAL PROCESSOR FOR DIGITAL CAMERAS

FEATURES

- **CCD Signal Processing:**
  - **Correlated Double Sampling (CDS)**
  - **Programmable Black Level Clamping**
- **Programmable Gain Amplifier (PGA) −6-dB to 42-dB Gain Ranging**
- **10-Bit Digital Data Output:**
  - **Up to 28-MHz Conversion Rate**
  - **No Missing Codes**
- **77-dB Signal-To-Noise Ratio**
- **Portable Operation:**
  - **Low Voltage: 2.7 V to 3.6 V**
  - **Low Power: 93 mW (Typ) at 3 V**
  - **Stand-By Mode: 6 mW**

APPLICATIONS

- **DSC, DVC, Security Camera**

DESCRIPTION

The VSP2270 device is a complete mixed-signal processing IC for digital cameras providing signal conditioning and analog-to-digital conversion for the output of a charge-coupled device (CCD) array. The primary CCD channel provides correlated double sampling (CDS) to extract the video information from the pixels, –6-dB to 42-dB gain range with digital control for varying illumination conditions, and black level clamping for an accurate black level reference. Input signal clamping and offset correction of the input CDS are also performed. The stable gain control is linear in dB. Additionally, the black level is quickly recovered after gain change.

The VSP2270Y device is available in a 48-lead LQFP package and the VSP2270M device is available in a 48-lead P-VQFN package. Both devices operate from a single 3-V/3.3-V supply.

AVAILABLE OPTIONS

PRODUCT	PACKAGE	PACKAGE OUTLINE DESIGNATOR†	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER‡	TRANSPORT MEDIA
VSP2270Y	48-Lead LQFP	PT	−25°C to 85°C	VSP2270Y	VSP2270Y	250-piece tray
VSP2270Y	48-Lead LQFP	PT	−25°C to 85°C	VSP2270Y	VSP2270Y/2K	Tape and reel
VSP2270M	48-Lead P-VQFN	RGN	−25°C to 85°C	VSP2270M	VSP2270M	250-piece tray
VSP2270M	48-Lead P-VQFN	RGN	−25°C to 85°C	VSP2270M	VSP2270M/2K	Tape and reel

† A detailed drawing and a dimension table are located at the end of the data sheet.

‡ Models with a slash (/) are available only in tape and reel in the quantities indicated (e.g., /2K indicates 2,000 devices per reel). Ordering 2,000 pieces of the VSP2270Y/2K device will get a single 2,000-piece tape and reel.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
VSP2270Y	LIFEBUY	LQFP	PT	48	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 85	VSP2270Y	

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

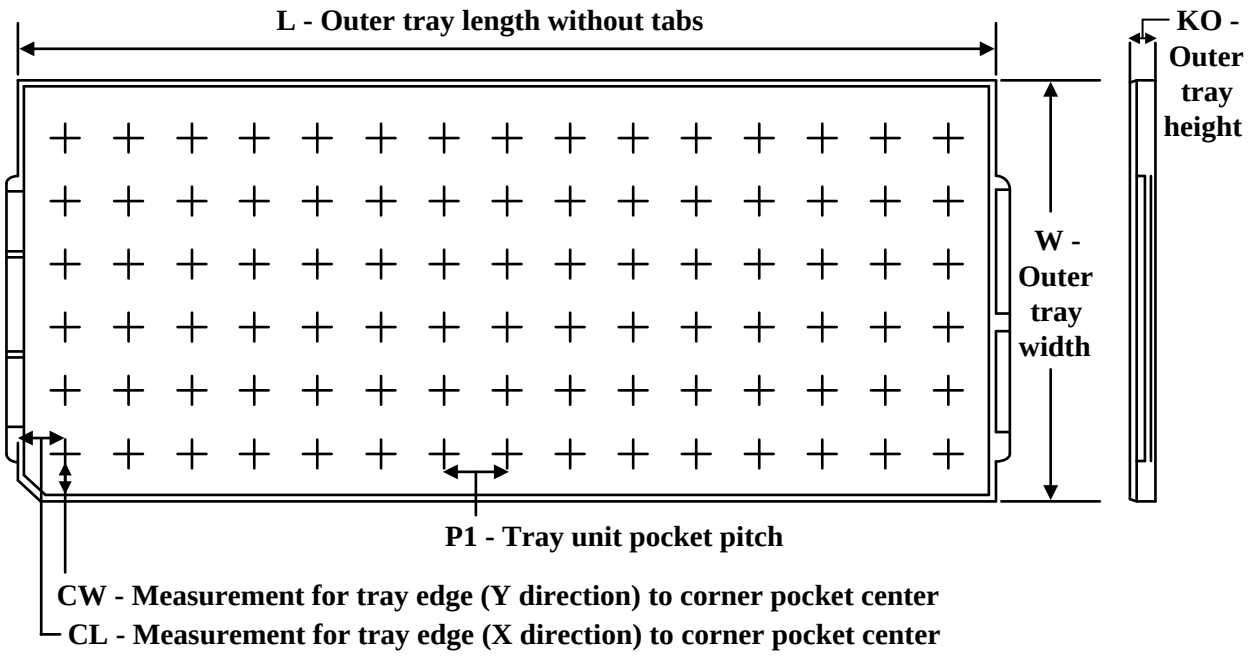
(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

TRAY



Chamfer on Tray corner indicates Pin 1 orientation of packed units.

*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	Unit array matrix	Max temperature (°C)	L (mm)	W (mm)	K0 (µm)	P1 (mm)	CL (mm)	CW (mm)
VSP2270Y	PT	LQFP	48	250	10x25	150	315	135.9	7.62	12.2	11.1	11.25

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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265