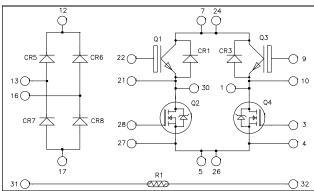
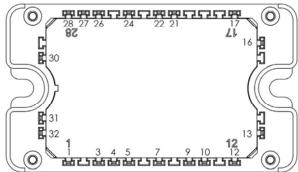


Full bridge + rectifier bridge CoolMOS & Trench + Field Stop IGBT3 Power Module



Top switches: Trench + Field Stop IGBT3

Bottom switches: CoolMOSTM



All multiple inputs and outputs must be shorted together 7/24; 5/26

Trench & Field Stop IGBT3 Q1, Q3:

 $V_{CES} = 600V$ ;  $I_C = 50A$  @  $T_C = 80$ °C

CoolMOSTM Q2, Q4:

 $V_{DSS} = 600V$ 

 $R_{DSon} = 70 m\Omega \text{ max } \text{(a)} \text{ Tj} = 25^{\circ}\text{C}$ 

#### Application

• Solar converter

#### **Features**

- Q2, Q4 CoolMOSTM
  - Ultra low R<sub>DSon</sub>
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- Q1, Q3 Trench & Field Stop IGBT3
  - Low voltage drop
  - Switching frequency up to 20 kHz
  - RBSOA & SCSOA rated
  - Low tail current
- · Very low stray inductance
- Kelvin source for easy drive
- Internal thermistor for temperature monitoring
- High level of integration

#### **Benefits**

- Optimized conduction & switching losses
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- Easy paralleling due to positive T<sub>C</sub> of V<sub>CEsat</sub>
- RoHS Compliant

#### All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



### 1. Top switches

### 1.1 Top Trench + Field Stop IGBT3 characteristics (per IGBT)

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 600V$				250	μA
V <sub>CE(sat)</sub>	Collector Emitter Saturation Voltage	$V_{GE} = 15V$	$T_j = 25^{\circ}C$		1.5	1.9	W
V CE(sat)	Conector Emitter Saturation Voltage	$I_C = 50A$	$T_{j} = 150^{\circ}C$		1.7		v
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 600 \mu A$		5.0	5.8	6.5	V
$I_{GES}$	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE}$	= 0V			600	nA

### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$C_{ies}$	Input Capacitance	$V_{GE} = 0V$			3150		
Coes	Output Capacitance	$V_{CE} = 25V$			200		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz			95		
$Q_{G}$	Gate charge	$V_{GE}=\pm 15V, I_{C}=5V_{CE}=300V$	50A		0.5		μС
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C)			110		
T <sub>r</sub>	Rise Time	$V_{GE} = \pm 15V$			45		
$T_{d(off)}$	Turn-off Delay Time	$V_{Bus} = 300V$ $I_{C} = 50A$			200		ns
$T_{\rm f}$	Fall Time	$R_G = 8.2\Omega$		40			
$T_{d(on)}$	Turn-on Delay Time	Inductive Switch	ning (150°C)		120		
$T_{r}$	Rise Time	$V_{GE} = \pm 15V$			50		ns
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 300V$ $I_{\text{C}} = 50A$			250		
$T_{\mathrm{f}}$	Fall Time	$R_G = 8.2\Omega$			60		
Е	Turn off Switching Energy	$V_{GE} = \pm 15V$ $V_{Bus} = 300V$	$T_j = 25^{\circ}C$		1.35		mJ
$E_{off}$	Turn-off Switching Energy	$I_C = 50A$ $R_G = 8.2\Omega$	$T_j = 150$ °C		1.75		1113
$I_{sc}$	Short Circuit data	$V_{GE} \le 15V ; V_{Bus} = 360V$ $t_p \le 6\mu s ; T_i = 150^{\circ}C$			250		A
$R_{thJC}$	Junction to Case Thermal resistance					0.85	°C/W



### 1.2 Top diode characteristics (CR1, CR3) (per diode)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			600			V
ī	Maximum Reverse Leakage Current	V <sub>R</sub> =600V	$T_j = 25$ °C			25	
$I_{RM}$		V <sub>R</sub> -000V	$T_{j} = 125^{\circ}C$			500	μA
$I_F$	DC Forward Current		Tc = 80°C		25		A
	Diode Forward Voltage	$I_F = 25A$			1.8	2.2	
$V_{\mathrm{F}}$		$I_F = 50A$			2.2		V
		$I_F = 25A$	$T_j = 125$ °C		1.6		
t <sub>rr</sub>	Reverse Recovery Time		$T_j = 25$ °C		30		ns
чт	Reverse Recovery Time	$I_F = 25A$ $V_R = 400V$	$T_{j} = 125^{\circ}C$		175		113
0	Reverse Recovery Charge	$di/dt = 200A/\mu s$	$T_j = 25$ °C		55		nC
Qπ	Reverse Recovery Charge		$T_{j} = 125^{\circ}C$		485		пС
$R_{thJC}$	Junction to Case Thermal resistance					1.4	°C/W

#### 2. Bottom switches

### 2.1 Bottom CoolMOSTM characteristics (Per CoolMOSTM)

### Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
Ţ	Continuous Drain Current	$T_c = 25$ °C	39	
$I_D$	Continuous Diani Current	$T_c = 80^{\circ}C$	29	Α
$I_{DM}$	Pulsed Drain current		160	
$V_{GS}$	Gate - Source Voltage		±20	V
R <sub>DSon</sub>	Drain - Source ON Resistance		70	mΩ
$P_{\mathrm{D}}$	Maximum Power Dissipation	$T_c = 25$ °C	250	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		20	A
$\mathrm{E}_{\mathrm{AR}}$	Repetitive Avalanche Energy		1	mJ
$E_{AS}$	Single Pulse Avalanche Energy		1800	IIIJ

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			25	4
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			250	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 39A$			70	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.7 \text{mA}$		3	3.9	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA



#### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		7		
$C_{oss}$	Output Capacitance	$V_{\rm DS} = 25V$		2.56		nF
$C_{rss}$	Reverse Transfer Capacitance	f=1MHz		0.21		
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		259		
$Q_{gs}$	Gate – Source Charge	$V_{\text{Bus}} = 300V$		29		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 39A$		111		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching @ 125°C		21		
T <sub>r</sub>	Rise Time	$V_{GS} = 15V$		30		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 39A$		283		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 5\Omega$		84		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		670		1
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 39A, R_G = 5\Omega$		980		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1096		1
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 39A, R_G = 5\Omega$		1206		μJ
$R_{\text{thJC}}$	Junction to Case Thermal resistance				0.5	°C/W

#### Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$I_S$	Continuous Source current		Tc = 25°C		39		Α
	(Body diode)		$Tc = 80^{\circ}C$		29		Λ
$V_{\mathrm{SD}}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = -39A$	L			1.2	V
dv/dt	Peak Diode Recovery <b>1</b>					6	V/ns
$t_{rr}$	Reverse Recovery Time	$I_S = -39A$	$T_j = 25^{\circ}C$		580		ns
Qrr	Reverse Recovery Charge	$V_R = 350V$ $di_S/dt = 100A/\mu s$	$T_j = 25^{\circ}C$		23		μС

• dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 $I_S \le -39A$   $di/dt \le 100A/\mu s$   $V_R \le V_{DSS}$   $T_j \le 150$ °C

### 3. Rectifier bridge (per diode)

#### Absolute maximum ratings

Symbol	Paramet	er			Max ratings	Unit
$V_R$	Maximum DC reverse Voltage				600	V
$V_{RRM}$	Maximum Peak Repetitive Reverse Vo	ltage			000	v
I <sub>F(AV)</sub>	Maximum Average Forward Current	Duty cycle = 50	0%	$T_C = 80$ °C	40	
$I_{FSM}$	Non-Repetitive Forward Surge Current	8.3	3ms	$T_J = 45^{\circ}C$	320	A

#### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 30A$			1.8	2.2	
		$I_F = 60A$			2.2		V
		$I_F = 30A$	$T_{j} = 125^{\circ}C$		1.5		
T	Maximum Reverse Leakage Current	V - 600V	$T_j = 25^{\circ}C$			250	
$I_{RM}$		$V_R = 600V$	$T_j = 125$ °C			500	μΑ



### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$t_{rr}$	Reverse Recovery Time	$I_F=1A, V_R=30V$ $di/dt = 100A/\mu s$	$T_j = 25^{\circ}C$		22		ns
t <sub>rr</sub>	Reverse Recovery Time		$T_j = 25^{\circ}C$		25		ns
VII	The verse receivery Time		$T_{j} = 125^{\circ}C$		160		113
$Q_{rr}$	Reverse Recovery Charge	$I_F = 30A$ $V_R = 400V$ $di/dt = 200A/\mu s$	$T_j = 25$ °C		35		nC
Vп	Reverse Recovery Charge		$T_i = 125^{\circ}C$		480		
$I_{RRM}$	Reverse Recovery Current		$T_j = 25$ °C		3		A
1RRM	Reverse Recovery Current		$T_{\rm j} = 125^{\circ}{\rm C}$		6		A
t <sub>rr</sub>	Reverse Recovery Time	$I_E = 30A$			85		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$V_R = 400V$ $di/dt = 1000A/\mu s$	$T_{j} = 125^{\circ}C$		920		μC
I <sub>RRM</sub>	Reverse Recovery Current				20		A
$R_{thJC}$	Junction to Case Thermal Resistance					1.2	°C/W

### 4. Thermal and package characteristics

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T <sub>C</sub> =100°C		4		%

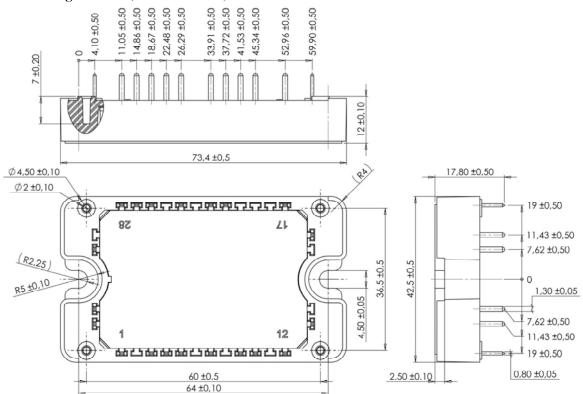
$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Thermistor temperature R<sub>T</sub>: Thermistor value at T

### Package characteristics

Symbol	Characteristic				Typ	Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1	MS Isolation Voltage, any terminal to case t =1 min, 50/60Hz					V
$T_{J}$	Operating junction temperature range			-40		175	
$T_{STG}$	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g



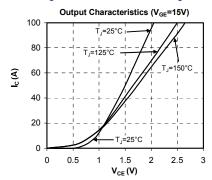
### SP3 Package outline (dimensions in mm)

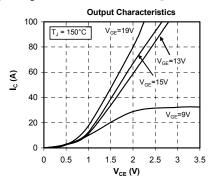


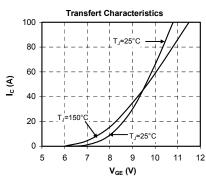


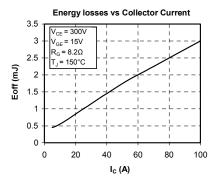
### 5. Top switches curves

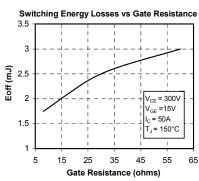
#### 5.1 Top Trench + Field Stop IGBT3 typical performance curves (per IGBT)

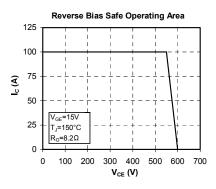


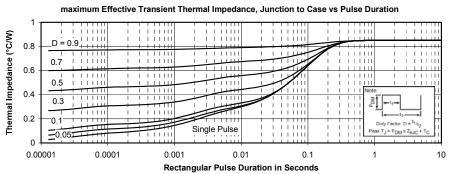






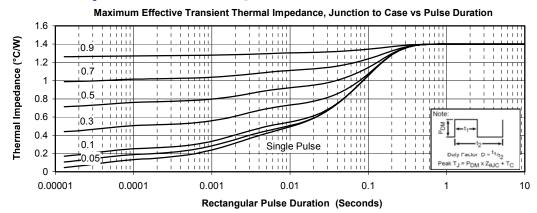


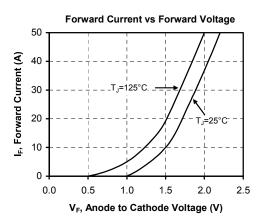






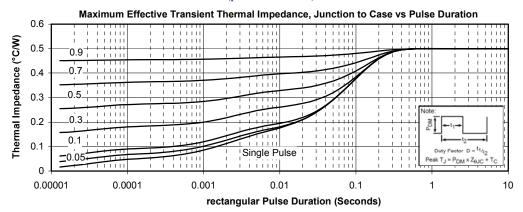
### **5.2 Top diode characteristics** (per diode)

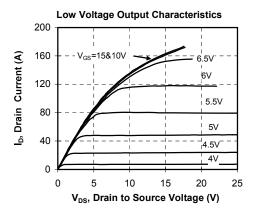


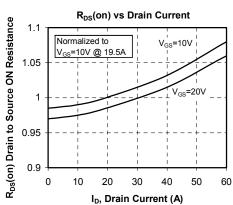


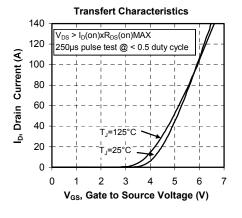


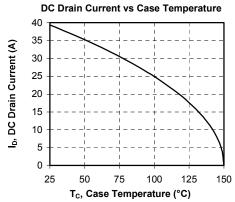
#### 6. Bottom switches CoolMOS<sup>TM</sup> (per CoolMOS<sup>TM</sup>)



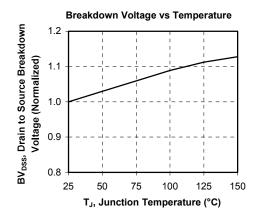


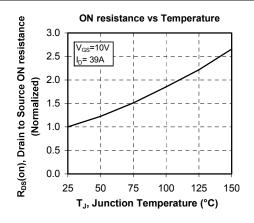


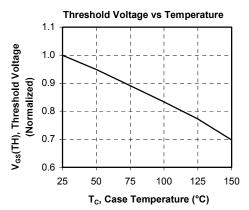


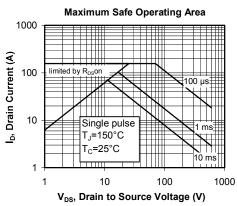


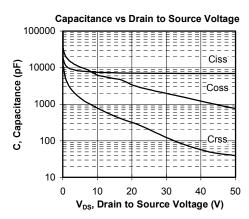


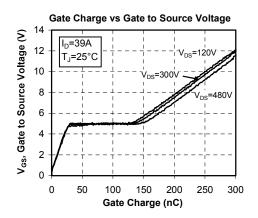




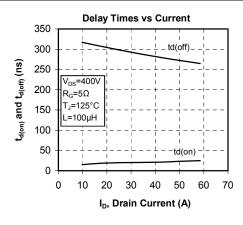


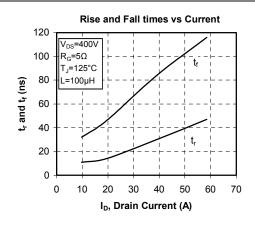


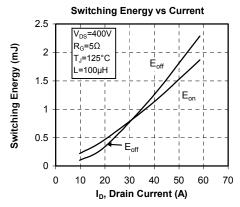


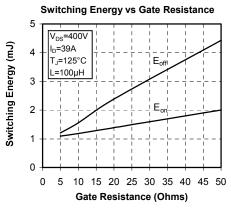


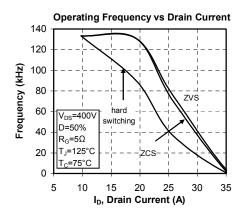


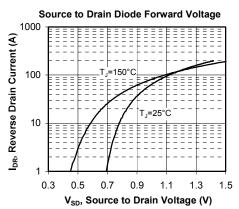






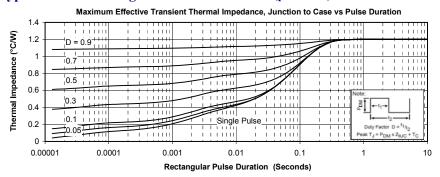


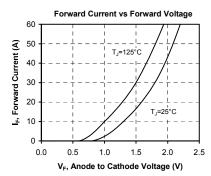


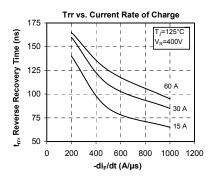


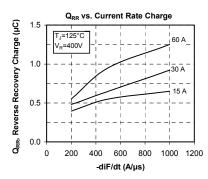


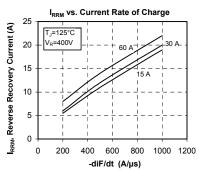
#### 7. Typical rectifier bridge Performance Curve (per diode)

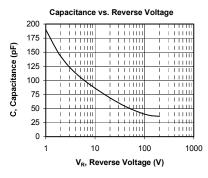












"COOLMOS" comprise a new family of transistors developed by Infineon Technologies AG. "COOLMOS" is a trademark of Infineon Technologies AG".

12 - 13

#### **DISCLAIMER**

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at <a href="http://www.microsemi.com/legal/tnc.asp">http://www.microsemi.com/legal/tnc.asp</a>

#### Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.