

General Description

The MAX4060/MAX4061/MAX4062 are differential-input microphone preamplifiers optimized for notebook and PDA audio systems. These devices feature adjustable gain with excellent power-supply rejection and common-mode rejection ratios, making them ideal for lownoise applications in portable audio systems.

The MAX4060/MAX4062 are capable of switching their output between the differential input and a singleended auxiliary microphone amplifier input. In addition, the MAX4060/MAX4062 have a low-noise microphone bias generator. The differential gain of the MAX4061/MAX4062 is set with a single resistor. The MAX4060 has a fixed gain of 10V/V and is PC99/2001 compliant. The MAX4061 includes a complete shutdown mode. In shutdown, the supply current is reduced to 0.3µA and the current to the microphone bias is cut off for ultimate power savings.

The MAX4060 operates from a 4.5V to 5.5V single supply and the MAX4061/MAX4062 operate from 2.4V to 5.5V. All devices are specified over the extended operating temperature range, -40°C to +85°C. The MAX4060/MAX4061 are available in tiny 8-pin thin QFN (3mm x 3mm x 0.8mm) and 8-pin $\mu MAX^{(g)}$ packages. The MAX4062 is available in a 10-pin µMAX package.

Applications

Notebook Audio USB Audio Peripherals Systems AES-42-Compliant **Tablet PCs** Microphones PDA Audio Systems Signal Conditioning

Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK
MAX4060ETA	-40°C to +85°C	8 Thin QFN-EP*	ABY
MAX4060EUA	-40°C to +85°C	8 µMAX	_
MAX4061ETA	-40°C to +85°C	8 Thin QFN-EP*	ABZ
MAX4061EUA	-40°C to +85°C	8 µMAX	_
MAX4062EUB	-40°C to +85°C	10 μMAX	

^{*}EP = Exposed paddle.

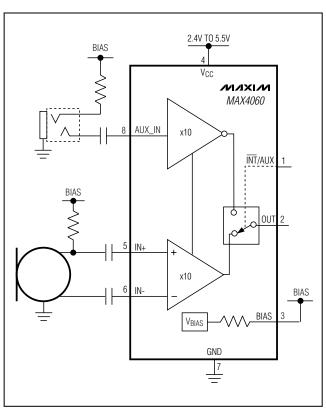
Pin Configurations and Selector Guide appear at end of data sheet.

µMAX is a registered trademark of Maxim Integrated Products, Inc.

Features

- ♦ 2.4V to 5.5V Single-Supply Operation
- ♦ Adjustable Gain or Fixed-Gain Options
- ♦ High PSRR (86dB at 1kHz)
- ♦ High CMRR (70dB at 1kHz)
- **♦ Low Input-Referred Noise**
- ♦ Integrated Microphone Bias
- ♦ 750µA Supply Current
- ♦ 0.3µA Shutdown Current
- ♦ ±4kV ESD Protection (AUX_IN)
- ♦ Rail-to-Rail Outputs
- ♦ THD+N: 0.04% at 1kHz
- ♦ Available in Space-Saving Packages 8-Pin Thin QFN (MAX4060/MAX4061) 8-Pin µMAX (MAX4060/MAX4061) 10-Pin µMAX (MAX4062)

Typical Operating Circuit



ABSOLUTE MAXIMUM RATINGS

Duration of Short Circuit to GND or VCC	Supply Voltage (V _{CC} to GND)0.3V to +6V Any Other Pin to GND0.3V to (V _{CC} + 0.3V)
Continuous Input Current (any pin)	
Continuous Power Dissipation ($T_A = +70^{\circ}C$) 8-Pin Thin QFN (derate 4.7mW/°C above +70°C)379mW	
8-Pin Thin QFN (derate 4.7mW/°C above +70°C)379mW	

10-Pin µMAX (derate 5.6mW/°C above	+70°C)444mW
Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC}=3V \text{ for MAX4061/MAX4062}, V_{CC}=5V \text{ for MAX4060}, GND=0V, \overline{SHDN}=V_{CC}, \overline{INT}/AUX=0V, R_G=11.11k\Omega, R_L=100k\Omega$ to 1.5V, $R_{BIAS}=\infty$, $T_A=T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A=+25^{\circ}C$.) (Notes 1, 2)

PARAMETER	SYMBOL	CON	IDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage Pange	Vcc	Inferred from	MAX4061/MAX4062	2.4		5.5	V
Supply Voltage Range	VCC	PSRR test	MAX4060	4.5		5.5	V
Supply Current	Icc				0.75	1.2	mA
Output Common-Mode Voltage	Vocm			1.25	1.5	1.75	V
Slew Rate	SR	$A_V = 10V/V$			±1		V/µs
Supply Current in Shutdown	ISHDN	V SHDN = 0V, MAX40	61		0.001	1	μΑ
Output Chart Circuit Current	la a	To GND			30		Λ
Output Short-Circuit Current	ISC	To V _{CC}			30		mA
DIFFERENTIAL INPUT (INT/AUX	= 0V for MAX	4060/MAX4062, defaul	It for MAX4061)				
Input Offset Voltage	Vos				±0.1	±5	mV
Common-Mode Input Voltage Range	V _{CM}					2	٧
Maximum Differential Input Voltage	VDIFFMAX	A _V = 1V/V, MAX4061/MAX4062			1		V
Small-Signal Bandwidth	BW _{-3dB}				600		kHz
Input Resistance	RIN	Either differential inpu	ut		100		kΩ
Input Resistance Match	RMATCH				1		%
		$A_V = 10V/V$, $f = 1kHz$			100		
Input Noise-Voltage Density	e _n	$A_V = 100V/V$, $f = 1kH$ only	lz, MAX4061/MAX4062		20		nV/√Hz
RMS Output Noise Voltage	V _{NRMS}	$A_V = 10V/V$, $BW = 22$	PHz to 22kHz		125		μV _{RMS}

__ /N/XI/M

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC}=3V \text{ for MAX4061/MAX4062}, V_{CC}=5V \text{ for MAX4060}, GND=0V, \overline{SHDN}=V_{CC}, \overline{INT}/AUX=0V, R_G=11.11k\Omega, R_L=100k\Omega$ to 1.5V, $R_{BIAS}=\infty$, $T_A=T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A=+25^{\circ}C$.) (Notes 1, 2)

PARAMETER	SYMBOL	CON	DITIONS	MIN	TYP	MAX	UNITS	
Total Harmonic Distortion Plus Noise	THD+N	$A_V = 10V/V$, $f = 1kHz$, $BW = 22Hz$ to $22kHz$	$V_{OUT} = 0.7V_{RMS}$		0.04		%	
		1V < V _{CM} < 2V,	RG = open	1	1.13	1.3		
Differential Gain	A. (5.155	$V_{OUT} = 0.7V_{RMS}$	$R_G = 11.11k\Omega$	9.6	10	10.4	V/V	
Differential Gain	AVDIFF	MAX4061/MAX4062	$RG = 1.01k\Omega$	96	100	104	V/V	
		1V < V _{CM} < 2V, V _{OUT}	$= 0.7 V_{RMS}, MAX4060$	9.6	10.0	10.4		
Common-Mode Rejection Ratio	CMRR	$V_{CM} = 500 \text{mV}_{P-P}, f =$	1kHz		70		dB	
		$T_A = +25^{\circ}C$		72	89			
Power-Supply Rejection Ratio	PSRR	$T_A = T_{MIN}$ to T_{MAX}		60			dB	
		$V_{CC} = 5V \pm 100 \text{mV}, f = 1 \text{kHz}$			86			
AUXILIARY INPUT (MAX4060/MA	X4062, INT/A	UX = VCC)						
Small-Signal Bandwidth	BW _{-3dB}				200		kHz	
Input Resistance	R _{IN}				100		kΩ	
Input Noise-Voltage Density	en	f = 1kHz			45		nV/√Hz	
RMS Output Noise Voltage	V _{NRMS}	BW = 22Hz to 22kHz			385		μV _{RMS}	
Total Harmonic Distortion Plus Noise	THD+N	f = 1kHz, BW = 22Hz to 22kHz			0.05		%	
	MAX4062, INT/AU BW-3dB RIN en VNRMS THD+N PSRR AVAUX 4062) VOUT	T _A = +25°C		65	90		ID	
Power-Supply Rejection Ratio		TA = TMIN - TMAX	50			dB		
Voltage Gain	Avaux	$V_{OUT} = 0.7V_{RMS}$		-10.7	-10	-9.3	V/V	
BIAS OUTPUT (MAX4060/MAX40	62)			•				
0 1 17/1		IBIAS = 0.8mA to GNE	2	2.2				
Output Voltage	VOUT	IBIAS = 0.5mA to GNE), MAX4062	2	2.2		V	
		I _{BIAS} = 0.8mA to GND, MAX4060 (T _A = +25°C)			2.5		kΩ	
Output Resistance	Rout	$I_{BIAS} = 0.5 \text{mA to GNE}$ $(T_A = +25^{\circ}\text{C})$		22	40	Ω		
	.,	I _{BIAS} = 0.8mA to GNE 22kHz, MAX4060), BW = 22Hz to		50		.,	
Output Noise Voltage	VNRMS	I _{BIAS} = 0.5mA to GNE 22kHz, MAX4062), BW = 22Hz to		20		μV _{RMS}	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC}=3V \text{ for MAX4061/MAX4062}, \ V_{CC}=5V \text{ for MAX4060}, \ GND=0V, \ \overline{SHDN}=V_{CC}, \ \overline{INT}/AUX=0V, \ R_G=11.11k\Omega, \ R_L=100k\Omega \text{ to } 1.5V, \ R_{BIAS}=\infty, \ T_A=T_{MIN} \text{ to } T_{MAX}, \ unless \text{ otherwise noted.}$ Typical values are at $T_A=+25^{\circ}C.$) (Notes 1, 2)

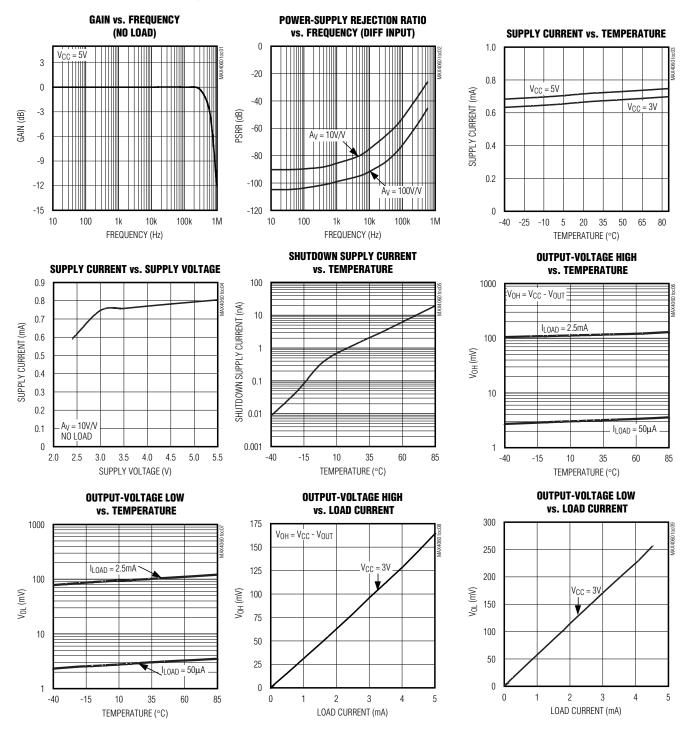
PARAMETER	SYMBOL	CO	NDITIONS	MIN	TYP	MAX	UNITS
		MAY 4000	$I_{BIAS} = 0.8$ mA to GND, $V_{CC} = 4.5$ V to 5.5 V	50	80		
Downey Cymphy Deigetion Detic	DCDD	MAX4060	I _{BIAS} = 0.8mA, V _{CC} = 5V + 100mV _{P-P} , f = 1kHz		70		۵D
Power-Supply Rejection Ratio	PSRR	MAY 4000	$I_{BIAS} = 0.5$ mA to GND, $V_{CC} = 2.4$ V to 5.5 V	50	74		dB
		MAX4062	$I_{BIAS} = 0.5 \text{mA}, V_{CC} = 3V + 100 \text{mV}_{P-P}, f = 1 \text{kHz}$		71		
DIGITAL INPUTS (SHDN for MAX4	061 and INT/A	UX for MAX4060/MAX	4062)				
Input Leakage Current	I _{IN}	V _{IN} = 0V or V _{CC}				±1	μΑ
Input-Voltage High	VINH			0.7 x V _C C			٧
Input-Voltage Low	VINL					0.3 x V _{CC}	V
Shutdown Enable Time	ton	MAX4061			10		μs
Shutdown Disable Time	toff	MAX4061			10		μs

Note 1: All specifications are 100% tested at T_A = +25°C. Specification limits over temperature (T_A = T_{MIN} to T_{MAX}) are guaranteed by design, not production tested.

Note 2: MAX4062 requires a 1µF capacitor from BIAS to ground.

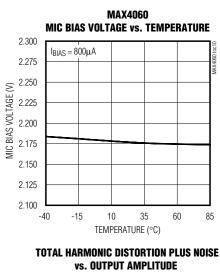
Typical Operating Characteristics

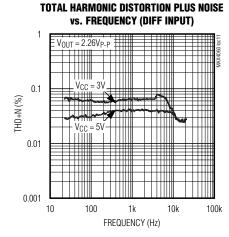
 $(V_{CC}=3V \text{ (MAX4061/MAX4062)}, V_{CC}=5V \text{ for MAX4060}, A_V=10V/V, R_L \ge 100k\Omega \text{ to } 1.5V, \overline{SHDN}=V_{CC} \text{ (MAX4061 only)}, T_A=+25^{\circ}C, unless otherwise noted.)$

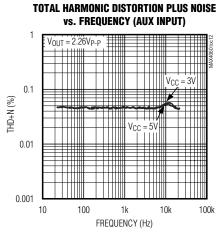


Typical Operating Characteristics (continued)

 $(V_{CC}=3V \text{ (MAX4061/MAX4062)}, V_{CC}=5V \text{ for MAX4060}, A_V=10V/V, R_L \ge 100k\Omega \text{ to } 1.5V, \overline{SHDN}=V_{CC} \text{ (MAX4061 only)}, T_A=+25^{\circ}C, unless otherwise noted.)}$







TOTAL HARMONIC DISTORTION PLUS NOISE

(DIFF INPUT, V_{CC} = 3V)

10

BW = 22kHz

f = 1kHz

0.01

0.001

0.05

1.0

1.5

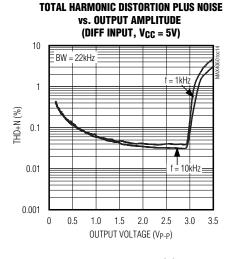
2.0

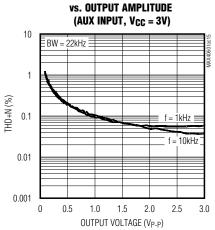
2.5

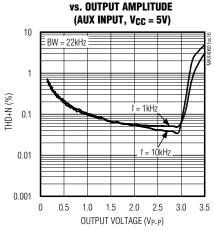
3.0

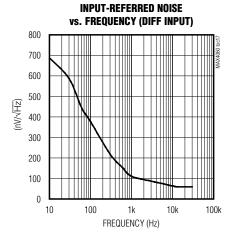
OUTPUT VOLTAGE (V_{P-P})

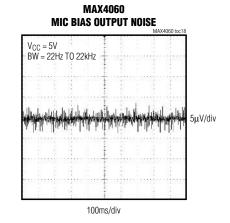
TOTAL HARMONIC DISTORTION PLUS NOISE











Typical Operating Characteristics (continued)

SMALL-SIGNAL TRANSIENT RESPONSE

FOR DIFF INPUT

10µs/div

LARGE-SIGNAL TRANSIENT RESPONSE

INPUT 5mV/div

OUTPUT 50mV/div

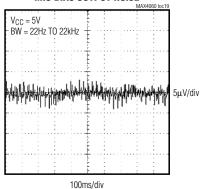
INPUT

100mV/div

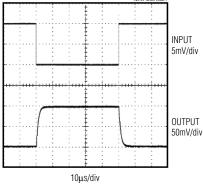
OUTPUT 1V/div

 $(V_{CC}=3V \text{ (MAX4061/MAX4062)}, V_{CC}=5V \text{ for MAX4060}, A_V=10V/V, R_L \ge 100k\Omega \text{ to } 1.5V, \overline{SHDN}=V_{CC} \text{ (MAX4061 only)}, T_A=+25^{\circ}C, unless otherwise noted.)$

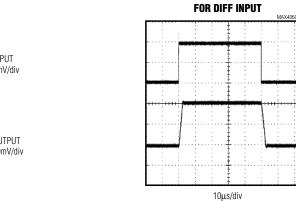
MAX4062 MIC BIAS OUTPUT NOISE



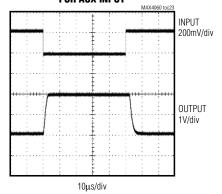
SMALL-SIGNAL TRANSIENT RESPONSE FOR AUX INPUT



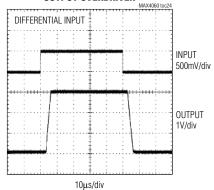
INT DECOMO



LARGE-SIGNAL TRANSIENT RESPONSE FOR AUX INPUT



OUTPUT OVERDRIVEN



Pin Description

	PIN		NAME	FUNCTION
MAX4060	MAX4061	MAX4062	NAME	FUNCTION
1	_	2	ĪNT/AUX	Internal (Differential) or Auxiliary (Single-Ended) Input Select. Drive INT/AUX low to select internal or high to select auxiliary microphone input.
2	3	3	OUT	Amplifier Output. OUT is high impedance when in shutdown mode.
3	_	_	BIAS	External Electret Microphone Capsule Bias Output. BIAS has a greater than $2k\Omega$ output impedance.
4	4	5	Vcc	Power Supply. Bypass the V _{CC} to GND with a 0.1µF capacitor.
5	5	6	IN+	Noninverting Differential Amplifier Input. AC-couple the audio signal into IN+.
6	6	7	IN-	Inverting Differential Amplifier Input. AC-couple the audio signal into IN
7	7	8	GND	Ground
8	_	9	AUX_IN	Single-Ended Input for Auxiliary Microphone. AC-couple the audio signal into AUX_IN.
_	1	1	G2	Gain-Selectable Input. Connect an external resistor between G1 and G2 to set the gain for the differential amplifier. (See <i>Adjustable Differential-Gain Setting</i> section.)
_	2	_	SHDN	Shutdown Input. Drive SHDN high for normal operation. Drive SHDN low for shutdown mode.
		4	BIAS	External Electret Microphone Capsule Bias Output Bypass BIAS with 1µF Capacitor to Ground
_	8	10	G1	Gain-Selectable Input. Connect an external resistor between G1 and G2 to set the gain for the differential amplifier.

Detailed Description

The MAX4060/MAX4061/MAX4062 are differential microphone preamplifiers providing high-quality audio, optimized for use in computer and mobile applications. These devices feature rail-to-rail outputs, very high power-supply rejection, and common-mode rejection, making them ideal for low-noise applications. The MAX4060/MAX4061/MAX4062 are particularly effective when layout constraints force the microphone amplifier to be physically remote from the ECM microphone and/or the rest of the audio circuitry.

The MAX4060/MAX4062 are capable of switching their output between the differential input and an inverting single-ended input. INT/AUX selects either the differential input or single-ended auxiliary input. In addition, the MAX4060/MAX4061 have an internal bias generator to bias the microphone in either differential or single-ended modes. The MAX4061 includes a complete

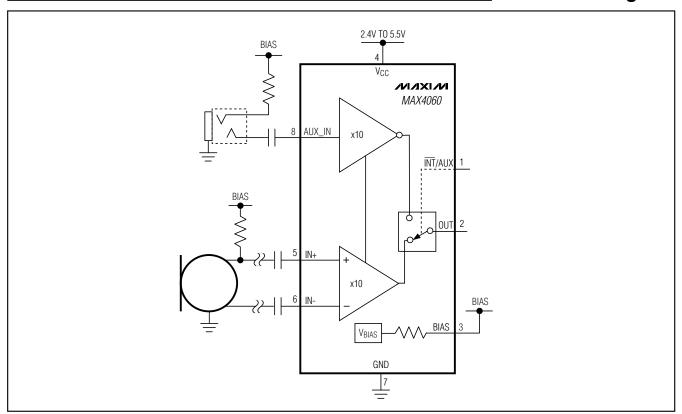
0.3µA shutdown mode for ultimate power savings. The differential gain of the MAX4061/MAX4062 is set with a single resistor connected between the G1 and G2 pins. The MAX4060 has a fixed gain of 10V/V.

Differential Input

The main microphone input is a low-noise, differential input structure. This is an almost essential element when faced with amplification of low-amplitude analog signals in digitally intense environments such as notebook PCs or PDAs. Used correctly, the advantages over a single-ended solution are:

- Better power-supply noise rejection.
- Less degradation from noise in PC board ground planes.
- The microphone and preamplifier can be placed physically further apart, easing PC board layout restrictions.

Functional Diagram



Fixed Differential Gain (MAX4060)

The MAX4060 has an internal fixed gain of 10V/V for its differential input. This feature simplifies design, reduces pin count, footprint, and eliminates external gain-setting resistors.

Adjustable Differential-Gain Setting

The MAX4061/MAX4062 allow the user to alter the gain to optimize the signal-to-noise ratio (SNR) of their system. The gain is set by a single external resistor (R_G) connected between the G1 and G2 pins, where:

$$R_G = 100k\Omega / (A_V - 1)$$

where Ay is the required voltage gain.

Hence, an $11.11k\Omega$ resistor yields a gain of 10V/V, or 20dB. Leaving the pins unconnected results in a gain of 1V/V. Gain for the MAX4061/MAX4062 is defined as:

$$AV = VOUT / (VIN+ - VIN-)$$

The resistor can be either fixed or variable, allowing the use of a digitally controlled potentiometer to alter the gain under software control.

Input Capacitors

The two differential microphone inputs and the single-ended auxiliary input of the MAX4060/MAX4061/MAX4062 have on-chip bias components, allowing the user to AC-couple any signals onto the input. The input resistance is $100 \text{k}\Omega$ (typ), so the capacitor size may be chosen accordingly to define the LF rolloff desired. This can be calculated as:

$$C_{IN} = 1 / (2\pi f_{CUT}R_{IN})$$

This assumes a low source impedance driving the inputs.

A further consideration for the differential input is the effect of these series input capacitors on low-frequency, common-mode rejection. Any mismatch in the values of these two capacitors degrades the CMRR at frequencies where the impedance of the capacitor is significant compared to the input resistance of the amplifier—this is usually most noticeable at low frequencies. One way to avoid the need for matched or tight tolerance capacitors is to deliberately oversize the values on the differ-

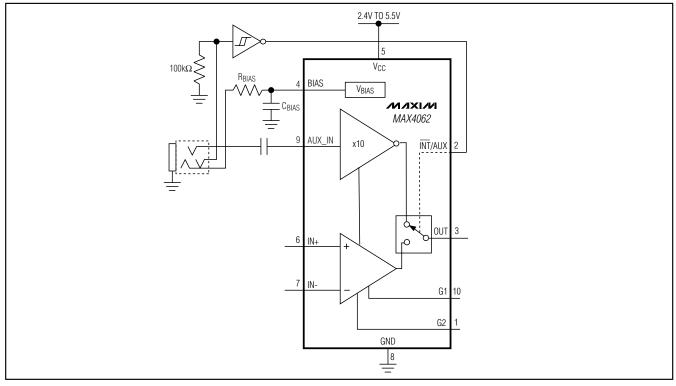


Figure 1. MAX4062 with Auxiliary Input Configuration

ential inputs and to set the lower 3dB point (f_{CUT}) of the amplifier by sizing the output capacitor appropriately.

The input impedance matching on the differential input is typically 1%, allowing input capacitor matching to be effective at improving low-frequency PSRR.

Common-Mode Rejection Ratio

The common-mode rejection ratio (CMRR) refers to the amount of rejection that the amplifier is capable of providing to any signal applied equally to the IN+ and IN-inputs. In the case of amplifying low-level microphone signals in noisy digital environments, it is a key figure of merit. In audio circuits, this is generally measured for VIN as an AC signal:

$CMRR(dB) = A_{DM} / A_{CM}$

where A_{DM} is the differential gain, A_{CM} is the common-mode gain.

Input voltages are sufficiently small such that the output is not clipped in either differential or common-mode application. The topology used in the MAX4061/MAX4062 means that the CMRR actually improves at higher differential gains—another advantage of using differential sensing.

Auxiliary Input

The auxiliary input is a single-ended input intended to be used with a jack-socket-type microphone input (Figure 1). Internal DC-bias components (as on the main inputs) allow the input signal to be AC-coupled. Mechanically switched jack sockets can be used in conjunction with the INT/AUX select pin, allowing the auxiliary microphone input to be automatically selected when a jack socket is inserted.

Microphone Bias Voltage MAX4060

The MAX4060 has a microphone bias voltage designed to comply with the Microsoft/Intel PC99/2001 audio standard. It features source impedance of greater than $2k\Omega$, and delivers more than 2V of bias when loaded with a current of $800\mu A$. This limits operation of this part to supplies between 4.5V to 5.5V (see Figure 2).

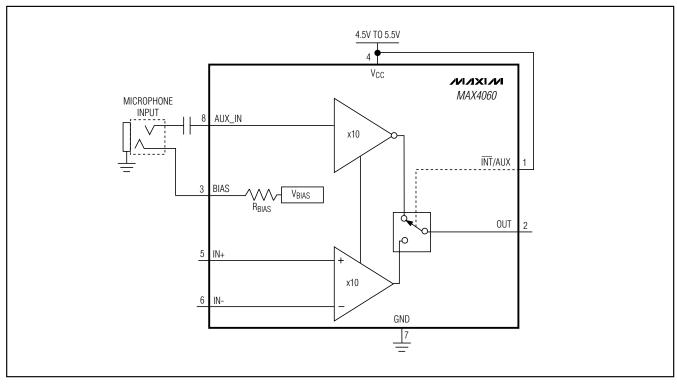


Figure 2. MAX4060 Used for Biasing a Microphone

MAX4061/MAX4062

The MAX4061/MAX4062 have a lower bias voltage and low-impedance outputs (optimum electret bias resistor can then be set externally). This gives a low-noise, flexible solution that can run from 2.4V to 5.5V, suitable for hand-held devices such as PDAs that typically have audio power supplies in the 3V region (see Figure 3).

In applications where the differential microphone is placed some distance from the MAX4060/MAX4061/MAX4062, using a remote differential bias scheme as shown in Figure 4 can provide improved noise rejection.

Output

MAX4060/MAX4061 DC Bias

The output voltage has a DC-bias voltage independent of the power supplies, resulting in superior PSRR performance. The MAX4061 output is high impedance when the part is in shutdown mode. AC-coupling the output into the next audio stage (e.g., CODEC) is recommended.

Applications Information

Shutdown Mode

The MAX4061 features a low-power, complete shutdown mode. When SHDN goes low, the supply current drops to 0.3µA, the output enters a high-impedance state, and the bias current to the microphone is switched off. Driving SHDN high enables the amplifier. SHDN should not be left floating.

Power Supplies and Layout

The MAX4060 operates from a 4.5V to 5.5V single supply and the MAX4061/MAX4062 operate from a 2.4V to 5.5V single supply. Bypass the power supply with a 0.1µF capacitor to ground. In systems where analog and digital grounds are available, the MAX4060/MAX4061 should be connected to the analog ground.

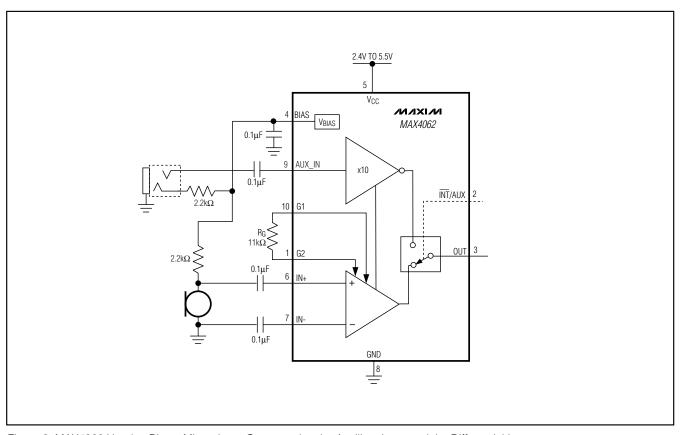


Figure 3. MAX4062 Used to Bias a Microphone Connected to the Auxiliary Input and the Differential Input

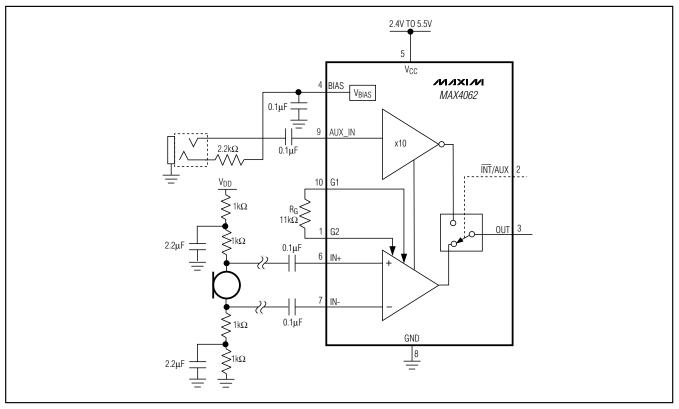
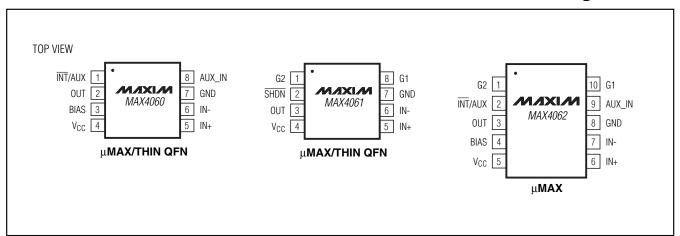
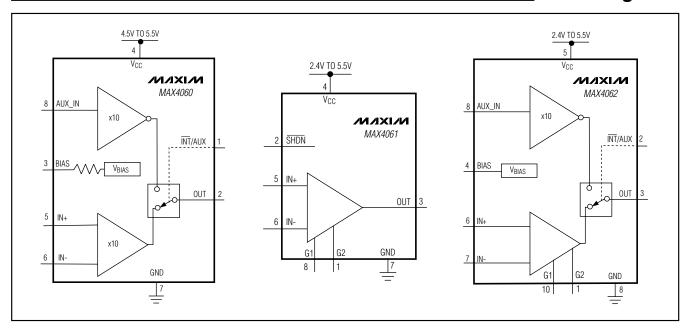


Figure 4. Remote Differential Microphone Bias Network Optimizes Noise Rejection in Long-Run, PC Board Traces

Pin Configurations



Block Diagrams



Selector Guide

PRODUCT*	AUXILIARY INPUT	DIFF INPUT GAIN	SINGLE-ENDED INPUT GAIN (dB)	MICROPHONE BIAS	SHUTDOWN MODE	SUPPLY VOLTAGE (V)
MAX4060	~	20dB	20	~	_	4.5 to 5.5
MAX4061	_	ADJ	_	_	V	2.4 to 5.5
MAX4062	~	ADJ	20	~	_	2.4 to 5.5

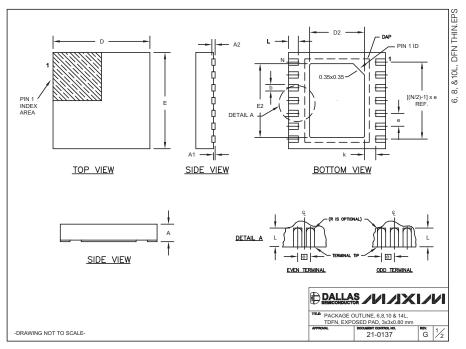
^{*}See Block Diagrams.

Chip Information

TRANSISTOR COUNT: 264 PROCESS: BICMOS

Package Information

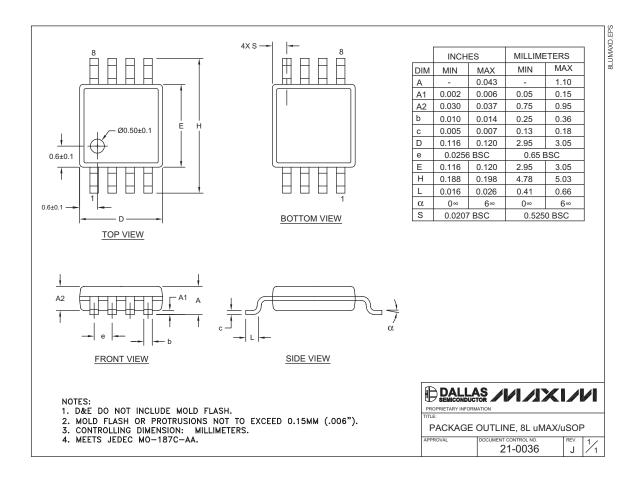
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



COMM	ON DIME	NSIONS								
SYMBOL	MIN.	MAX.	1							
A	0.70	0.80								
D	2.90	3.10								
E	2.90	3.10								
A1	0.00	0.05								
L k	0.20	0.40								
A2	_	25 MIN. 20 REF.	-							
	0.2		ı							
									1	
PACKAGE VAR	RIATIONS	3								
PKG. CODE	N	D2	E2	е	JEDEC SPEC	b	[(N/2)-1] x e	DOWNBONDS ALLOWED		
T633-1	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF	NO	1	
T633-2	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF	NO	1	
T833-1	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF	NO	1	
T833-2	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF	NO	1	
T833-3	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF	YES	1	
T1033-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF	NO	1	
T1433-1	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF	YES	1	
T1433-2	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF	NO	1	
NOTES: 1. ALL DIMENS 2. COPLANARIT 3. WARPAGE S 4. PACKAGE LI SPECIAL C	Y SHALL HALL NO ENGTH/F HARACTE ONFORMS	NOT EXCE T EXCEED PACKAGE W RISTIC(S).	ED 0.08 n 0.10 mm. DTH ARE C	onsidered	AS IENSIONS "D2" ANI) "E2",	Φı	DALLAS	/N/1X	

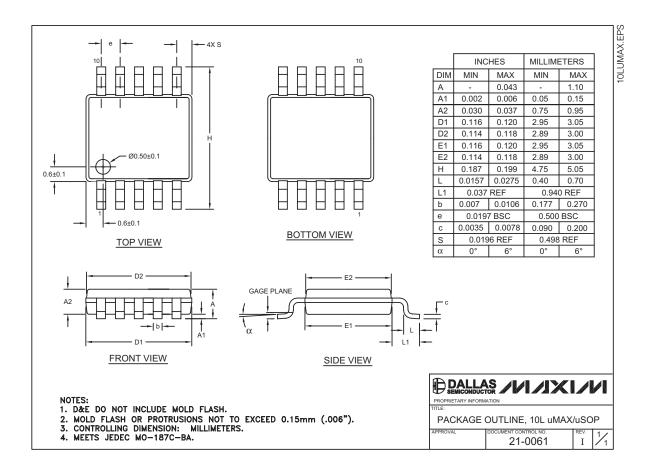
Package Information (continued)

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Package Information (continued)

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