





20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	RDS(ON) Max	I _{D Max} T _A = +25°C
001/	13.5mΩ @ V _{GS} = 4.5V	11A
20V	15.5mΩ @ V _{GS} = 2.5V	10.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

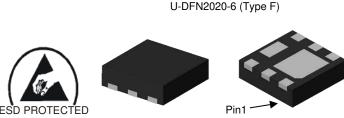
- General Purpose Interfacing Switch
- **Power Management Functions**
- Load Switch

Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

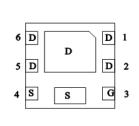
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)

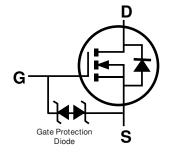


Top View

Bottom View



Pin Out **Bottom View**



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Reel Size (inches)	Quantity Per Reel
DMN29M9UFDF-7	U-DFN2020-6 (Type F)	7	3,000
DMN29M9UFDF-13	U-DFN2020-6 (Type F)	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



OV = Product Type Marking Code YM = Date Code Marking Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



OV = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 1 = 2021) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Cod	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Χ	Υ	Z



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 6) Vgs = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lo	11 8.8	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	50	Α
Maximum Body Diode Continuous Current (Note 6)	ls	2.9	Α		
Avalanche Current (Note 7) L = 0.1mH	valanche Current (Note 7) L = 0.1mH		las	3.4	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	0.6	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	95	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	2	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		RθJA	60	°C/W
Thermal Resistance, Junction to Case (Note 6)	Rejc	8.4	- C/VV	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS		_	1	μΑ	$V_{DS} = 16V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 10V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			10.4	13.5		$V_{GS} = 4.5V$, $I_D = 5A$
Static Drain-Source On-Resistance	RDS(ON)		11.8	15.5	mΩ	$V_{GS} = 2.5V, I_D = 3A$
			14.6	24		$V_{GS} = 1.8V, I_D = 1A$
Diode Forward Voltage	VsD	_	0.59	1.2	V	$V_{GS} = 0V, I_{S} = 0.5A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	655	_	pF	., ., ., ., .,
Output Capacitance	Coss	_	114	_	pF	$V_{DS} = 8V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	72	_	pF	1 = 1.0Wil IZ
Gate Resistance	Rg		6.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (VGS = 4.5V)	Qg		7.3	_	nC	
Total Gate Charge (VGS = 10V)	Qg	_	14.6	_	nC	\/
Gate-Source Charge	Q _{gs}		2.1	_	nC	$V_{DS} = 8V$, $I_D = 5A$
Gate-Drain Charge	Qgd	_	0.8	_	nC	1
Turn-On Delay Time	td(on)	_	6	_	ns	
Turn-On Rise Time	t _R	_	2	_	ns	$V_{GS} = 4.5V, V_{DD} = 8V,$
Turn-Off Delay Time	tD(OFF)	_	48	_	ns	$R_G = 1.8\Omega$, $I_D = 5A$
Turn-Off Fall Time	tF	_	22	_	ns	

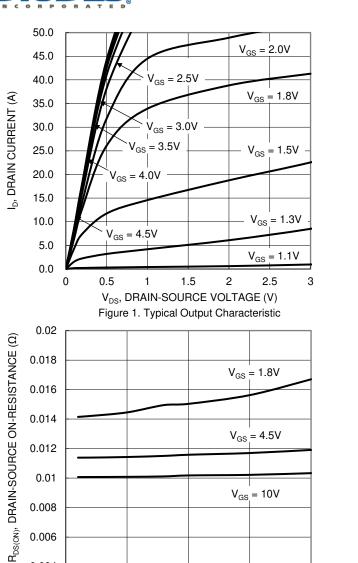
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.



0.01

0.008



0.006 0.004 0 5 10 15 I_D , DRAIN-SOURCE CURRENT (A) Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

 $V_{GS} = 10V$

20

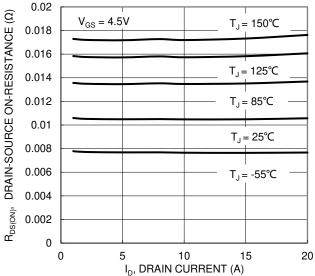


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

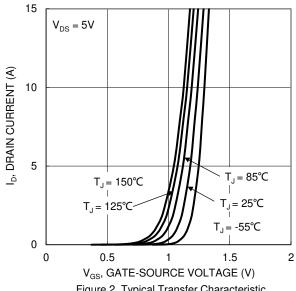
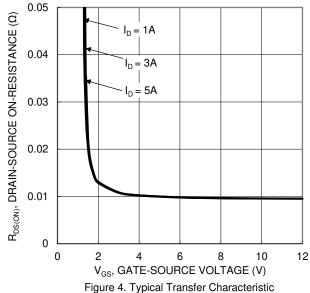


Figure 2. Typical Transfer Characteristic

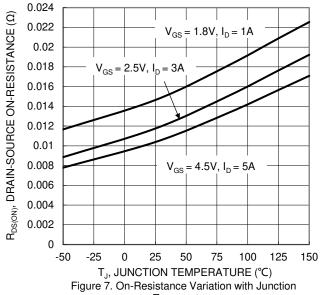


1.8 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) $V_{GS} = 4.5V, I_D = 5A$ 1.6 $V_{GS} = 2.5V, I_D = 3A$ 1.4 1.2 1 $V_{GS} = 1.8V, I_{D} = 1A$ 8.0 0.6 -50 -25 0 25 50 100 125 150 75 T_J, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Junction Temperature







Temperature

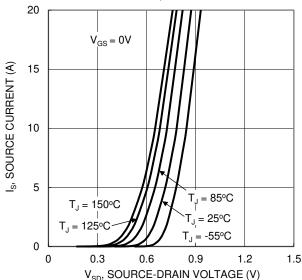


Figure 9. Diode Forward Voltage vs. Current

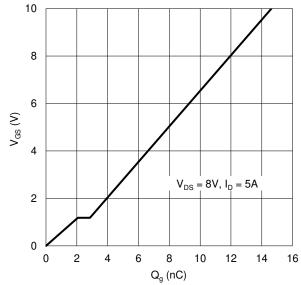


Figure 11. Gate Charge

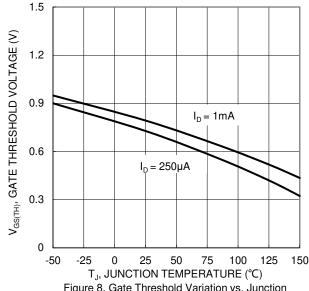


Figure 8. Gate Threshold Variation vs. Junction Temperature

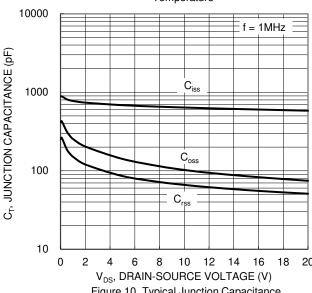


Figure 10. Typical Junction Capacitance

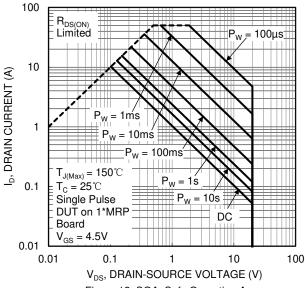


Figure 12. SOA, Safe Operation Area



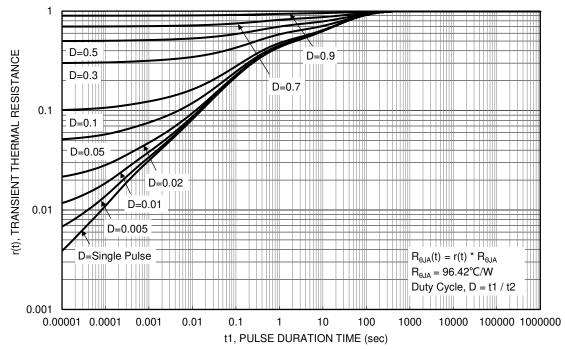


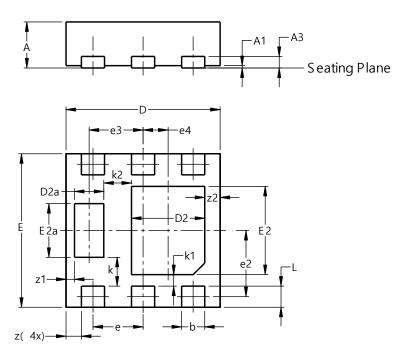
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

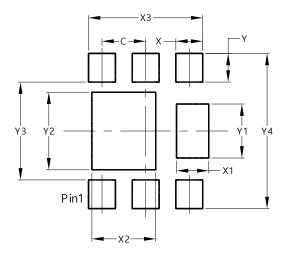


U-DFN2020-6								
	(Type F)							
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е		0.65 BS	O					
e2	().863 BS	SC					
e3		0.70 BS	С					
e4	().325 BS	SC					
k		0.37 BS	С					
k1		0.15 BS	С					
k2		0.36 BS						
L	0.225	0.325	0.275					
Z	0.20 BSC							
z 1	().110 BS	SC					
z2		0.20 BS						
All C)imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value
Difficusions	(in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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