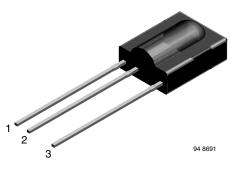


Vishay Semiconductors

## **IR Receiver Modules for Remote Control Systems**



## LINKS TO ADDITIONAL RESOURCES



### DESCRIPTION

The TSOP311.., TSOP313.., and TSOP315.. series are miniaturized receivers for infrared remote control systems. A PIN diode and a preamplifier are assembled on a lead frame, the epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP313.. series devices are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. AGC3 may also suppress some data signals if continuously transmitted.

The TSOP311.. series are provided primarily for compatibility with old AGC1 designs. New designs should prefer the TSOP313.. series containing the newer AGC3. The TSOP315.. series contain a very robust AGC5. This series should only be used for critically noisy environments.

These components have not been qualified according to automotive specifications.

### **FEATURES**

- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization:
   for alefinitions of according to the second s

for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **MECHANICAL DATA**

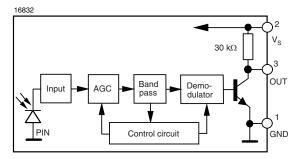
## Pinning:

1 = GND, 2 = V<sub>S</sub>, 3 = OUT

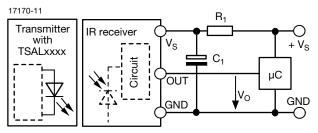
### **ORDERING CODE**

TSO.1. - 1000 pieces in tubes

### **BLOCK DIAGRAM**



### **APPLICATION CIRCUIT**



 $R_1$  and  $C_1$  recommended to reduce supply ripple for  $V_S < 2.8 V$ 





FREE

GREEN

(5-2008)

Rev. 1.7, 02-Aug-2021



## www.vishay.com

## **Vishay Semiconductors**

PARTS TAE	BLE				
AGC		LEGACY, FOR SHORT BURST REMOTE CONTROLS (AGC1)	NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	30 kHz	TSOP31130	TSOP31330	TSOP31530	
	33 kHz	TSOP31133	TSOP31333	TSOP31533	
Carrier	36 kHz	TSOP31136	TSOP31336 <sup>(1)(6)</sup>	TSOP31536	
frequency	38 kHz	TSOP31138	TSOP31338 <sup>(2)(3)(4)(5)</sup>	TSOP31538	
	40 kHz	TSOP31140	TSOP31340	TSOP31540	
	56 kHz	TSOP31156	TSOP31356	TSOP31556	
Package		Cast			
Pinning		1 = GND, 2 = V <sub>S</sub> , 3 = OUT	1 = GND, 2 = V <sub>S</sub> , 3 = OUT	1 = GND, 2 = V <sub>S</sub> , 3 = OUT	
Dimensions (mm)		10.0 W x 12.5 H x 5.8 D			
Mounting		Leaded			
Application		Remote control			
Best remote control code		<sup>(1)</sup> MCIR <sup>(2)</sup> Mitsubishi <sup>(3)</sup> RECS-80 Code <sup>(4)</sup> r-map <sup>(5)</sup> XMP-1, XMP-2 <sup>(6)</sup> RCMM			
Special options		Low voltage option: <u>www.vishay.com/doc?82382</u>			

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage (pin 2)		Vs	-0.3 to +6	V
Supply current (pin 2)		I <sub>S</sub>	3	mA
Output voltage (pin 3)		Vo	-0.3 to (V <sub>S</sub> + 0.3)	V
Output current (pin 3)		Ι <sub>Ο</sub>	5	mA
Junction temperature		Тj	100	°C
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW
Soldering temperature	$t \le 10$ s, 1 mm from case	T <sub>sd</sub>	260	°C

#### Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND OPTICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply current (pin 2)	$E_v = 0, V_S = 3.3 V$	I <sub>SD</sub>	0.27	0.35	0.45	mA
	E <sub>v</sub> = 40 klx, sunlight	I <sub>SH</sub>	-	0.45	-	mA
Supply voltage		VS	2.5	-	5.5	V
Transmission distance	$E_v = 0$ , test signal see Fig. 1, IR diode TSAL6200, I <sub>F</sub> = 50 mA	d	-	24	-	m
Output voltage low (pin 3)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V <sub>OSL</sub>	-	-	100	mV
Minimum irradiance	$\begin{array}{c} \mbox{Pulse width tolerance:} \\ t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o, \mbox{test signal} \\ \mbox{see Fig. 1} \end{array}$	E <sub>e min.</sub>	-	0.12	0.25	mW/m <sup>2</sup>
Maximum irradiance	$\begin{array}{c} t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o, \text{ test signal} \\ \text{see Fig. 1} \end{array}$	E <sub>e max.</sub>	30	-	-	W/m <sup>2</sup>
Directivity	Angle of half transmission distance	Φ1/2	-	± 45	-	0

Rev. 1.7, 02-Aug-2021



Vishay Semiconductors

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

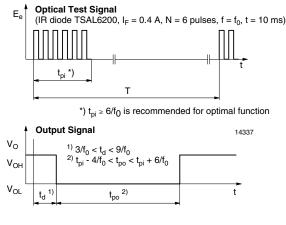


Fig. 1 - Output Active Low

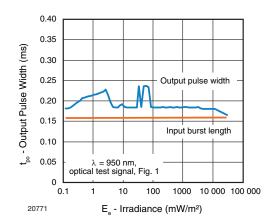


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

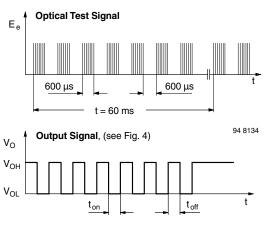


Fig. 3 - Output Function

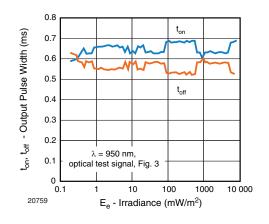


Fig. 4 - Output Pulse Diagram

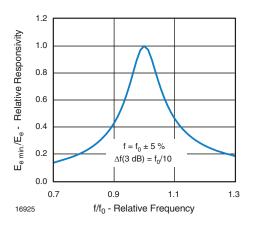


Fig. 5 - Frequency Dependence of Responsivity

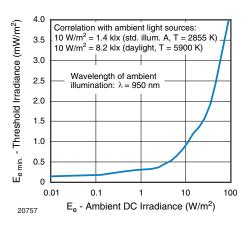


Fig. 6 - Sensitivity in Bright Ambient

Rev. 1.7, 02-Aug-2021

Document Number: 82493

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <a href="http://www.vishay.com/doc?91000">www.vishay.com/doc?91000</a>



**Vishay Semiconductors** 

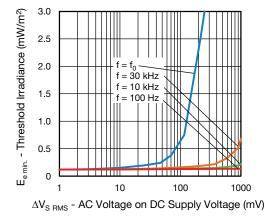


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

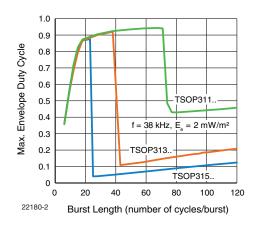


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

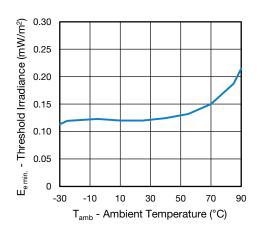


Fig. 9 - Sensitivity vs. Ambient Temperature

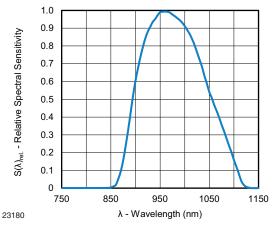


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

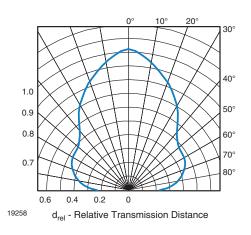


Fig. 11 - Horizontal Directivity

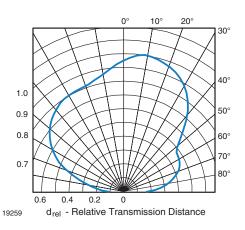


Fig. 12 - Vertical Directivity

Rev. 1.7, 02-Aug-2021

4

Document Number: 82493

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <a href="http://www.vishay.com/doc?91000">www.vishay.com/doc?91000</a>



Vishay Semiconductors

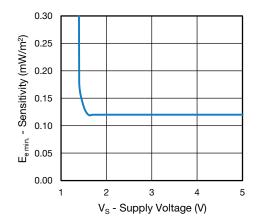


Fig. 13 - Sensitivity vs. Supply Voltage



## **Vishay Semiconductors**

## SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 14 or Fig. 15).

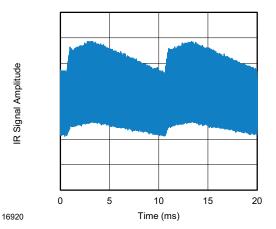


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

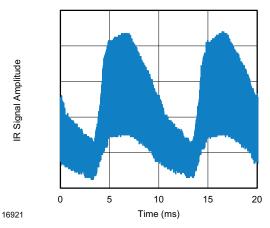


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

	TSOP311	TSOP313	TSOP315
Minimum burst length	6 cycles/burst	6 cycles/burst	6 cycles/burst
After each burst of length A gap time is required of	6 to 70 cycles ≥ 10 cycles	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 1.2 x burst length	35 cycles > 6 x burst length	24 cycles > 25 ms
Maximum number of continuous short bursts/second	2000	2000	2000
MCIR code	Yes	Preferred	Yes
RCMM code	Yes	Preferred	Yes
XMP-1, XMP-2 code	Yes	Preferred	Yes
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 14)	Complex disturbance patterns are suppressed (example: signal pattern of Fig. 15)	Critical disturbance patterns are suppressed, e.g. highly dimmed LCDs

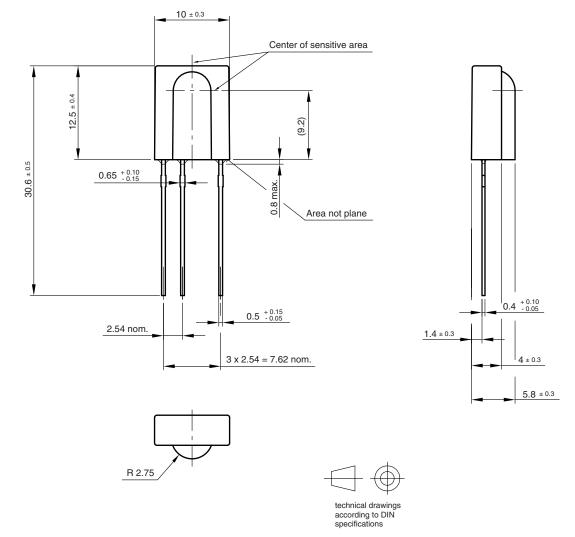
#### Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP312.., TSOP314..



Vishay Semiconductors

## **PACKAGE DIMENSIONS** in millimeters



Drawing-No.: 6.550-5095.01-4 Issue: 20; 15.03.10 96 12116



Vishay

## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.