TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

# 2SK2962

Chopper Regulator, DC-DC Converter and Motor Drive Applications

4-V gate drive

• Low drain-source ON resistance :  $R_{DS (ON)} = 0.5 \Omega (typ.)$ 

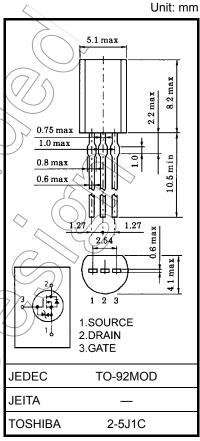
• High forward transfer admittance : |Y<sub>fs</sub>| = 1.2 S (typ.)

• Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 100 V)

• Enhancement mode :  $V_{th} = 0.8$  to 2.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	(Unit )
Drain-source voltage		$V_{DSS}$	100	$\bigvee V$
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	100	$\checkmark$
Gate-source voltage		V <sub>GSS</sub>	±20	$\rightarrow$ v
Drain current	DC (Note 1)	ID		Α
	Pulse (Note 1)	I <sub>DP</sub>	3	A
Drain power dissipation	1	PD	0.9	/(w
Single pulse avalanche	e energy (Note 2)	E <sub>A</sub> \$	137	m3
Avalanche current		JAR \	1 (	<u>Α</u>
Repetitive avalanche e	nergy (Note 3)	(EAR))	0.09	/mJ
Channel temperature		Tch	150	(°C)
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C



Weight: 0.36 (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics Symbol	Max	Unit
Thermal resistance, channel to ambient Rth (ch-a)	138	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 221 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 1 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

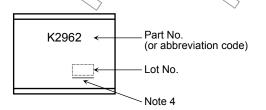
#### **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage co	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ	
Drain cut-off cu	ırrent	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ	
Drain-source b	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	100	_	_	V	
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V	
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 0.5 A		0.65	0.95	Ω	
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A	) />	0.5	0.7		
Forward transfe	er admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A	0.6	1.2	_	S	
Input capacitance		C <sub>iss</sub>			140	_		
Reverse transfer capacitance		C <sub>rss</sub>	C <sub>rss</sub> V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	20	_	pF	
Output capacitance		Coss			45			
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> OV I ID=0.5A V <sub>OUT</sub>	- (	8	/> I		
	Turn-on time	t <sub>on</sub>	$R_{L} = 100 \Omega$		13	) _	no	
	Fall time	t <sub>f</sub>	v <sub>DD</sub> ≒ <sub>50V</sub>		45	_	ns	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{w}} = 10 \mu \mathbf{s}$	) –	175	_		
Total gate charged plus gate-drain	ge (gate-source )	Qg			6.3			
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 1 \text{ A}$	_	4.3	_	nC	
Gate-drain ("miller") Charge		Qgd		_	2	_		

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	1	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	3	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 50 A / μs	_	80	_	ns
Reverse recovery charge	Qrr	iDR = 1 A, VGS = 0 V, αiDR / αt = 30 A / μs	_	140	_	nC

## Marking

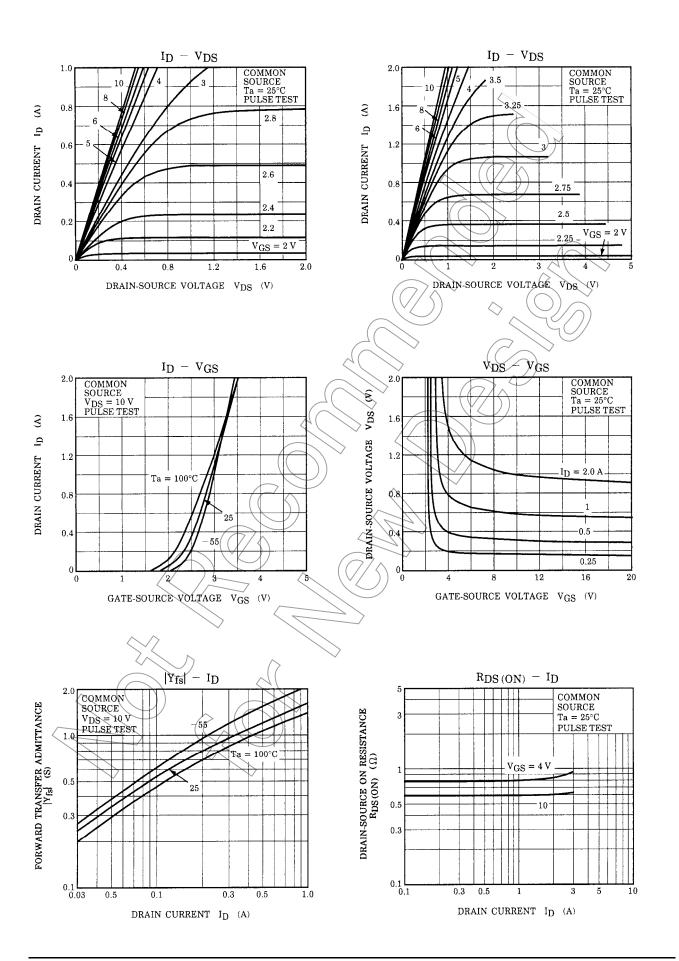


Note 4: A line under a Lot No. identifies the indication of product Labels.

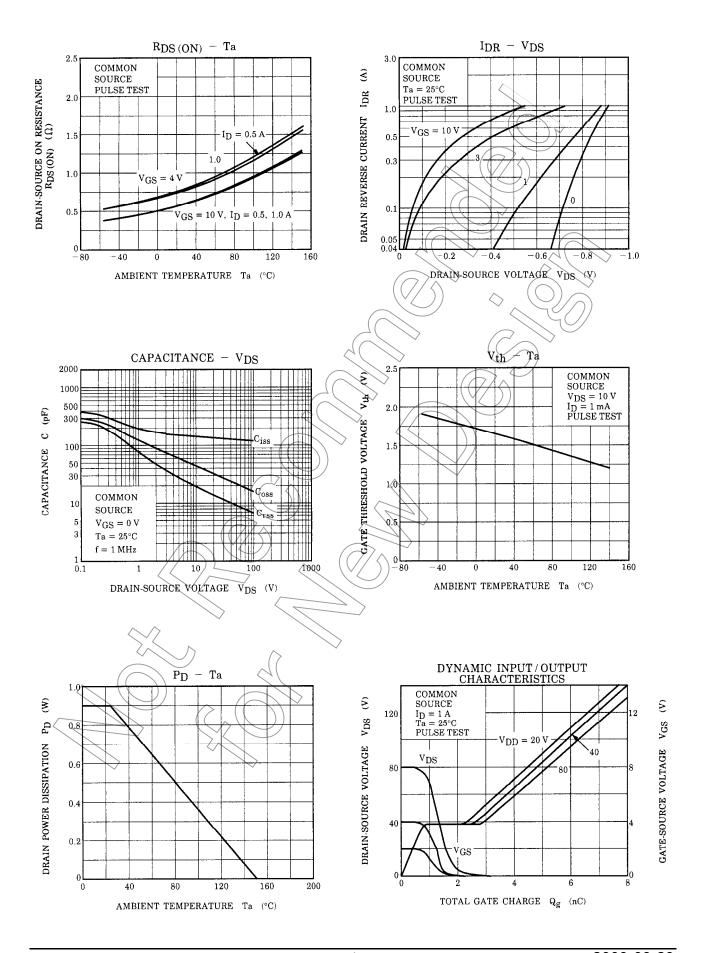
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

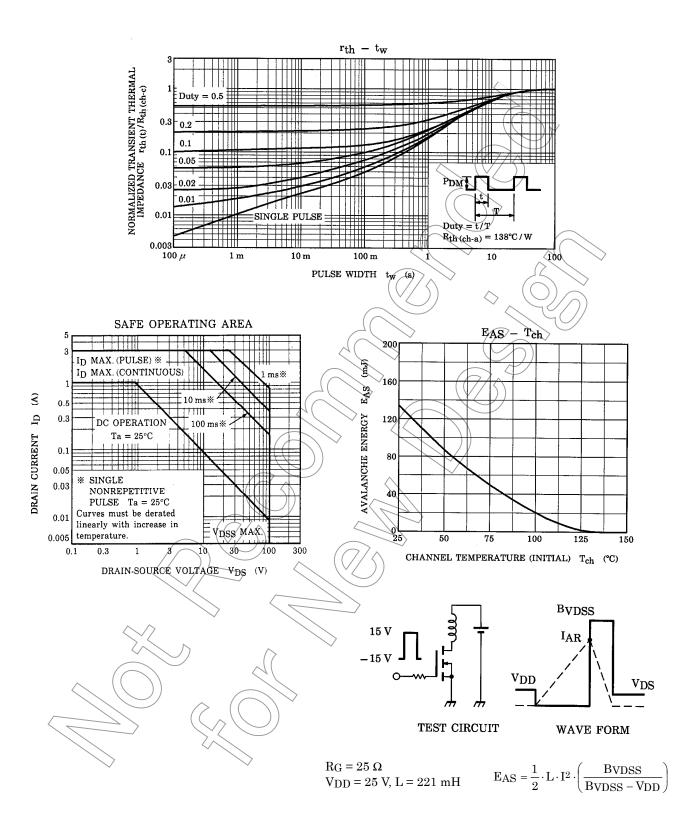
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2 2009-09-29



3 2009-09-29





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