



#### Overview

Gate driver 2DMBxxxxxCC is a dual channel gate driver designed for IGBT and SiC MOSFET. The high breakdown voltage and low parasitic capacitance make it suitable for gate drives such as SiC MOSFET and IGBT.

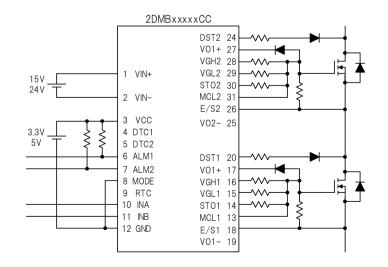


- ·Ideal for drive of IGBT and SiC MOSFET
- •Gate voltage : +15V/-15V
- ·ALL-IN-ONE (Built-in isolated DC / DC converter and gate drive circuit)
- ·Low parasitic capacitance (about 12pF); highly resistant to common-mode noise.
- ·Fast response : about 100nsec(typ)
- ·The gate drive circuit used a isolator.
- ·Input-to-Output dielectric withstand voltage : AC5000V
- ·Output CH1-to-Ouput CH2 dielectric withstand voltage : AC4000V
- Input-to-Output insulation distance : 14mm (clearance.creepage)
- ·Output CH1-to-Output CH2 insulation distance : 7mm (clearance), 12mm(creepage)
- $\cdot$ DC/DC converter input voltage :13 $\sim$ 28V
- $\cdot$ Signal input voltage : 3.3V,5V
- $\cdot \text{Overload}$  protection (DC/DC converter)
- $\cdot$ Overheat protection (DC/DC converter)
- ·Half bridge mode (Gate drive circuit)
- ·Desaturation protection (Gate drive circuit)
- ·Soft turn-off function (Gate drive circuit)
- ·Fault signal output function (Gate drive circuit)
- ·Miller clamp function (Gate drive circuit)
- ·Under-voltage lockout(UVLO) (Gate drive circuit)
- ·Safety satudards : UL508 (certification pending)

#### Application

Industrial inverter, power conditioner, etc  $\cdots$ 

### ■Ciruit Image



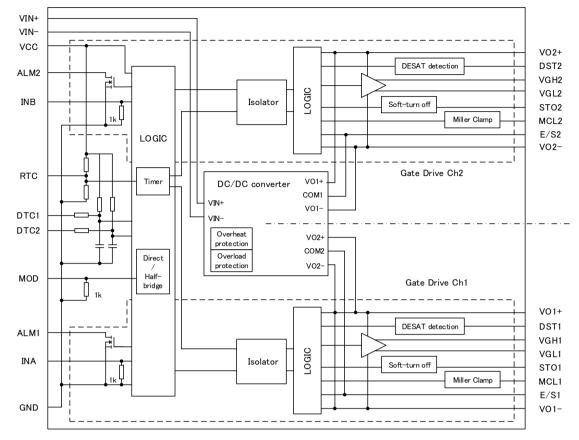






# 2DMB51507CC Datasheet

## ■Internal Block Diagram



### ■Pin Connection

1					
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input			
Pin No.	Name		Function
1	VIN+	Common	Power supply for DC/DC converter(+)
2	VIN-	Common	Power supply for DC/DC converter(-)
3	VCC	-	Power supply for drive circuit
4	DTC1	1	Dead time adjustment
5	DTC2	2	Dead time adjustment
6	ALM1	1	Alarm signal output
7	ALM2	2	Alarm signal output
8	MOD	-	Mode select
9	RTC	-	Recovery time of protection circuit con
10	INA	1	Control input A
11	INB	2	Control input B
12	GND	-	Ground for drive circuit

Outpu	t		
Pin No.	Name	CH	Function
13	MCL1	1	Miller clamp pin
14	ST01	1	Soft turn off pin
15	VGL1	1	Gate OFF side pin
16	VGH1	1	Gate ON side pin
17	V01+	1	DC/DC converter output pin
18	E/S1	1	Emitter · source connection pin
19	V01-	1	DC/DC converter output pin
20	DST1	1	Desaturation protection pin
21	NONE	I	None
22	NONE	-	None
23	NONE	I	None
24	DST2	2	Desaturation protection pin
25	V02-	2	DC/DC converter output pin
26	E/S2	2	Emitter · source connection pin
27	V01+	2	DC/DC converter output pin
28	VGH2	2	Gate ON side pin
29	VGL2	2	Gate OFF side pin
30	ST02	2	Soft turn off pin
31	MCL2	2	Miller clamp pin



# ■I/O Condition Table

No.	Status			Inp	out				Out	tput(C	H1)			Out	tput(Cl	H2)	
INU.		VO+	DST1	DST2	MOD	INA	INB	ALM1	VGH1	VGL1	ST01	MLC1	ALM2	VGH2	VGL2	ST02	MLC2
1	V01+,V02+ UVLO	UVLO	Х	Х	Х	Х	Х	L	Hi-Z	Hi-Z	Hi-Z	Hi-Z	L	Hi-Z	Hi-Z	Hi-Z	Hi-Z
2		0	Х	L	L	Х	L	Х	Х	Х	Х	Х	Hi-Z	Hi-Z	L	L	L
3	Normal	0	Х	L	L	Х	Н	Х	Х	Х	Х	Х	Hi-Z	Н	Hi-Z	Hi-Z	Hi-Z
4	operation	0	L	Х	L	L	Х	Hi-Z	Hi-Z	L	L	L	Х	Х	Х	Х	Х
5		0	L	Х	L	Н	Х	Hi-Z	Н	Hi-Z	Hi-Z	Hi-Z	Х	Х	Х	Х	Х
6	Normal	0	L	L	Η	Х	L	Hi-Z	Hi-Z	L	L	L	Hi-Z	Hi-Z	L	L	L
7	operation	0	L	L	Η	L	H	Hi-Z	Hi-Z	L	L	L	Hi-Z	Η	Hi-Z	Hi-Z	Hi–Z
8	(HBM)	0	L	L	Η	Η	H	Hi-Z	Η	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Hi-Z	L	L	L
9		0	L	Hi-Z	L	Х	L	Hi-Z	Х	Х	Х	Х	L	Hi-Z	Hi-Z	L	L
10	Desaturation	0	L	Hi-Z	L	Х	H	Hi-Z	Х	Х	Х	Х	L	Hi-Z	Hi-Z	L	L
11	protection1	0	L	Hi-Z	Η	L	H	Hi-Z	Hi-Z	L	L	L	L	Hi-Z	Hi-Z	L	
12		0	L	Hi-Z	Η	Η	H	Hi-Z	Η	Hi-Z	Hi-Z	Hi-Z	L	Hi-Z	Hi-Z	L	L
13		0	Hi-Z	L	L	L	Х	L	Hi-Z	Hi-Z	L	L	Hi-Z	Х	Х	Х	Х
14	Desaturation	0	Hi-Z	L	L	Η	Х	L	Hi-Z	Hi-Z	L	L	Hi-Z	Х	Х	Х	Х
15	protection2	0	Hi-Z	L	Н	L	Η	L	Hi-Z	Hi-Z	L	L	Hi-Z	Н	Hi-Z	Hi-Z	Hi-Z
16		0	Hi-Z	L	Н	Η	Н	L	Hi-Z	Hi-Z	L	L	Hi-Z	Hi-Z	L	L	L

 $\bigcirc$  : VO UVLO > UVLO, X : Don't care



## ■Absolute Maximum Ratings

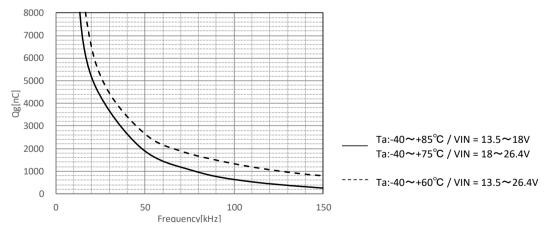
ltem	Symbol	Min	Max	Unit	Conditions · Note
Input voltage for DC/DC converter	V <sub>IN</sub>	-0.3	28	Vdc	Between VIN+ to VIN-
Input-side signal voltage	$V_{CC,}V_{SG}$	-0.3	5.5	V	VCC, RTC, MOD, INA, INB
input-side signal voltage	$V_{SG}$	-0.3	28	V	ALM1, ALM2
Input-side signal maximum current	$I_{SG}$	-	5	mA	ALM1, ALM2
DESAT pin input voltage	$V_{\text{DESAT}}$	-0.3	$V_{GH}$ +0.3	V	
Miller clamp pin input voltage	$V_{\text{CLAMP}}$	$V_{GL}$ -0.3	$V_{GH}$ +0.3	V	
Maximum gate current	I <sub>GPEAK</sub>	-	43	А	Guaranteed by design
DC/DC converter output power	P <sub>OUT</sub>	-	3.3	W	Per output circuit
Switching frequency	F <sub>SW</sub>	-	150	kHz	
Operating temperature range	T <sub>OP</sub>	-40	85	C°	See the derating curve
Operating humidity	RH <sub>OP</sub>	20	95	%RH	No condensation
Storage temperature range	T <sub>STG</sub>	-40	90	°C	
Storage humidity	$\mathrm{RH}_{\mathrm{STG}}$	5	95	%RH	No condensation

## ■Recommended Operating Conditons

ltem	Symbol	Min	Max	Unit	Conditions · Note
Input voltage range for DC/DC converter	V <sub>IN</sub>	13.5	26.4	Vdc	
Input-side signal voltage range	$V_{CC,}V_{SG}$	3	5.5	Vdc	
Driver circuit number	Ν	-	2	-	
Logic high level input voltage	$V_{SGH}$	$V_{CC}  imes 0.7$	-	V	MOD, INA, INB
Logic low level input voltage	$V_{SGL}$	-	$V_{\text{CC}}  imes 0.3$	V	MOD, INA, INB
Source current of control signal	I <sub>SG</sub>	5	-	mA	MOD, INA, INB $V_{SG}$ =5V
Maximum gete drive capability ( 150 kHz)	Q <sub>MAX</sub>	-	210	nC	I <sub>GAVE</sub> = 110mA Reference value
Maximum gete drive capability ( 30 kHz)	Q <sub>MAX</sub>	-	5000	nC	I <sub>GAVE</sub> = 110mA Reference value
Maximum gete charge amount	Q <sub>G</sub>	-	8000	nC	
Minimum input pulse width	t <sub>INMSK</sub>	80	-	ns	

## ■Derating Curve

Reduce the output power according to the following derating table.





# ■Electrical Specification (Vin=24V, Vcc=5V.Ta=25°C, Unless otherwise specified)

DC/DC converter block

ltem	Symbol	Min	Тур	Max	Unit	Conditions.Note
Start-up voltage	$V_{START}$	-	-	13	V	
Max input current	I <sub>INMAX</sub>	-	0.45	-	А	Fsw=30kHz /Ta:60°C
Standby power	P <sub>STBY</sub>	-	1.4	-	W	No load

### Gate drive block

	ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
Logic							•
Logic high lev	vel input voltage	$V_{\text{SGH}}$	$V_{\text{CC}} \ x0.7$	-	-	V	MOD, INA, INB
Logic low lev	el input voltage	$V_{SGL}$	-	-	$V_{\text{CC}} \ x0.3$	V	INA, INB
Logic pull-do	wn resistance	$R_{SGD}$	-	1000	-	Ω	INA, INB
Output			•				•
Gate ON side	pin voltage	$V_{GH}$	14	15	16	V	No load
Gate OFF sid	e pin voltage	$V_{GL}$	-16	-15	-14	V	No load
Miller clamp v	oltage	$V_{CLAMP}$	-	1.2	-	V	$I_{CLAMP}$ =500mA, Guaranteed by design
Miller clamp C	N threshold voltage	V <sub>CLPON</sub>	-	$V_{GL}$ +2	-	V	
Dolov timo	Turn ON time	t <sub>PON</sub>	-	100	-	ns	
Delay time	Turn OFF time	t <sub>POFF</sub>	-	100	-	ns	
Dead time	÷	t <sub>DEAD</sub>	-	4.1	-	US	Half bridge mode
Common-mod	le transient immunity	CMTI	-	-	70	kV/us	

## Protection

DC/DC converter block

ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
Overload protection	-	8.8	-	-	W	Auto recovery
Overheat protection	-	120	-	150	°C	Auto recovery,CASE temperature

#### Gate drive block

ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
Gate ON side UVLO ON voltage	$V_{\text{UVLOGHH}}$	13.2	13.5	13.8	V	
Gate ON side UVLO OFF voltage	$V_{\rm UVLOGHL}$	12.2	12.5	12.8	V	
DESAT charge current	IDESAT	200	240	280	uA	
DESAT detection voltage	V <sub>SD</sub>	6.0	6.35	7.0	V	
DESAT detection filter time	t <sub>DSTFIL</sub>	_	300	-	ns	Guaranteed by design
DESAT detection time	t <sub>DSTOUT</sub>	-	350	-	ns	Guaranteed by design
Alarm signal output L voltage	V <sub>ALML</sub>	_	-	0.5	V	I <sub>ALM</sub> =5mA
Alarm signal output time	t <sub>ALM</sub>	-	550	-	ns	
Restart time	t <sub>restart</sub>	_	110	-	ms	
Soft turn off duration	t <sub>sto</sub>	-	4	_	US	



## $\blacksquare$ Insulation

ltem	Specification	Conditions · Note
Between Input-Output		
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA
Insulation resistance	$100M\Omega$ or more	DC500V
Minimum clearance distances	14mm	
Minimum creepage distances	14mm	
Partial discharge extinction volt.	1768Vpeak or more	According to EN50178/IEC 60270
Between CH1-CH2		
Dielectric withstand voltage	AC4000V	1min, Cutoff 2mA
Insulation resistance	$100M\Omega$ or more	DC500V
Minimum clearance distances	7mm	
Minimum creepage distances	12mm	
Partial discharge extinction volt.	1700Vpeak or more	According to EN50178/IEC 60270





#### ■Pin Function

·Vin(+), Vin(-) (Power supply pin for DC/DC converter)

·VCC(Power supply pin for drive curcuit)

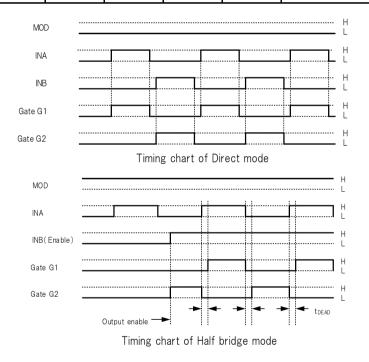
•GND(Ground pin for drive curcuit)

·MOD, INA, INB(Mode switching pin, Control input pin)(\*Note1)

The INA, INB and MOD pin is a pin used to determine output logic. Direct mode / Half bridge mode can be switched by MOD pin.

In Half bridge mode, it functions as INA: gate signal, INB: enable signal.

Mode	Gate G2	Gate G1	INB	INA	MOD
	Х	L	Х	L	L
Direct mode	Х	Н	Х	Н	L
Direct mode	L	Х	L	Х	L
	Н	Х	Н	Х	L
	L	L	L	Х	Н
Half bridge mode	Н	L	Н	L	Н
	L	Н	Н	Н	Н



 $\cdot$ DTC1,2(Dead time adjustment pin)

When half-bridge mode, this pin is adjust the dead time of gate output G1,G2

 $\cdot \mathsf{RTC}(\mathsf{Recovery\ time\ of\ protection\ circuit\ control\ pin})$ 

When abnormality occurs (UVLO, short circuit detected), this pin is used to adjust the recovery time.



## ·ALM1,2(Alarm signal output pin)

When abnormality occurs (UVLO, short circuit detected), This pin outputs an alarm signal. (Open-Drain)

Status			
While in normal operation			
UVLO, When detecting short circuit			

#### ·MCL1,2(Miller clamp pin)

The MCL pin is a pin for preventing inrease in gate voltage due to the miller current of the power device connected to Gate pin.

#### ·VGL1,2(Gatte OFF side pin)

The VGL pin is a pin for gate drive of low signal. VGL pin connect to the gate pin through gate resistor of OFF side.

### ·DST1,2(Desaturaion protection pin)

The DESAT pin is a pin used to detect desaturation. When the DESAT pin voltage exceeds  $V_{DESAT}$ , the DESAT function will be activated. This may cause the IC to malfunction in an open state. To avoid such trouble, short-circuit the DESAT pin to the E/S pin if the desaturation protection is not used. In order to prevent the wrong detection due to noise, the noise mask time  $t_{DESATFIL}$  is set.

#### ·E/S1,2(Emitter/source pin)

E/S pin connect to the emitter / source of the power device.

#### ·VGH1,2(Gate ON side pin)

The VGH pin is a pin for gate drive of high signal. VGH pin connect to the gate pin through gate resistor of ON side.

### $\cdot \texttt{STO1,2}(\texttt{Soft turn off pin})$

The STO pin is a pin for gradually decrease gate voltage in operating desaturation protection. STO pin connect to the gate pin though a resistance value higher than the resistance value connected to VGL 1 and 2.

## ·V01+,V02+,V01-,V02-(DC/DC converter output pin)

The V0+,V02+,V01-,V02- pin is a output pin of built-in DC/DC converter. If necessary, connect a capacitor.

#### Description

1. Gate voltage rise prevention function (Miller clamp function)

If gate output Gx=L and the Miller clamp pin voltage < V<sub>CLPON</sub>, the internal MOSFET of the miller clamp pin turns on.

Gate Gx	MCL	Internal MOSFET of the MCL pin
L	Less than $V_{\text{CLPON}}$	ON
L	Not less than $V_{\text{CLPON}}$	OFF
Н	Х	OFF

2. Undervoltage Lockout (UVLO) function

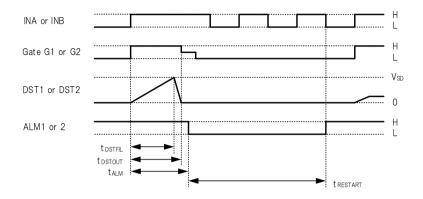
The control circuit incorporates the undervoltage lockout (UVLO) function both on the output voltage + sides. When the output voltage+ drops to the UVLO ON voltage, the gate ON/OFF side pin and the ALM pin both will output the "L"signal. When the output voltage+ rises to the UVLO OFF voltage, these pins will be reset.



3. Short circuit protection function, Soft turn-off function

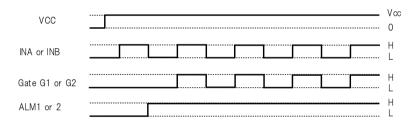
When the collector/drain pin voltage exceeds  $V_{SD}$ , the short circuit protection function will be activated. When the short circuit protection function is activated, the gate ON/OFF side pin will be set to the "Hi-Z" level, and then the ALM pin voltage to the "L" level.

Also, soft turn-off function works to reduce collector/drain voltage surge due to short circuit current. Short circuit protection is automatically canceled after the abnormal state recovery time.



Timing chart of short circuit protection function

\*Note1:After input to voltage in Vcc, it input High signal—Low signal in INx then it activate.





## ■Reliability

ltem	Test condition and acceptance criterion				
Exposure in high temperature	90°C, 240H, ※				
Exposure in low temperature	-40°C, 240H, ※				
Exposure in high temperature and high humidity	60°C, 90∼95%RH, 240H, ※				
Thermal shock	-40°C/30min to 100°C/30min, 500cycles, ※				
Low temperature operation	Input voltage:DC24V, Output current:Rated Load				
	-40°C, 240H, 💥				
High temperature operation	Input voltage:DC24V, Output current:Rated Load				
	85°С, 240Н, Ж				
high temperature	Input voltage:DC24V, Output current:Rated Load				
and high humidity operation	85°C, 85%RH, 240H, 💥				
Vibration	Vibration amplitude:1.5mm(peak to peak), Vibration Frequency:10 to 55Hz, Sweeping:1min.				
	In each X, Y and Z direction:once, 120min. 💥				
Impact	Acceleration:490m/s <sup>2</sup> (50G), Operating time:11ms				
	In each $\pm$ X, Y and Z direction:3 times, $$ $$ $$				
Drop test for packaged freights	Dorp to concrete. Height:40cm				
	Dorp surface:1 corner, 3 spines, 6 surfaces, 1 time each.				
Solderblity	Sample shall be dipped into the solution of Methanol and Rosin				
	(having 75% Methanol and having 25% Rosin by weight measuring)				
	and shall be dippend into the solder bath having the solder Sn-3Ag-0.5Cu				
	of $250\pm5^\circ$ C to the position to 3mm from the end of terminal for $3.0\pm0.5$ seconds,				
	and pulled up. After above treatment, the sample shall be coveredby solder uniformly				
	at more than 75% of circumference and shall not show any unusual appearance.				
Resistance to soldering heat	Sample shall be dipped into the solution of Methanol and Rosin				
	(having 75% Methanol and having 25% Rosin by weight measuring)				
	and shall be dippend into the solder bath having the solder Sn-3Ag-0.5Cu				
	of 260 $\pm5^\circ\!C$ to the position to 3mm from the end of terminal for 10.0 $\pm$ 0.5				
	seconds, and pulled up. After that sample shall be replace in normal ambient				
	for 1 $\sim$ 2 hours and shall not show any unusual appearance.				

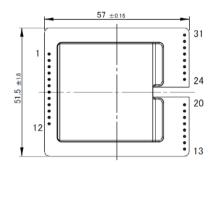
% After each test, exposure at room temperature and humidity condition for 24 hours.

There shall be no abnormality on the electrical specification and appearance.

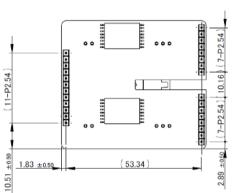


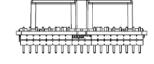
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### ■Outline Dimensional Drawing











■Product Weight

45g(TYP)



## Recommended Soldering Condition

<ul> <li>Flow soldering d</li> </ul>	condition
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255±3°C Less than 3sec
 Temperature of preheating 110°C~130°C
 End temperature of preheating 110°C±10°C
 360°C(MAX) Less than 3sec

 $\cdot \, \text{Soldering condition of hand work}$ 

#### ■Storage Conditions

ltem	Min	Max	Unit	Conditions · Note
Storage temperature	-25	60	°C	A packing state

%If you want to use past the long period there is a concern that the solder non-wetting by terminal oxidation to occur. Therefore, please use from taking enough tests.

#### ■Usage Cautions

- Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product.
   Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature.
   When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality.
   Do not combine high voltage line and fuse.
- Make sure the rise/fall time of the input signal is 500ns or less.
- Please do not apply excessive stress to this product when attaching to power module.





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    - Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
    - Use in locations where corrosive gases such as salt air, C12, H2S, NH3, SO2, or NO2, are present.
    - Use in environments with strong static electricity or electromagnetic radiation.
    - Use that involves placing inflammable material next to the product.
    - · Use of this product either sealed with a resin filling or coated with resin.
    - · Use of water or a water soluble detergent for flux cleaning.
    - · Use in locations where condensation is liable to occur.
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