



PMXB56EN

30 V, N-channel Trench MOSFET

27 October 2020

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Leadless ultra small and thin SMD plastic package: 1.1 × 1.0 × 0.37 mm
- Exposed drain pad for excellent thermal conduction
- Very low Drain-Source on-state resistance $R_{DSon} = 49\text{ m}\Omega$
- Very fast switching

3. Applications

- Low-side load switch and charging switch for portable devices
- Power management in battery-driven portables
- LED driver
- DC-to-DC converters

4. Quick reference data

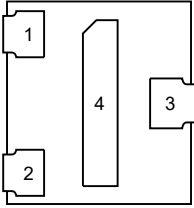
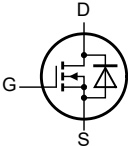
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^\circ\text{C}$	-	-	30	V
V_{GS}	gate-source voltage		-20	-	20	V
I_D	drain current	$V_{GS} = 10\text{ V}; T_{amb} = 25\text{ }^\circ\text{C}$	[1]	-	3.2	A
Static characteristics						
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10\text{ V}; I_D = 3.2\text{ A}; T_j = 25\text{ }^\circ\text{C}$	-	49	55	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 <p>Transparent top view DFN1010D-3 (SOT1215)</p>	 <p>017aaa253</p>
2	S	source		
3	D	drain		
4	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMXB56EN	DFN1010D-3	plastic, leadless thermal enhanced ultra thin small outline package with side-wettable flanks (SWF); 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1215

7. Marking

Table 4. Marking codes

Type number	Marking code
PMXB56EN	01 10 10

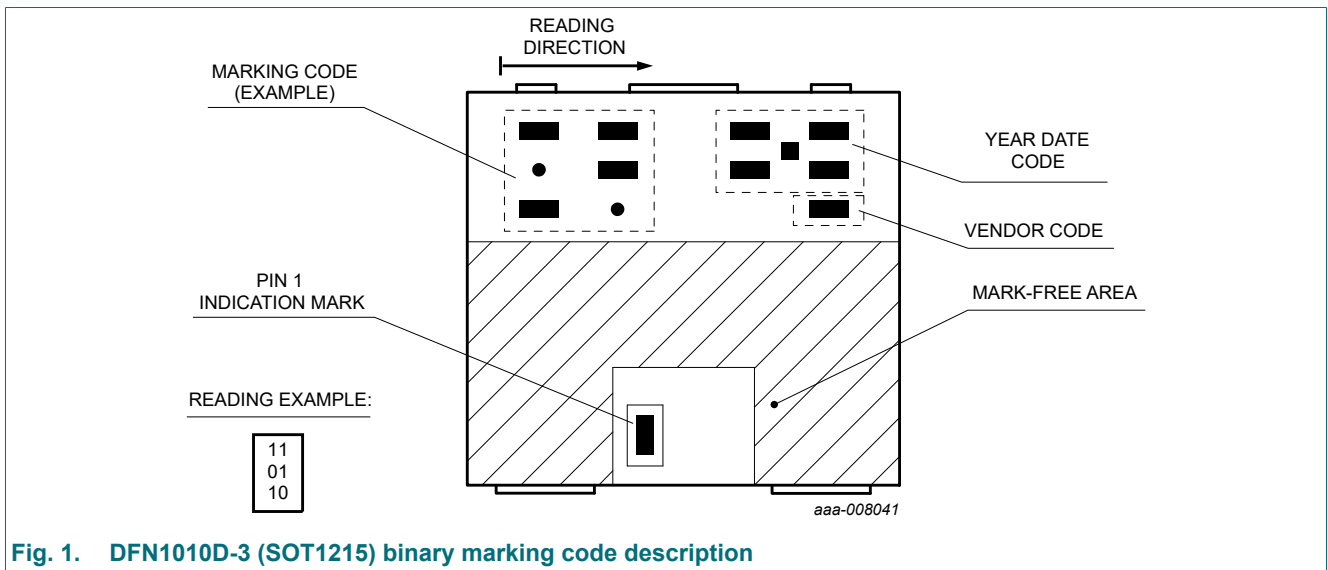


Fig. 1. DFN1010D-3 (SOT1215) binary marking code description

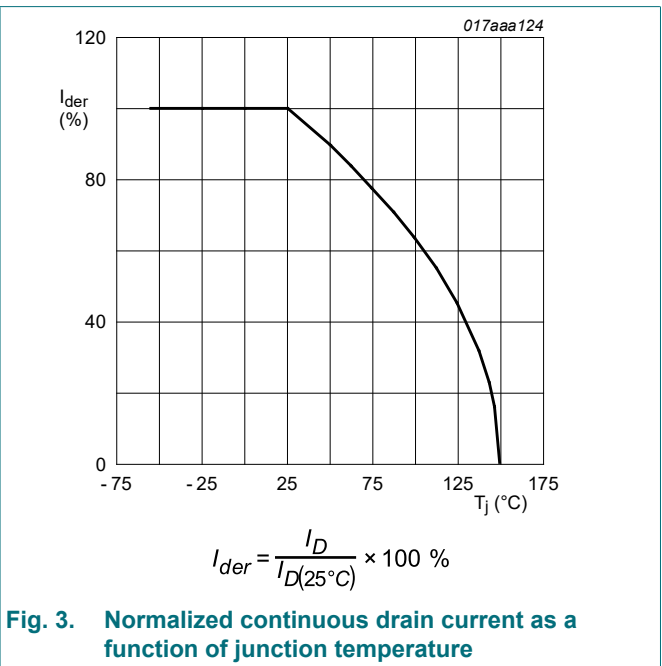
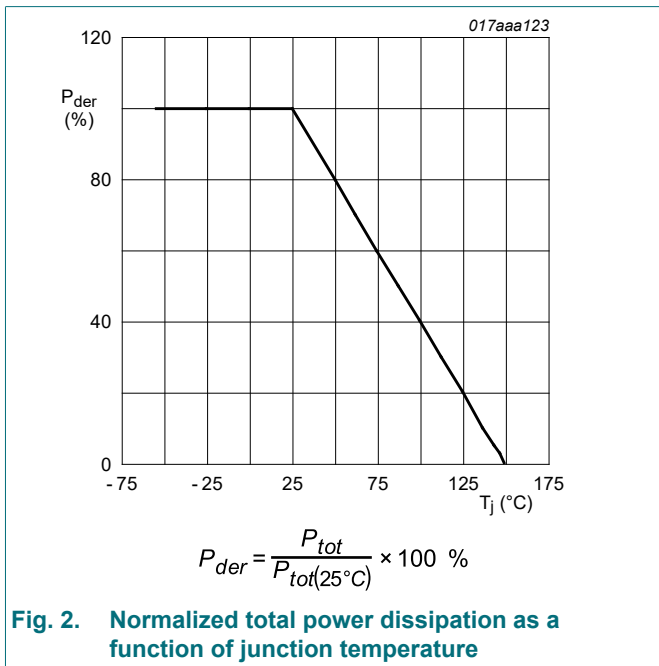
8. Limiting values

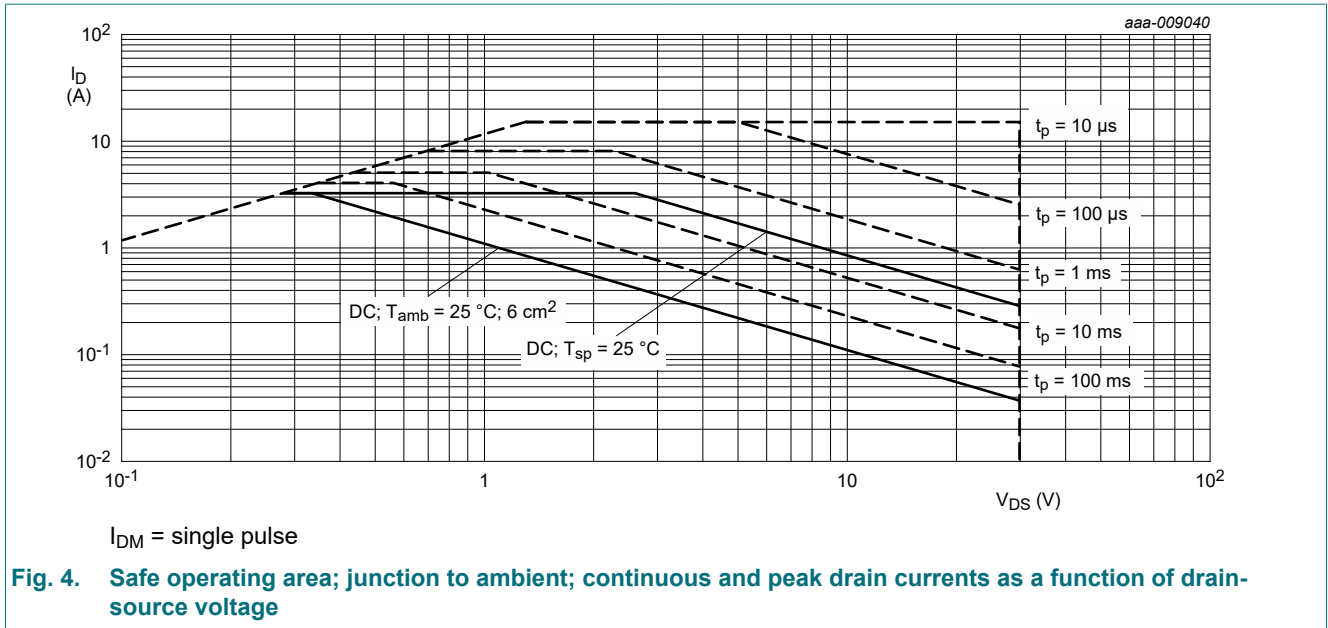
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	3.2	A
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	2.8	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	15	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	0.4	W
			[1]	-	1.07	W
		T _{sp} = 25 °C		-	8.33	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	1	A

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
 [2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



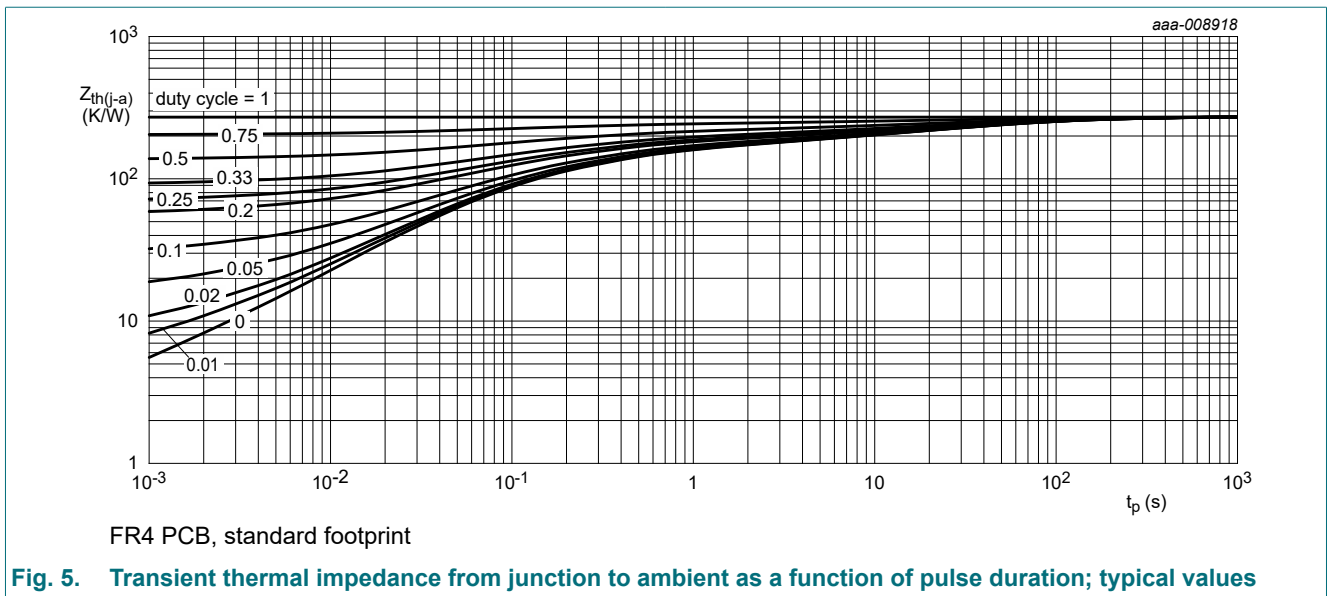


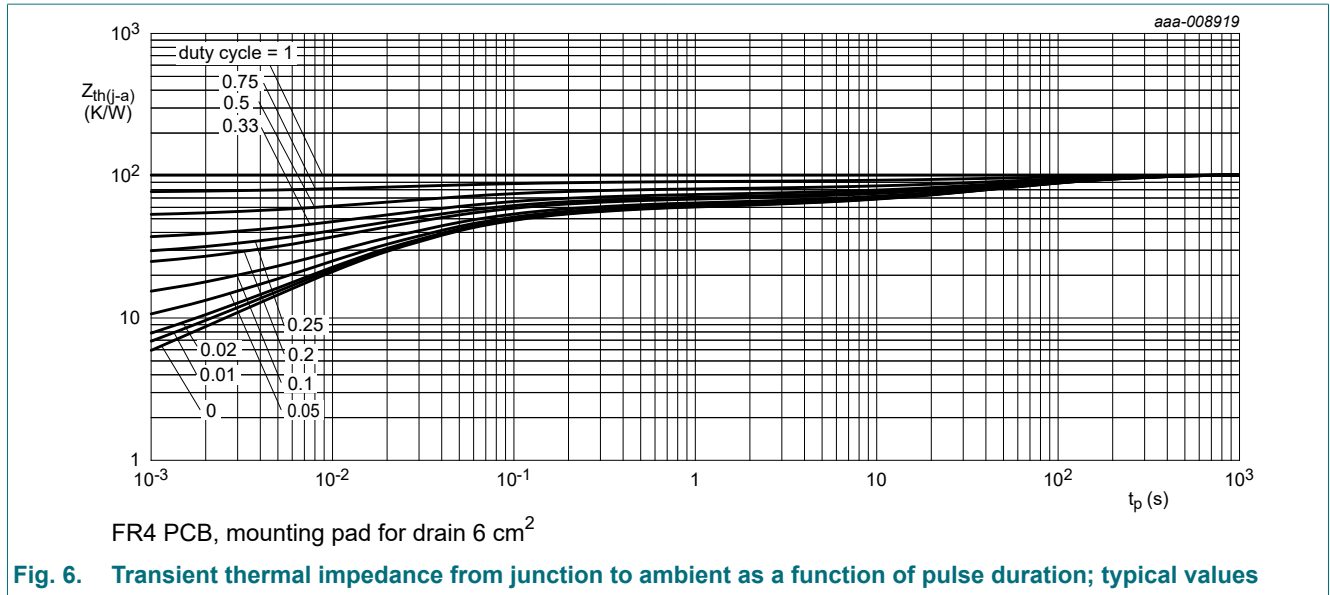
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	271	312	K/W
			[2]	-	102	117	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	10	15	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm^2 .





10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	30	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ }^\circ C$	1	1.5	2	V
I_{DSS}	drain leakage current	$V_{DS} = 30 V$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	1	μA
I_{GSS}	gate leakage current	$V_{GS} = 20 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	100	nA
		$V_{GS} = -20 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 V$; $I_D = 3.2 A$; $T_j = 25 \text{ }^\circ C$	-	49	55	m Ω
		$V_{GS} = 10 V$; $I_D = 2.8 A$; $T_j = 150 \text{ }^\circ C$	-	77	87	m Ω
		$V_{GS} = 4.5 V$; $I_D = 3.2 A$; $T_j = 25 \text{ }^\circ C$	-	56	65	m Ω
g_{fs}	forward transconductance	$V_{DS} = 10 V$; $I_D = 3.2 A$; $T_j = 25 \text{ }^\circ C$	-	13	-	S
R_G	gate resistance	$f = 1 \text{ MHz}$; $T_j = 25 \text{ }^\circ C$	-	2.3	-	Ω
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 15 V$; $I_D = 3.2 A$; $V_{GS} = 10 V$; $T_j = 25 \text{ }^\circ C$	-	3.6	6.3	nC
Q_{GS}	gate-source charge		-	0.5	-	nC
Q_{GD}	gate-drain charge		-	0.4	-	nC
C_{iss}	input capacitance	$V_{DS} = 15 V$; $f = 1 \text{ MHz}$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	209	-	pF
C_{oss}	output capacitance		-	50	-	pF
C_{rss}	reverse transfer capacitance		-	17	-	pF
$t_{d(on)}$	turn-on delay time		$V_{DS} = 15 V$; $I_D = 3.2 A$; $V_{GS} = 10 V$; $R_{G(ext)} = 6 \Omega$; $T_j = 25 \text{ }^\circ C$	-	3	-
t_r	rise time	-		12	-	ns
$t_{d(off)}$	turn-off delay time	-		11	-	ns
t_f	fall time	-		2	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 1 A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	0.7	1.2	V

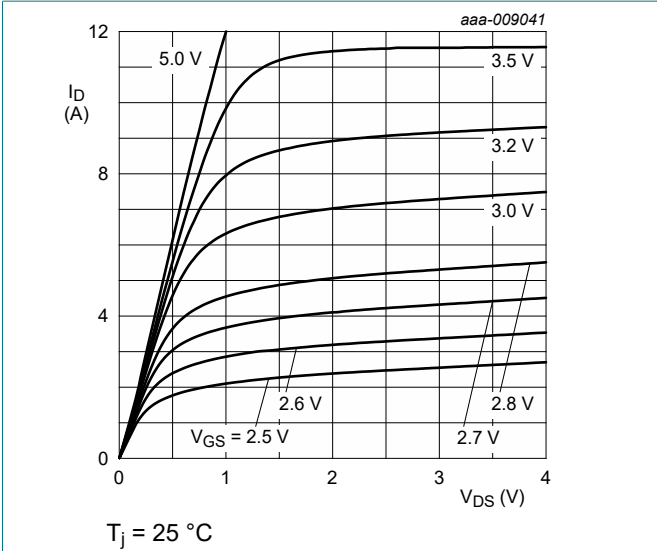


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

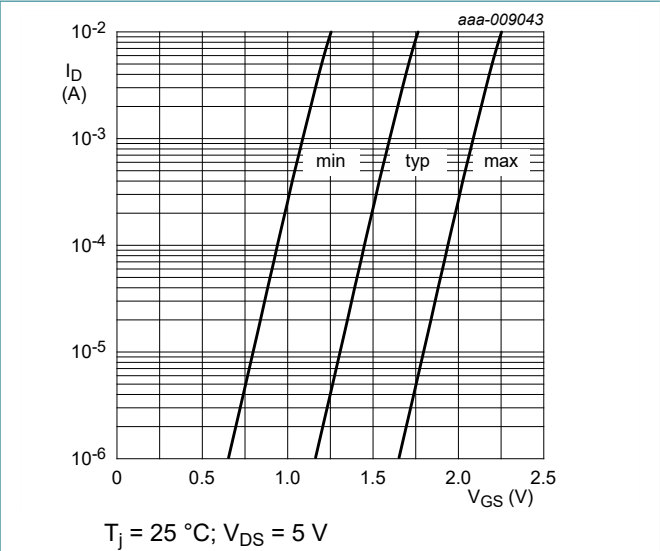


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

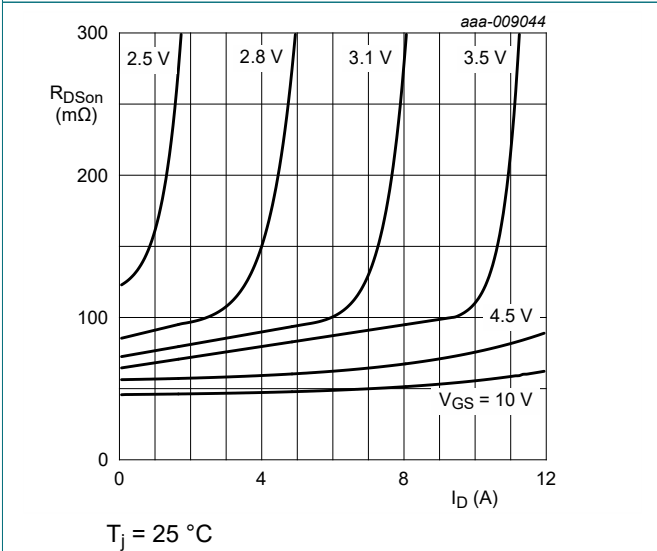


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

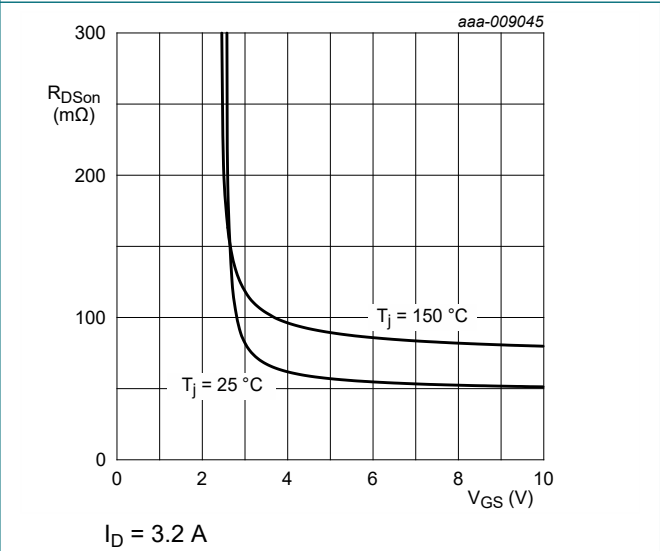


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

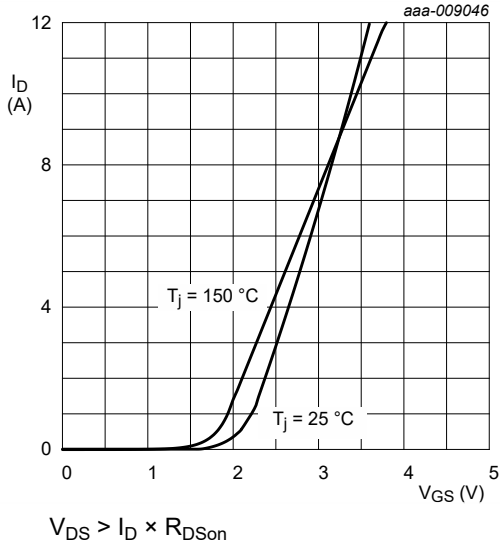


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

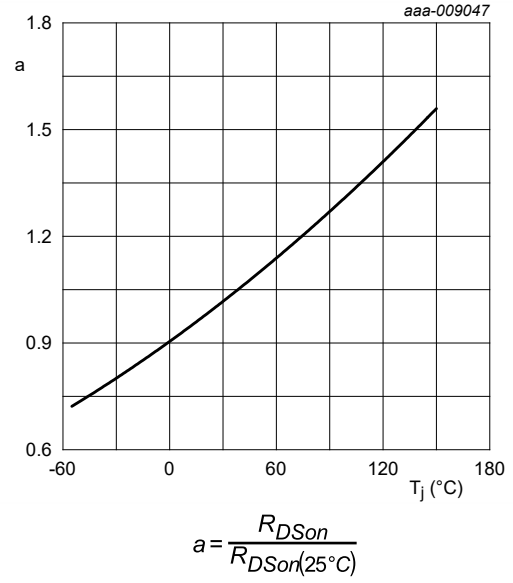


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

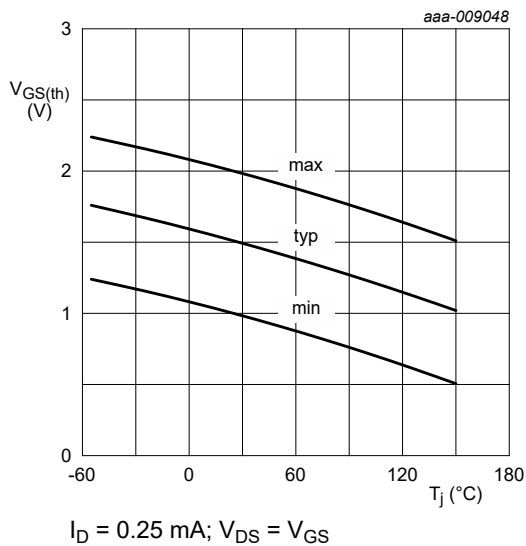


Fig. 13. Gate-source threshold voltage as a function of junction temperature

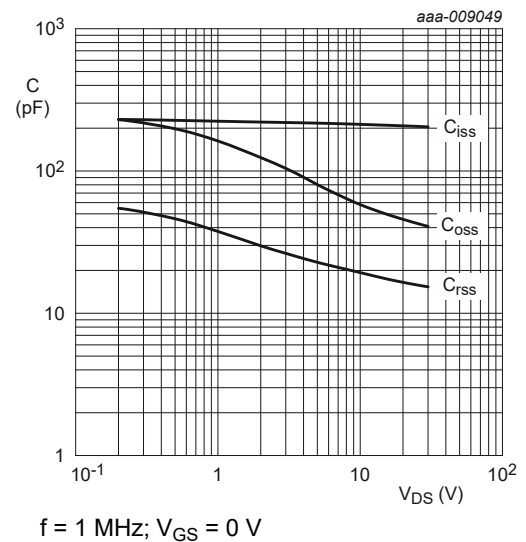
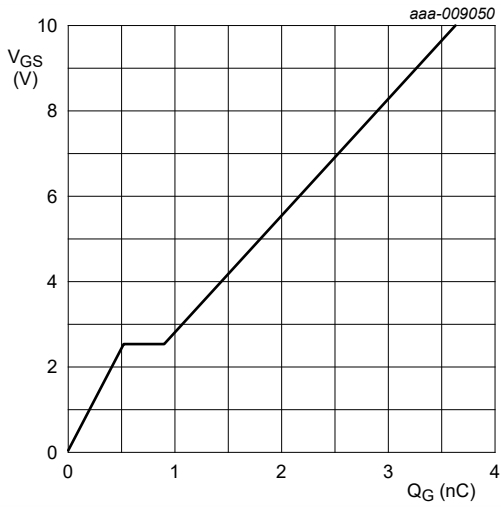


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 3.2 \text{ A}$; $V_{DS} = 15 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$

Fig. 15. Gate-source voltage as a function of gate charge; typical values

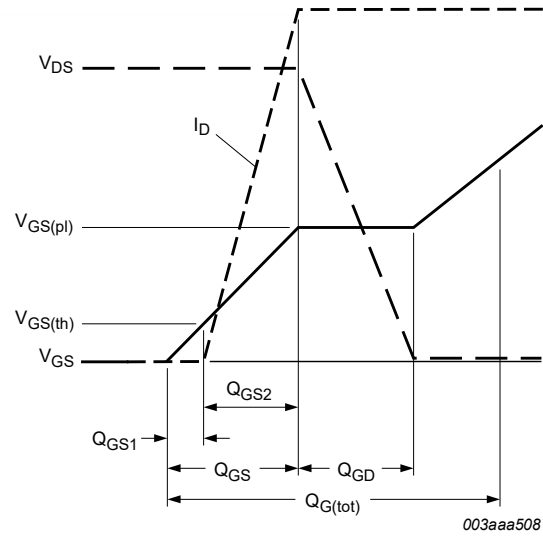
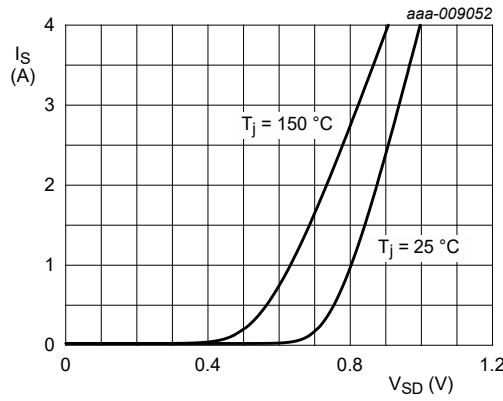


Fig. 16. Gate charge waveform definitions



$V_{GS} = 0 \text{ V}$

Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

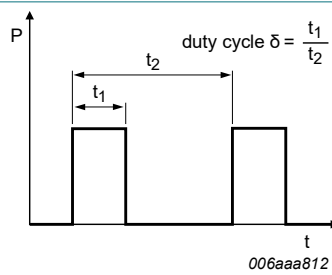


Fig. 18. Duty cycle definition

12. Package outline

DFN1010D-3: plastic thermal enhanced ultra thin small outline package; no leads;
3 terminals; body: 1.1 x 1.0 x 0.37 mm

SOT1215

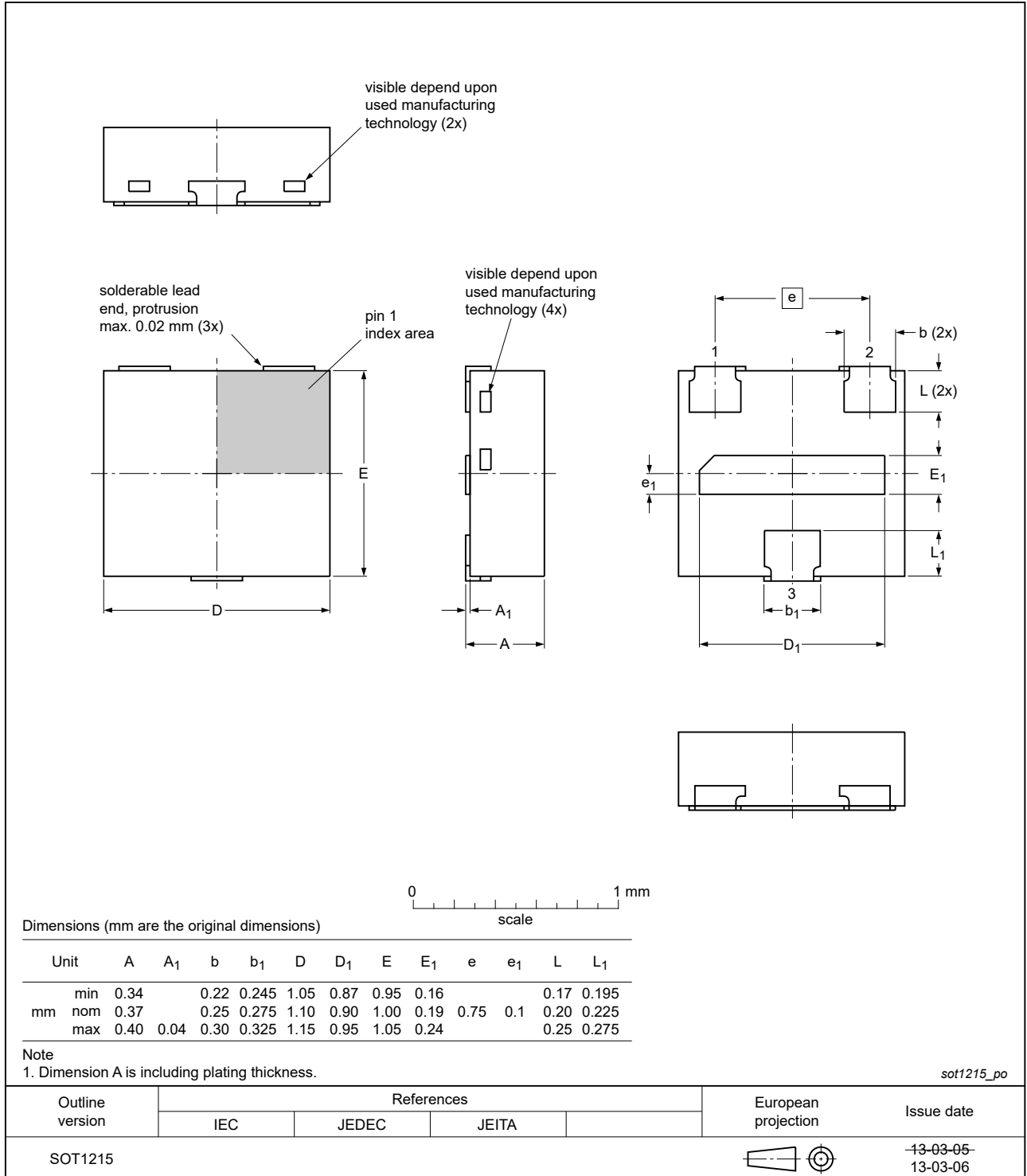


Fig. 19. Package outline DFN1010D-3 (SOT1215)

13. Soldering

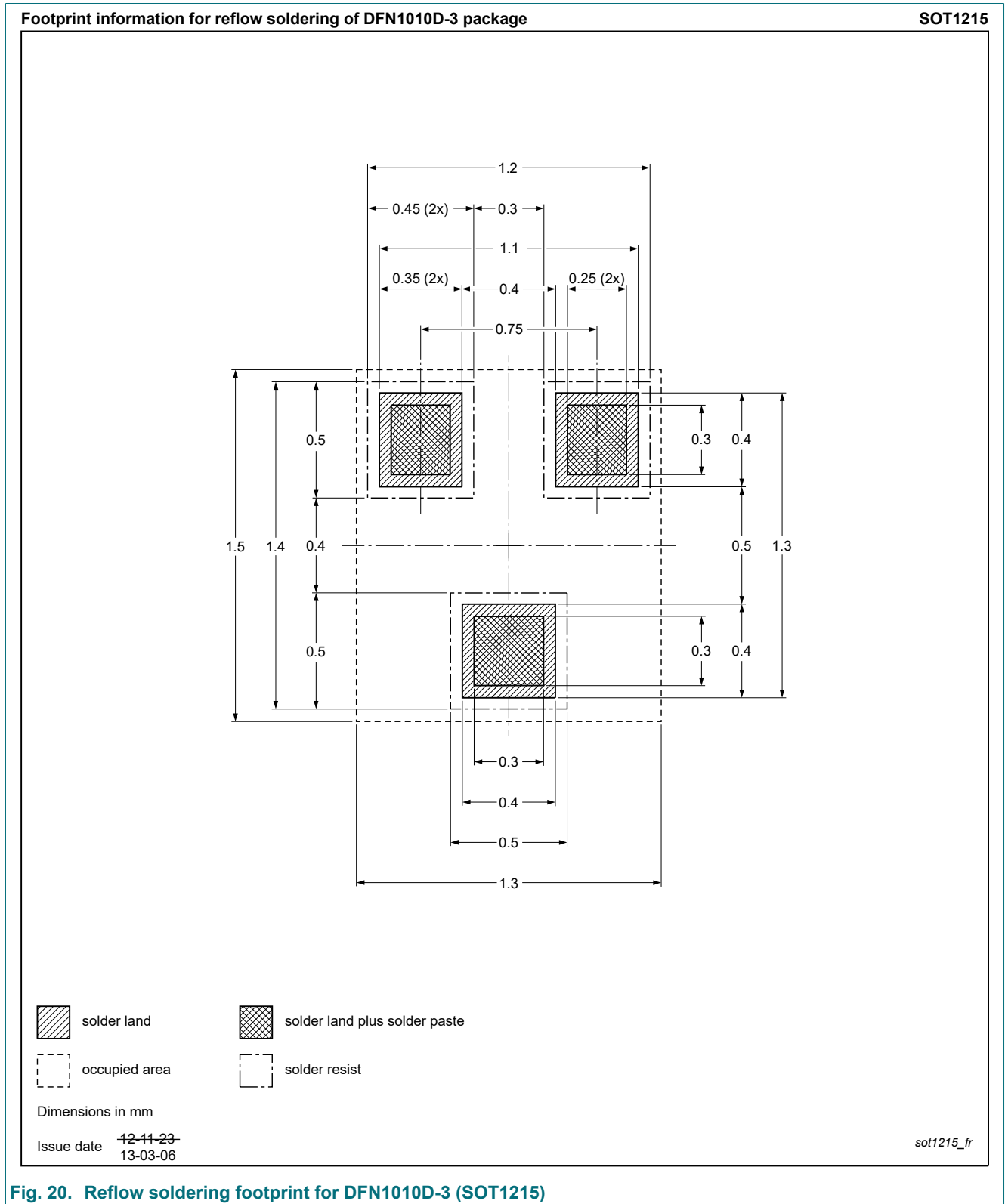


Fig. 20. Reflow soldering footprint for DFN1010D-3 (SOT1215)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMXB56EN v.4	20201027	Product data sheet	-	PMXB56EN v.3
Modifications:	<ul style="list-style-type: none">The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.			
PMXB56EN v.3	20170111	Product data sheet	-	PMXB56EN v.2
PMXB56EN v.2	20140430	Product data sheet	-	PMXB56EN v.1
PMXB56EN v.1	20130925	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	3
9. Thermal characteristics.....	4
10. Characteristics.....	6
11. Test information.....	9
12. Package outline.....	10
13. Soldering.....	11
14. Revision history.....	12
15. Legal information.....	13

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