

FGH20N6S2 / FGP20N6S2 / FGB20N6S2

600V, SMPS II Series N-Channel IGBT

General Description

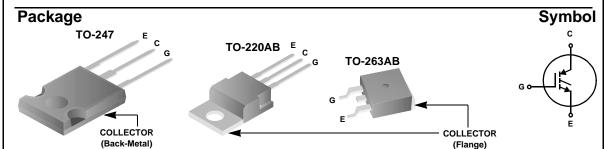
The FGH20N6S2, FGP20N6S2, FGB20N6S2, are Low Gate Charge, Low Plateau Voltage SMPS II IGBTs combining the fast switching speed of the SMPS IGBTs along with lower gate charge and plateau voltage and high avalanche capability (UIS). These LGC devices shorten delay times, and reduce the power requirement of the gate drive. These devices are ideally suited for high voltage switched mode power supply applications where low conduction loss, fast switching times and UIS capability are essential. SMPS II LGC devices have been specially designed for:

- Power Factor Correction (PFC) circuits
- · Full bridge topologies
- · Half bridge topologies
- · Push-Pull circuits
- Uninterruptible power supplies
- · Zero voltage and zero current switching circuits

Formerly Developmental Type TA49330.

Features

- 100kHz Operation at 390V, 7A
- · 200kHZ Operation at 390V, 5A
- · 600V Switching SOA Capability
- Typical Fall Time 85ns at TJ = 125°C
- Low Gate Charge 30nC at V_{GE} = 15V
- Low Plateau Voltage6.5V Typical
- Low Conduction Loss
- Low E_{on}



Device Maximum Ratings T_C= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV _{CES}	Collector to Emitter Breakdown Voltage	600	V
I _{C25}	Collector Current Continuous, T _C = 25°C	28	А
I _{C110}	Collector Current Continuous, T _C = 110°C	13	Α
I _{CM}	Collector Current Pulsed (Note 1)	40	Α
V _{GES}	Gate to Emitter Voltage Continuous	±20	V
V_{GEM}	Gate to Emitter Voltage Pulsed	±30	V
SSOA	Switching Safe Operating Area at T _J = 150°C, Figure 2	35 at 600V	А
E _{AS}	Pulsed Avalanche Energy, I _{CE} = 7.0A, L = 4mH, V _{DD} = 50V	100	mJ
E _{ARV}	Pulsed Avalanche Energy, I _{CE} = 7.0A, L = 4mH, V _{DD} = 50V	100	mJ
P _D	Power Dissipation Total T _C = 25°C	125	W
	Power Dissipation Derating T _C > 25°C	1.0	W/°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
T _{STG}	Storage Junction Temperature Range	-55 to 150	°C

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTE:

^{1.} Pulse width limited by maximum junction temperature.

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
20N6S2	FGH20N6S2	TO-247	Tube	N/A	30 Units
20N6S2	FGP20N6S2	TO-220AB	Tube	N/A	50 Units
20N6S2	FGB20N6S2	TO-263AB	Tube	N/A	50 Units
20N6S2	FGB20N6S2T	TO-263AB	330mm	24mm	800 Units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

BV _{CES} BV _{ECS} I _{CES} I _{GES} On State (Characteristics Collector to Emitter Breakdown Voltage Emitter to Collector Breakdown Voltage Collector to Emitter Leakage Current Gate to Emitter Leakage Current Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage Gate to Emitter Plateau Voltage	$I_{C} = -10 \text{mA}, V_{C}$ $V_{CE} = 600 \text{V}$ $V_{GE} = \pm 20 \text{V}$ $I_{C} = 7.0 \text{A},$ $V_{GE} = 15 \text{V}$ $I_{C} = 7.0 \text{A},$ $V_{CE} = 300 \text{V}$ $I_{C} = 250 \mu \text{A}, V_{C}$	$G_{E} = 0$ $T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$	600 20 - - - -	- - - - - - 1.9	250 2.0 ±250 2.7 2.2 36 45	V V μA mA nA
BV _{ECS} I _{CES} I _{GES} On State (V _{CE(SAT)} Dynamic (Q _{G(ON)} V _{GE(TH)}	Emitter to Collector Breakdown Voltage Collector to Emitter Leakage Current Gate to Emitter Leakage Current Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$I_{C} = -10 \text{mA}, V_{C}$ $V_{CE} = 600 \text{V}$ $V_{GE} = \pm 20 \text{V}$ $I_{C} = 7.0 \text{A},$ $V_{GE} = 15 \text{V}$ $I_{C} = 7.0 \text{A},$ $V_{CE} = 300 \text{V}$ $I_{C} = 250 \mu \text{A}, V_{C}$	$G_{E} = 0$ $T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$	20 - - - -	- - - - - 1.9	2.0 ±250 2.7 2.2	V μA mA nA
I _{CES} I _{GES} On State (V _{CE(SAT)} Dynamic (Q _{G(ON)} V _{GE(TH)}	Collector to Emitter Leakage Current Gate to Emitter Leakage Current Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$V_{CE} = 600V$ $V_{GE} = \pm 20V$ $I_{C} = 7.0A,$ $V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$ $T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$	-	2.2	2.0 ±250 2.7 2.2	μA mA nA V V
I _{CES} I _{GES} On State (V _{CE(SAT)} Dynamic (Q _{G(ON)} V _{GE(TH)}	Gate to Emitter Leakage Current Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$V_{GE} = \pm 20V$ $I_{C} = 7.0A$, $V_{GE} = 15V$ $I_{C} = 7.0A$, $V_{CE} = 300V$ $I_{C} = 250\mu A$, $V_{C} = 250\mu A$	$T_{J} = 125^{\circ}C$ $T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$		2.2	2.0 ±250 2.7 2.2	mA nA V V N
On State (VCE(SAT) Dynamic (QG(ON) VGE(TH)	Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$I_{C} = 7.0A,$ $V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 25^{\circ}C$ $T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$		2.2	±250 2.7 2.2	NA V V NC
On State (VCE(SAT) Dynamic (QG(ON) VGE(TH)	Characteristics Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$I_{C} = 7.0A,$ $V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$		2.2	2.7 2.2	V V
$V_{CE(SAT)}$ Dynamic ($Q_{G(ON)}$ $V_{GE(TH)}$	Collector to Emitter Saturation Voltage Characteristics Gate Charge Gate to Emitter Threshold Voltage	$V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$	-	1.9	2.2	V
Dynamic (Q _{G(ON)} V _{GE(TH)}	Characteristics Gate Charge Gate to Emitter Threshold Voltage	$V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$	-	1.9	2.2	V
Dynamic (Q _{G(ON)} V _{GE(TH)}	Characteristics Gate Charge Gate to Emitter Threshold Voltage	$V_{GE} = 15V$ $I_{C} = 7.0A,$ $V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$T_{J} = 125^{\circ}C$ $V_{GE} = 15V$ $V_{GE} = 20V$	-	1.9	2.2	nC
Q _{G(ON)}	Gate Charge Gate to Emitter Threshold Voltage	$V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$V_{GE} = 15V$ $V_{GE} = 20V$	-			
Q _{G(ON)}	Gate Charge Gate to Emitter Threshold Voltage	$V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$V_{GE} = 20V$	-			
V _{GE(TH)}	Gate to Emitter Threshold Voltage	$V_{CE} = 300V$ $I_{C} = 250\mu A, V_{C}$	$V_{GE} = 20V$	-			
		$I_C = 250 \mu A, V_C$. 40	i nc
				3.5	4.3	5.0	V
GLI		$I_C = 7.0A$, $V_{CE} = 300V$		-	6.5	8.0	V
SSOA	Characteristics Switching SOA	$\begin{split} &T_J=150^{\circ}\text{C}, R_G=25\Omega, V_{GE}=\\ &15\text{V}, L=0.5\text{mH}, \text{Vce}=600\text{V} \\ &\text{IGBT and Diode at } T_J=25^{\circ}\text{C}, \\ &I_{CE}=7\text{A}, \\ &V_{CE}=390\text{V}, \\ &V_{GE}=15\text{V}, \\ &R_G=25\Omega \\ &L=0.5\text{mH} \\ &\text{Test Circuit - Figure 20} \end{split}$		35	-	-	А
t worm	Current Turn-On Delay Time			_	7.7	_	ns
t _{d(ON)I}	Current Rise Time			_	4.5	_	ns
t _{d(OFF)I}	Current Turn-Off Delay Time			_	87	_	ns
t _{fl}	Current Fall Time			-	50	-	ns
E _{ON1}	Turn-On Energy (Note 1)			-	25	-	μJ
E _{ON2}	Turn-On Energy (Note 1)			-	85	-	μJ
E _{OFF}	Turn-Off Energy (Note 2)			-	58	75	μJ
t _{d(ON)I}	Current Turn-On Delay Time	IGBT and Dioc	de at T _J = 125°C,	-	7	-	ns
t _{rl}	Current Rise Time	$I_{CE} = 7A,$ $V_{CE} = 390V,$ $V_{GE} = 15V,$ $R_{G} = 25\Omega$ $L = 0.5mH$ $Test Circuit - Figure 20$		-	4.5	-	ns
t _{d(OFF)I}	Current Turn-Off Delay Time			-	120	145	ns
t _{fl}	Current Fall Time			-	85	105	ns
E _{ON1}	Turn-On Energy (Note 1)			-	20	-	μJ
E _{ON2}	Turn-On Energy (Note 1)				125	140	μJ
E _{OFF}	Turn-Off Energy (Note 2)			-	135	180	μJ
Thermal C	Characteristics						
$R_{\theta JC}$	Thermal Resistance Junction-Case			-	-	1.0	°C/W

NOTE:

^{1.} Values for two Turn-On loss conditions are shown for the convenience of the circuit designer. E_{ON1} is the turn-on loss of the IGBT only. E_{ON2} is the turn-on loss when a typical diode is used in the test circuit and the diode is at the same T_J as the IGBT. The diode type is specified in figure 20.

^{2.} Turn-Off Energy Loss (E_{OFF}) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero (I_{CF} = 0A). All devices were tested per JEDEC Standard No. 24-1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss.

Typical Performance Curves

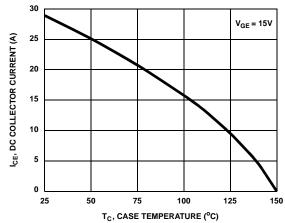


Figure 1. DC Collector Current vs Case Figure 2. Minim Temperature

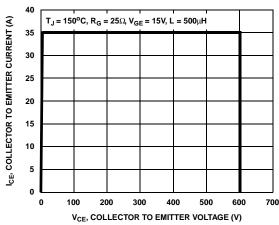


Figure 2. Minimum Switching Safe Operating Area

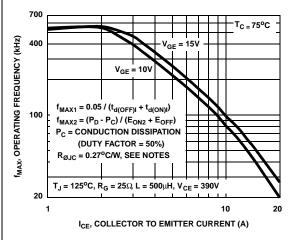


Figure 3. Operating Frequency vs Collector to Emitter Current

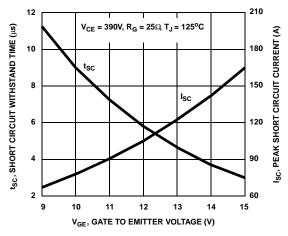


Figure 4. Short Circuit Withstand Time

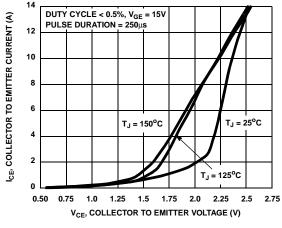


Figure 5. Collector to Emitter On-State Voltage

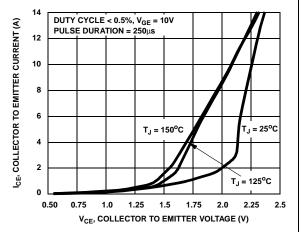


Figure 6. Collector to Emitter On-State Voltage

Typical Performance Curves (Continued)

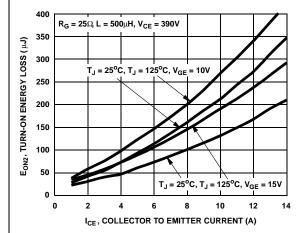


Figure 7. Turn-On Energy Loss vs Collector to Emitter Current

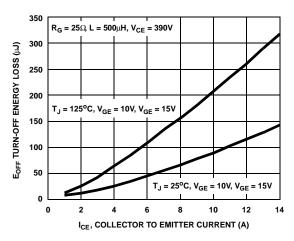


Figure 8. Turn-Off Energy Loss vs Collector to Emitter Current

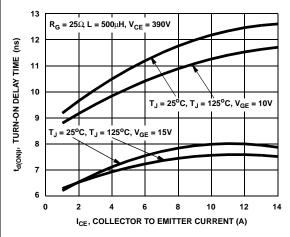


Figure 9. Turn-On Delay Time vs Collector to Emitter Current

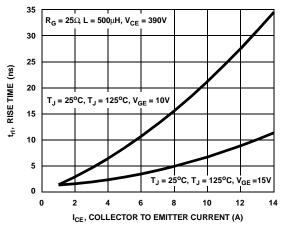


Figure 10. Turn-On Rise Time vs Collector to Emitter Current

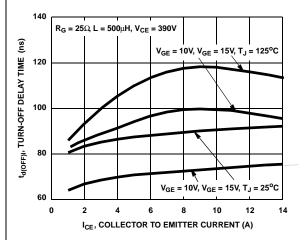


Figure 11. Turn-Off Delay Time vs Collector to Emitter Current

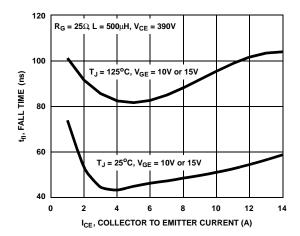
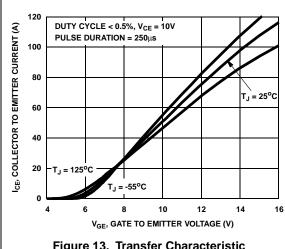


Figure 12. Fall Time vs Collector to Emitter
Current



Typical Performance Curves (Continued)



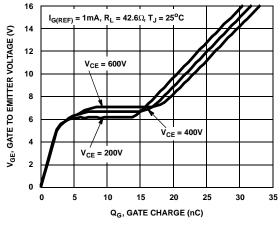


Figure 14. Gate Charge

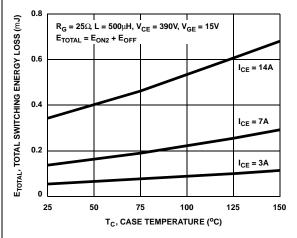


Figure 15. Total Switching Loss vs Case **Temperature**

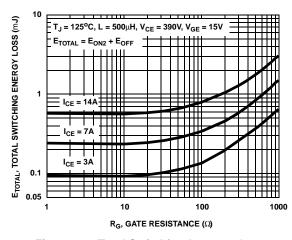


Figure 16. Total Switching Loss vs Gate Resistance

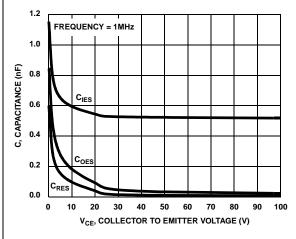


Figure 17. Capacitance vs Collector to Emitter Voltage

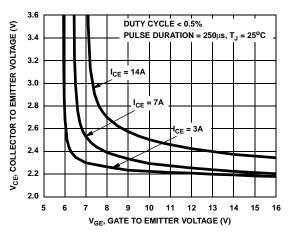


Figure 18. Collector to Emitter On-State Voltage vs **Gate to Emitter Voltage**

Typical Performance Curves (Continued)

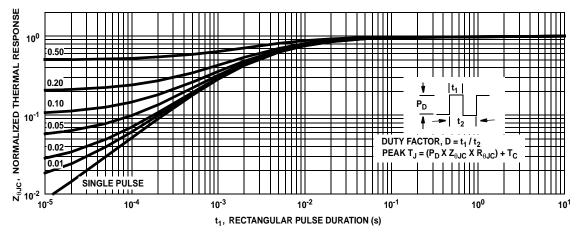


Figure 19. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuit and Waveforms

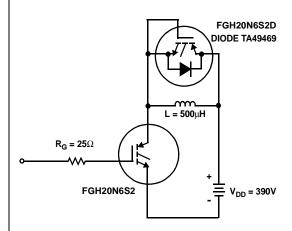


Figure 20. Inductive Switching Test Circuit

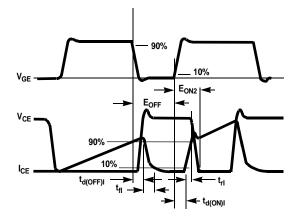


Figure 21. Switching Test Waveforms

Handling Precautions for IGBTs

Insulated Gate Bipolar Transistors are susceptible to gate-insulation damage by the electrostatic discharge of energy through the devices. When handling these devices, care should be exercised to assure that the static charge built in the handler's body capacitance is not discharged through the device. With proper handling and application procedures, however, IGBTs are currently being extensively used in production by numerous equipment manufacturers in military, industrial and consumer applications, with virtually no damage problems due to electrostatic discharge. IGBTs can be handled safely if the following basic precautions are taken:

- Prior to assembly into a circuit, all leads should be kept shorted together either by the use of metal shorting springs or by the insertion into conductive material such as "ECCOSORBD™ LD26" or equivalent.
- When devices are removed by hand from their carriers, the hand being used should be grounded by any suitable means - for example, with a metallic wristband.
- 3. Tips of soldering irons should be grounded.
- 4. Devices should never be inserted into or removed from circuits with power on.
- Gate Voltage Rating Never exceed the gatevoltage rating of V_{GEM}. Exceeding the rated V_{GE} can result in permanent damage to the oxide layer in the gate region.
- 6. Gate Termination The gates of these devices are essentially capacitors. Circuits that leave the gate open-circuited or floating should be avoided. These conditions can result in turn-on of the device due to voltage buildup on the input capacitor due to leakage currents or pickup.
- Gate Protection These devices do not have an internal monolithic Zener diode from gate to emitter. If gate protection is required an external Zener is recommended.

Operating Frequency Information

Operating frequency information for a typical device (Figure 3) is presented as a guide for estimating device performance for a specific application. Other typical frequency vs collector current (I_{CE}) plots are possible using the information shown for a typical unit in Figures 5, 6, 7, 8, 9 and 11. The operating frequency plot (Figure 3) of a typical device shows f_{MAX1} or f_{MAX2} ; whichever is smaller at each point. The information is based on measurements of a typical device and is bounded by the maximum rated junction temperature.

 f_{MAX1} is defined by $f_{MAX1}=0.05/(t_{d(OFF)I}+t_{d(ON)I}).$ Deadtime (the denominator) has been arbitrarily held to 10% of the on-state time for a 50% duty factor. Other definitions are possible. $t_{d(OFF)I}$ and $t_{d(ON)I}$ are defined in Figure 21. Device turn-off delay can establish an additional frequency limiting condition for an application other than $T_{JM}\cdot t_{d(OFF)I}$ is important when controlling output ripple under a lightly loaded condition

 f_{MAX2} is defined by $f_{MAX2} = (P_D - P_C)/(E_{OFF} + E_{ON2}).$ The allowable dissipation (P_D) is defined by $P_D = (T_{JM} - T_C)/R_{\theta JC}.$ The sum of device switching and conduction losses must not exceed $P_D.$ A 50% duty factor was used (Figure 3) and the conduction losses (P_C) are approximated by $P_C = (V_{CE} \times I_{CE})/2.$

 E_{ON2} and E_{OFF} are defined in the switching waveforms shown in Figure 21. E_{ON2} is the integral of the instantaneous power loss (I_{CE} x V_{CE}) during turn-on and E_{OFF} is the integral of the instantaneous power loss (I_{CE} x V_{CE}) during turn-off. All tail losses are included in the calculation for E_{OFF} ; i.e., the collector current equals zero ($I_{CE}=0$)

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