TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

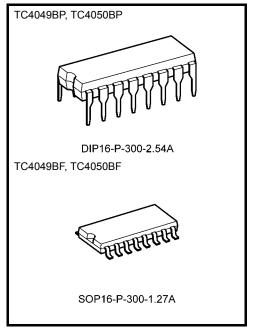
TC4049BP, TC4049BF TC4050BP, TC4050BF

TC4049B Hex Buffer/Converter (inverting type)
TC4050B Hex Buffer/Converter (non-inverting type)

TC4049B, TC4050B contain six circuits of buffers. TC4049B is inverter type and TC4050B is non-inverter type.

Since one TTL or DTL can be directly driven having large output current, these are useful for interfacing from CMOS to TTL or DTL. As voltage up to $V_{\rm SS}$ + 18 volts can be applied to the input regardless of $V_{\rm DD}$, these can be also used as the level converter IC's which converts CMOS logical circuits of 15 volts or 10 volts system to CMOS/TTL logical circuits of 5 volts system.

Ideal switching characteristic has been obtained by the circuit diagram of three stage inverters for TC4049B and two stage inverters for TC4050B.

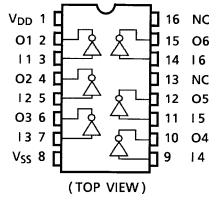


Weight

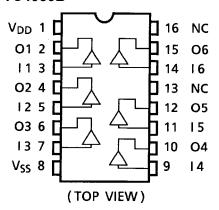
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Pin Assignment

TC4049B

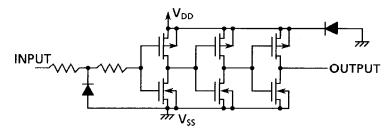


TC4050B

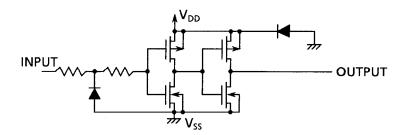


Circuit Diagram

1/6 TC4049B



1/6 TC4050B



Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|------------------|----------------------------------|------|
| DC supply voltage | V_{DD} | V_{SS} – 0.5 to V_{SS} + 20 | V |
| Input voltage | V _{IN} | V_{SS} – 0.5 to V_{SS} + 20 | V |
| Output voltage | V _{OUT} | V_{SS} – 0.5 to V_{DD} + 0.5 | > |
| DC input current | I _{IN} | -10 | mA |
| Power dissipation | P_{D} | 300 (DIP)/180 (SOP) | mW |
| Operating temperature range | T _{opr} | -40 to 85 | °C |
| Storage temperature range | T _{stg} | –65 to 150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (V_{SS} = 0 V) (Note)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------|----------|----------------|-----|------|-----|------|
| DC supply voltage | V_{DD} | _ | 3 | _ | 18 | V |
| Input voltage | V_{IN} | | 0 | | 18 | V |

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics (V_{SS} = 0 V)

| 01 | | Sym- | Test Condition | | -40°C | | | 25°C | | | 85°C | | |
|---------------------------|--------------------|-----------------------|--|------------------------|-------|------|-------|-------------------|------|-------|------|------|--|
| Charac | teristics | bol | | V _{DD} (V) | Min | Max | Min | Тур. | Max | Min | Max | Unit | |
| High-level output voltage | | V _{OH} | $ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$ | 5 | 4.95 | _ | 4.95 | 5.00 | _ | 4.95 | _ | | |
| | | | | 10 | 9.95 | _ | 9.95 | 10.00 | _ | 9.95 | _ | V | |
| | | | VIN = VSS, VDD | 15 | 14.95 | _ | 14.95 | 15.00 | _ | 14.95 | _ | | |
| 1 11 | | | I _{OUT} < 1 μΑ | 5 | _ | 0.05 | _ | 0.00 | 0.05 | _ | 0.05 | | |
| Low-level voltage | output | V _{OL} | $V_{IN} = V_{SS}, V_{DD}$ | 10 | _ | 0.05 | _ | 0.00 | 0.05 | _ | 0.05 | V | |
| | | | *IN *33, *DD | 15 | _ | 0.05 | _ | 0.00 | 0.05 | _ | 0.05 | | |
| | | | V _{OH} = 4.6 V | 5 | -0.73 | _ | -0.65 | -1.2 | _ | -0.58 | _ | | |
| | | | V _{OH} = 2.5 V | 5 | -2.40 | _ | -2.10 | -3.9 | _ | -1.90 | _ | mA | |
| Output hig | h current | Іон | V _{OH} = 9.5 V | 10 | -1.80 | _ | -1.65 | -2.5 | _ | -1.35 | _ | | |
| | | | V _{OH} = 13.5 V | 15 | -4.80 | _ | -4.30 | -8.0 | _ | -3.50 | _ | | |
| | | | $V_{IN} = V_{SS}, V_{DD}$ | | | | | | | | | | |
| | | l _{OL} | V _{OL} = 0.4 V | 5 | 3.8 | _ | 3.2 | 6.4 | _ | 2.9 | _ | | |
| Output low | / current | | V _{OL} = 0.5 V | 10 | 9.6 | _ | 8.0 | 16.0 | _ | 6.6 | _ | mA | |
| Output low | Output low current | | V _{OL} = 1.5 V | 15 | 28.0 | _ | 24.0 | 48.0 | _ | 20.0 | _ | 111/ | |
| | | | $V_{IN} = V_{SS}, V_{DD}$ | | | | | | | | | | |
| | | VIH | V _{OUT} = 0.5 V, 4.5 V | 5 | 3.5 | | 3.5 | 2.75 | _ | 3.5 | _ | V | |
| Input high | voltogo | | V _{OUT} = 1.0 V, 9.0 V | 10 | 7.0 | _ | 7.0 | 5.50 | _ | 7.0 | _ | | |
| input nign | voitage | | V _{OUT} = 1.5 V, 13.5 V | 15 | 11.0 | _ | 11.0 | 8.25 | _ | 11.0 | _ | V | |
| | | | $ I_{OUT} < 1 \mu A$ | | | | | | | | | | |
| | | | V _{OUT} = 0.5 V, 4.5 V | 5 | _ | 1.5 | _ | 2.25 | 1.5 | _ | 1.5 | | |
| Input low voltage | | VIL | V _{OUT} = 1.0 V, 9.0 V | 10 | _ | 3.0 | _ | 4.50 | 3.0 | _ | 3.0 | V | |
| | | | V _{OUT} = 1.5 V, 13.5 V | 15 | _ | 4.0 | _ | 6.75 | 4.0 | _ | 4.0 | | |
| | | $ I_{OUT} < 1 \mu A$ | | | | | | | | | | | |
| Input | "H" level | liH | V _{IH} = 18 V | 18 | _ | 0.1 | _ | 10 ⁻⁵ | 0.1 | _ | 1.0 | ۸ | |
| current | "L" level | I _{IL} | V _{IL} = 0 V | 18 | _ | -0.1 | _ | -10 ⁻⁵ | -0.1 | _ | -1.0 | μΑ | |
| | · | | | 5 | _ | 1 | _ | 0.002 | 1 | _ | 30 | | |
| Quiescent supply current | | I _{DD} | $V_{IN} = V_{SS}, V_{DD}$ (Note) | 10 | _ | 2 | _ | 0.004 | 2 | — | 60 | μΑ | |
| 3 | Carroni | | (Note) | 15 | _ | 4 | _ | 0.008 | 4 | _ | 120 | | |

Note: All valid input combinations.

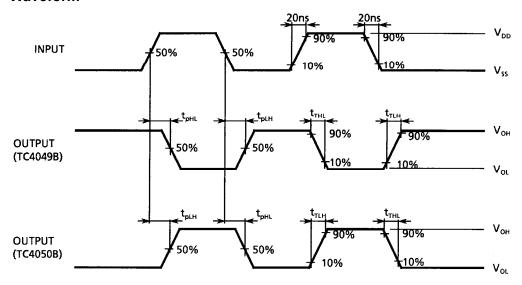


Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

| Characteristics | | Symbol | Test Condition | Min | Тур. | Max | Unit | |
|-----------------|--------------------------------------|------------------|----------------|---------------------|--------|------|-------|-------|
| | Characteristics | Symbol | | V _{DD} (V) | IVIIII | τyp. | IVIAX | Offic |
| Outr | out transition time | | | 5 | _ | 60 | 160 | |
| | to high) | t _{TLH} | _ | 10 | _ | 30 | 80 | ns |
| (IOW | to riigir) | | | 15 | | 25 | 60 | |
| Outr | out transition time | | | 5 | | 120 | 60 | |
| | n to low) | t _{THL} | _ | 10 | _ | 10 | 40 | ns |
| (High | 1 to low) | | | 15 | _ | 8 | 30 | |
| | Propagation delay time | | | 5 | _ | 60 | 120 | |
| _ | (low to high) | t _{pLH} | _ | 10 | _ | 35 | 65 | ns |
|)49B | (low to riigir) | | | 15 | _ | 30 | 50 | |
| TC4049B | Propagation delay time (high to low) | | | 5 | _ | 40 | 60 | |
| ' | | t _{pHL} | _ | 10 | _ | 20 | 30 | ns |
| | (riigir to low) | | | 15 | _ | 15 | 20 | |
| | Propagation delay time | | | 5 | _ | 50 | 130 | |
| | (low to high) | t _{pLH} | _ | 10 | _ | 30 | 70 | ns |
|)50E | | | | 15 | _ | 25 | 55 | |
| TC4050B | Propagation delay time (high to low) | | | 5 | _ | 30 | 70 | |
| | | t _{pHL} | _ | 10 | _ | 17 | 35 | ns |
| | (iligit to low) | | | 15 | _ | 14 | 25 | |
| Inpu | t capacitance | C _{IN} | _ | _ | 5 | 7.5 | pF | |

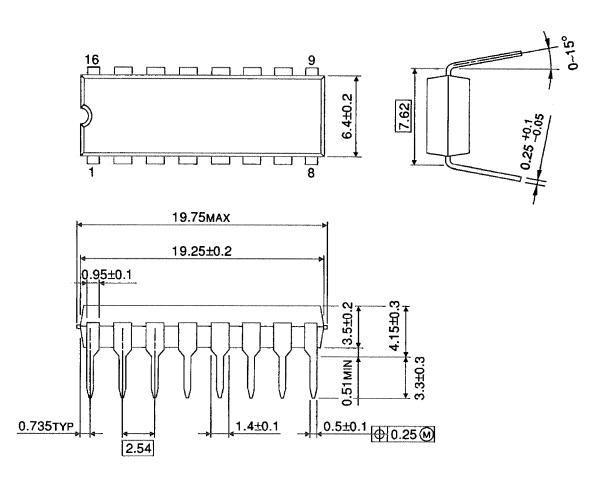
Waveform for Measurement of Dynamic Characteristics

Waveform



Package Dimensions

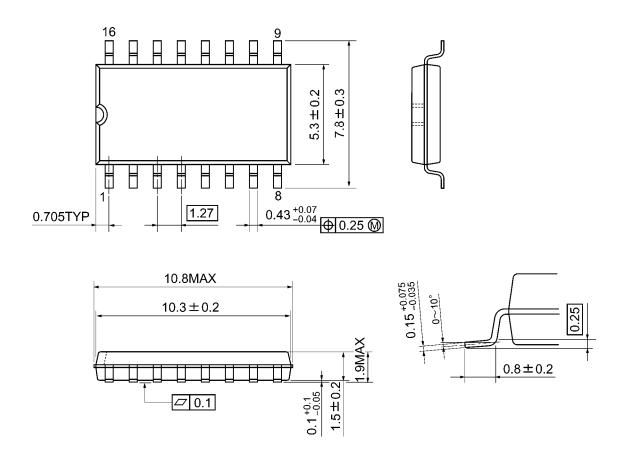
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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