



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	60mΩ @ V <sub>GS</sub> = 10V	3.4A
300	$100m\Omega$ @ $V_{GS}$ = $4.5V$	2.7A

## **Description**

This new generation MOSFET has been designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

- Backlighting
- DC-DC Converters
- Power Management Functions

# **Features and Benefits**

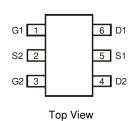
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

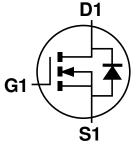
### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (Approximate)

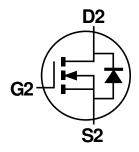


Top View





Q1 N-Channel MOSFET



Q2 N-Channel MOSFET

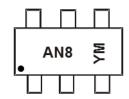
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3061SVT-7	TSOT26	3000 / Tape & Reel
DMN3061SVT-13	TSOT26	10000 / Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



AN8 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	201	9	2020		2021	20	22	2023		2024	2	2025
Code	G		Н			,	J	K		L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristi	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	I <sub>D</sub>	3.4 2.7	А
Maximum Continuous Body Diode Forward Current (	Is	1.4	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	20	Α

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	$P_{D}$	0.88	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	$R_{\theta JA}$	142	°C/W
Power Dissipation (Note 6)	P <sub>D</sub>	1.08	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 6)	$R_{\theta JA}$	116	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

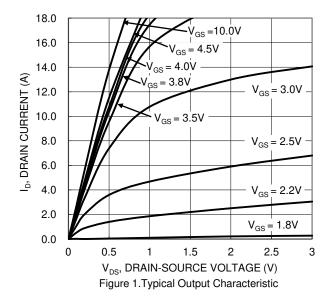
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	$BV_DSS$	30		_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	I	1.0	μΑ	$V_{DS} = 24V$ , $V_{GS} = 0V$		
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	1.3	1.8	V	$V_{DS} = V_{GS}, I_D = 250\mu A$		
			35	60		$V_{GS} = 10V, I_D = 3.1A$		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	41	100	mΩ	$V_{GS} = 4.5V, I_D = 2A$		
	, ,		51	200		$V_{GS} = 3.3V, I_D = 1.5A$		
Diode Forward Voltage	$V_{SD}$	_	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1A$		
DYNAMIC CHARACTERISTICS (Note 8)				•		•		
Input Capacitance	Ciss	_	278	_		V 45V V 0V		
Output Capacitance	Coss	_	44	_	рF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	29	_		1 = 1.0WH 12		
Gate Resistance	$R_g$	_	4.2	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$	_	3.5	_		$V_{DS} = 15V, V_{GS} = 4.5V, I_{D} = 3A$		
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	6.6	_	nC			
Gate-Source Charge	$Q_{gs}$	_	0.1	_	IIC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 3A$		
Gate-Drain Charge	$Q_{gd}$	_	1.3	_				
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.7	_				
Turn-On Rise Time	t <sub>R</sub>	_	97	_	ns	$V_{GS} = 10V, V_{DS} = 15V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	12.6	_	115	$R_G = 3\Omega$ , $R_L = 1.7\Omega$		
Turn-Off Fall Time	t <sub>F</sub>	_	51	_				

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
   Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.

  8. Guaranteed by design. Not subject to production testing.





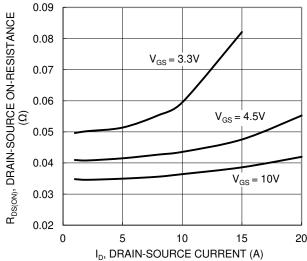


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

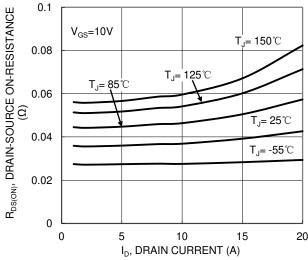
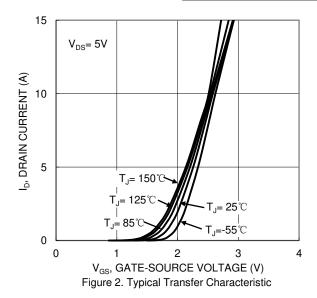
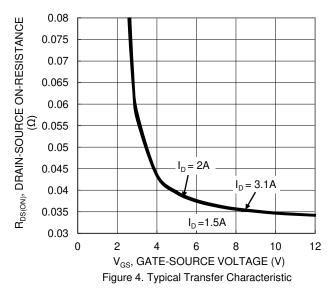


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





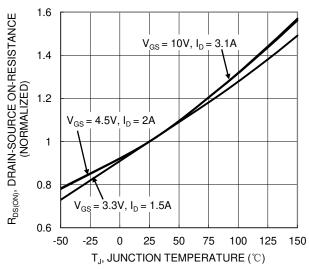


Figure 6. On-Resistance Variation with Temperature



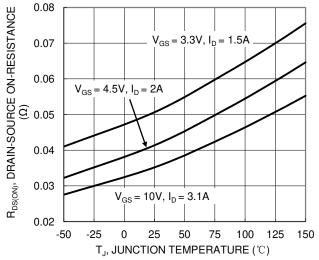
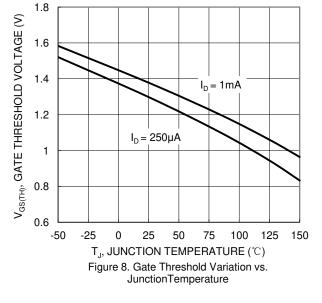
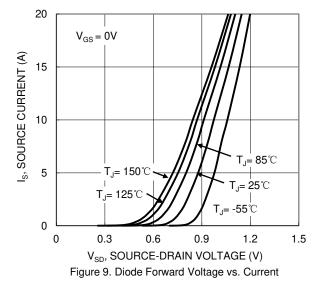
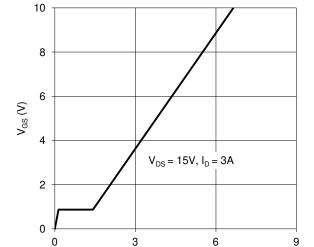


Figure 7. On-Resistance Variation with Temperature

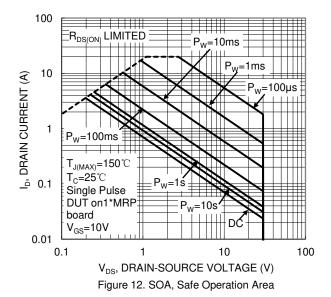






Qg (nC)

Figure 11. Gate Charge





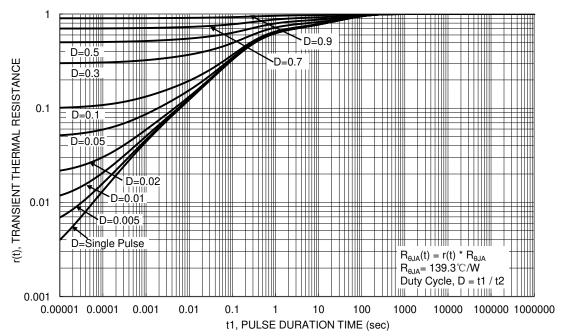


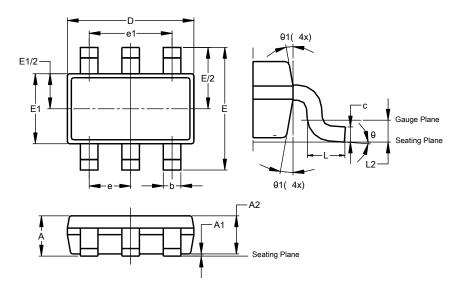
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TSOT26

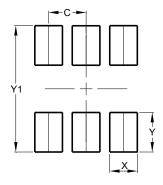


	TSOT26							
Dim	Min	Max	Тур					
Α	-	1.00	-					
<b>A</b> 1	0.010	0.100	-					
A2	0.840	0.900	-					
D	2.800	3.000	2.900					
E	2	2.800 BS	O					
E1	1.500	1.700	1.600					
b	0.300	0.450	-					
С	0.120	0.200	_					
е	0.950 BSC							
e1	1	.900 BS	С					
L	0.30 0.50 -		-					
L2	0.250 BSC							
θ	0°	8°	4°					
θ1	4°	12°	-					
All Dimensions in mm								

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3.199



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