

Test Procedure for the NCP4305 Put-In Board 2 x SO8FL DN05071

ON Semiconductor®



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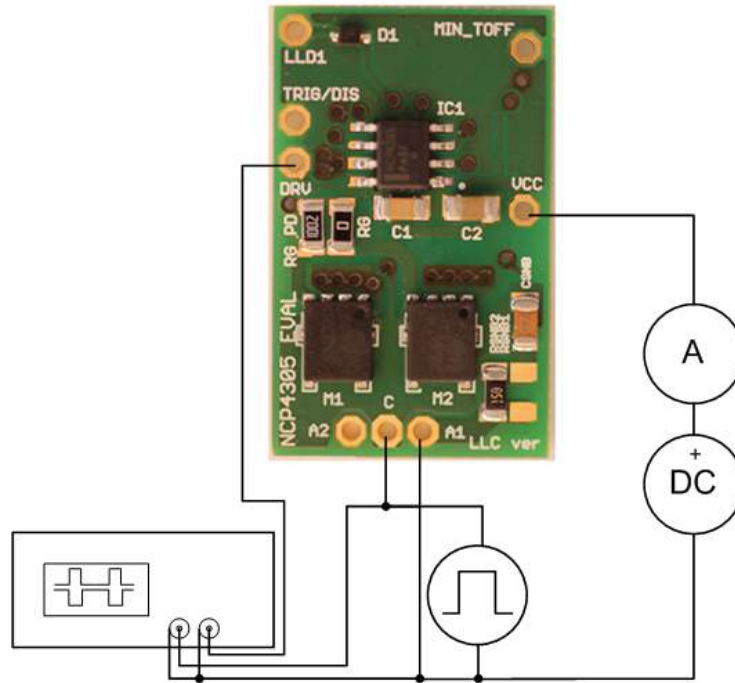


Figure 1: Test Setup

The following steps describe the test procedure for all these boards:

Required Equipment:

DC voltage source (e.g. STATRON 2229)	1pc
DC Amp-Meter (e.g. KEITHLEY 2000).....	1pc
Function generator (e.g. AFG3252)	1pc
2 channel oscilloscope	1pc

Test Procedure:

1. Connect the test setup as shown in figure 1.
2. Apply an supply voltage, $V_{CC} = 12\text{ V}$
3. Apply pulse from generator (pulse, $f = 100\text{ kHz}$, $DC = 50\%$, $V_{LOW} = -1\text{ V}$, $V_{HIGH} = 4\text{ V}$, output impedance = high Z)
4. Check that $I_{CC} = 20\text{ mA}$, waveforms look like in figure 2 (DRV pulses may oscillate between 470 ns and $5\text{ }\mu\text{s}$)

5. Set DC to 13%
6. Check that $I_{CC} = 1.6 \text{ mA}$, waveforms look like in figure 3 (no DRV pulses)
7. Turn off V_{CC}
8. End of the test

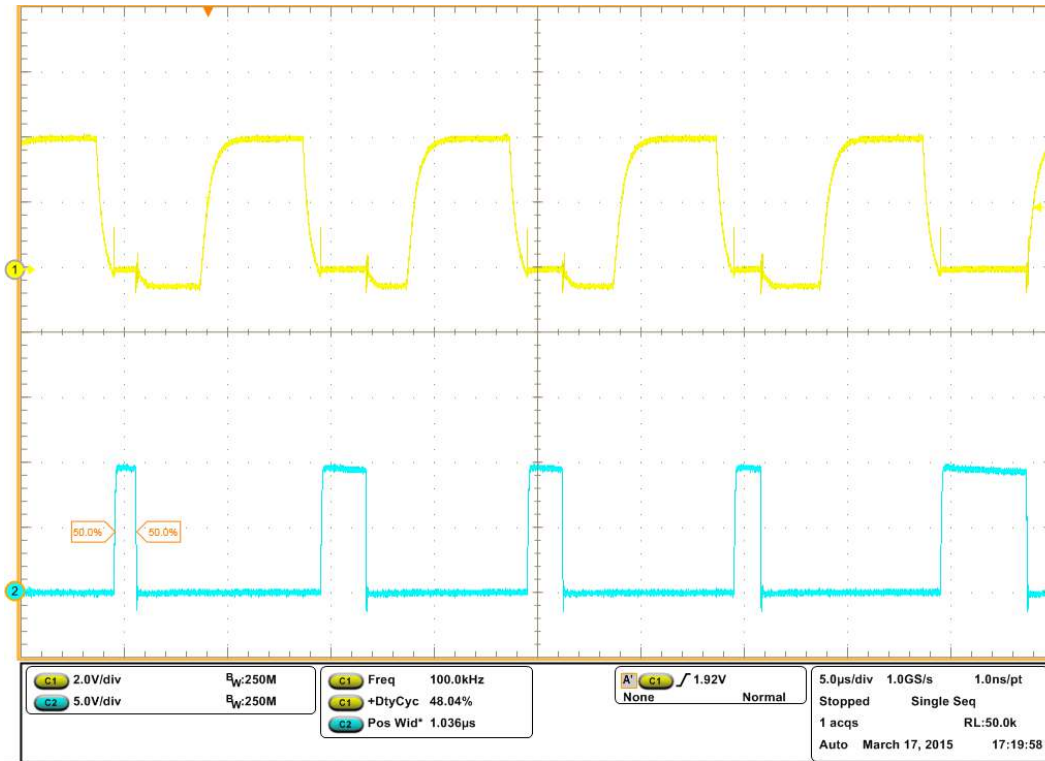


Figure 2: $V_{CC} = 12 \text{ V}$, $f = 100 \text{ kHz}$, $DC = 50\%$, $V_{LOW} = -1 \text{ V}$, $V_{HIGH} = 4 \text{ V}$

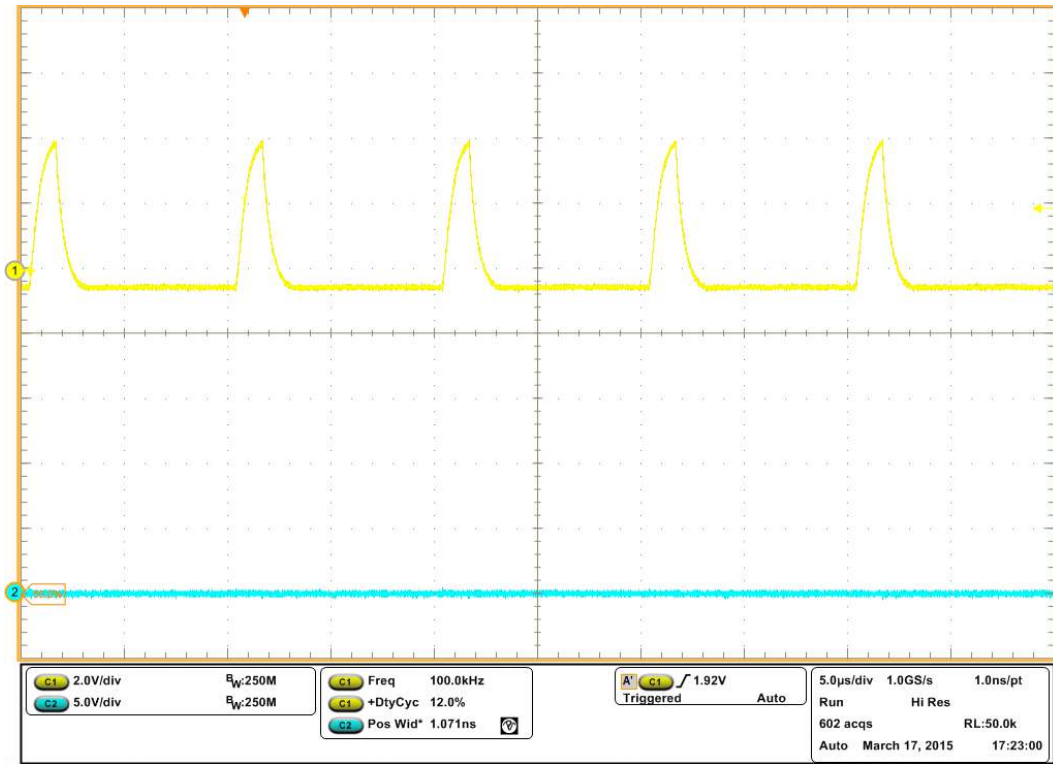


Figure 3: $V_{CC} = 12\text{ V}$, $f = 100\text{ kHz}$, $DC = 13\%$, $V_{LOW} = -1\text{ V}$, $V_{HIGH} = 4\text{ V}$