SEIKO EPSON CORPORATION

CRYSTAL OSCILLATOR (Programmable) OUTPUT: CMOS

SG-8101 series

• Frequency range : 0.67 MHz to 170 MHz (1 × 10^{-6} Step)

• Supply voltage : 1.62 V to 3.63 V

• Function : Output enable (OE) or Standby (\overline{ST})

• Frequency tolerance : ±15 × 10⁻⁶ (-40 °C to +85 °C)

 $\pm 20 \times 10^{-6}$, $\pm 50 \times 10^{-6}$ (-40 °C to +105 °C)

• PLL technology to enable short lead time

• Available field oscillator programmer "SG-Writer II"





Product Number

SG-8101CG: X1G005181xxxx00 SG-8101CE: X1G005211xxxx00 SG-8101CB: X1G005201xxxx00 SG-8101CA: X1G005191xxxx00



CA 2 mm 7.0 × 5.0 mm

CG	CE

2.5 × 2.0 mm 3.2 × 2.5 mm 5.0 × 3.2 mm

Specifications (characteristics)

Item	Symbol		Specifi	ications		Cor	nditions/Remarks
Supply voltage		1.80 V Typ. 2.50 V Typ. 3.30 V Typ.					
Supply voltage	Vcc	1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V		-
Output frequency range	fo	0.67 MHz to 170 MHz					
Storage temperature	T_stg		-40 °C to	+125 °C		Storage as single p	roduct.
Operating temperature	T use		-40 °C t	o +85 °C			-
Operating temperature	1_use			+105 °C			
		B: ±15 × 10 ⁻⁶			T_use = -40 °C to +		
Frequency tolerance*1	f_tol	C: ±20 × 10 ⁻⁶			T_use = -40 °C to +		
				0 × 10 ⁻⁶	T	T_use = -40 °C to +	-105 °C
		3.2 mA Max.	3.3 mA Max.	3.4 mA Max.	3.5 mA Max.	T_use = +105 °C	No load, fo = 20 MHz
Current consumption	Icc		пА Тур.	2.9 mA Typ.	3.0 mA Typ.	T_use = +25 °C	140 16dd, 16 20 WHZ
Current consumption	100	5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	T_use = +105 °C	No load, fo= 170 MHz
			пА Тур.	5.7 mA Typ.	6.8 mA Typ.	T_use = +25 °C	,
Output disable current	I_dis	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, $f_0 = 170$) MHz
Standby current	I std	0.9 μA Max.	1.0 µA Max.	1.5 µA Max.	2.5 μA Max.	T_use = +105 °C	ST = GND
	1_014	0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	T_use = +25 °C	o. one
Symmetry	SYM		45 % t	to 55 %		50 % V _{CC} Level	
							[mA]
	Vон		00.0/ \	/cc Min.		Rise/Fall time	Vcc *A *B *C *D
	VOH		90 % V	CC IVIIII.		Default (f _O > 40 MHz). Fast	
Output voltage							I _{OL} 2.5 3.5 4.0 5.0 I _{OH} -1.5 -2.0 -2.5 -3.0
(DC characteristics)						Default (f _O ≤ 40 MHz)	lo _L 1.5 2.0 2.5 3.0
,						Slow	Іон -1.0 -1.5 -2.0 -2.5
	VoL		10 % V	cc Max.		I _{OL} 1.0 1.5 2.0 2.5	
						62 V to 1.98 V, *B: 1.98 V to 2.20 V	
0 1 11 1 12						*C: 2.20 V to 2.80 V, *D: 2.70 V to 3.63 V	
Output load condition	L_CMOS			oF Max.			<u> </u>
Input voltage	V _{IH}			/ _{CC} Min.		OE or ST	
	V _{IL}		30 % V	_{cc} Max.		02 01 01	
Default		3.0 ns Max.			f _O > 40 MHz		
Rise time		6.0 ns Max.			f _O ≤ 40 MHz	20 % - 80 % Vcc.	
/Fall time Fast	tr/tf		3.0 ns Max.		f _O = 0.67 MHz to 17		
Slow		10.0 ns Max.			f ₀ = 0.67 MHz to 20		
Output disable time (OE)	tstp oe						time OE or ST pin crosses 30 %
Output disable time (ST)	tstp_st		1	µs Max.		V _{CC}	32 51 51 pill 0100000 00 70
Output enable time (OE)	tsta_oe	1 μs Max.			Measured from the	time OE pin crosses 70 % V _{CC}	
Output enable time (ST)	tsta_st		3 n	ns Max.			time ST pin crosses 70 % V _{CC}
Start-up time	t_str		3 n	ns Max.		Measured from the minimum value, 1.6	time V _{CC} reaches its rated
Frequency aging	f_age	This is ir	ncluded in frequer	ncy tolerance spe	cification.	+25 °C, first year	

^{*1} Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 1 year).

Pin description

	iii uescripiiori					
Pin	Name	I/O type	Function			
	OE	OE Input Output enable High*2: Specified frequency output from OUT pin	High*2: Specified frequency output from OUT pin			
	OL	iliput	Output enable	Low: Out pin is low (weak pull down), only output driver is disabled.		
1			High*2: Specified frequency output from OUT pin			
ST	ST	Input	Standby	Low: Out pin is low (weak pull down),		
				Device goes to standby mode. Supply current reduces to the least as I_std.		
2	GND	Power	Ground			
3	OUT	Output	Clock output			
4	Vcc	Power	Power supply			

^{*2} Please do not use the OE/ST terminal in the open state.



Product Name

SG-8101CG 25.000000MHz <u>TCHPA</u> 45678

1)Model 2)Package type

③Frequency ④Supply voltage (T: 1.8 V to 3.3 V Typ.)

5 Frequency tolerance 6 Operating temperature

7 Function 8 Rise/Fall time

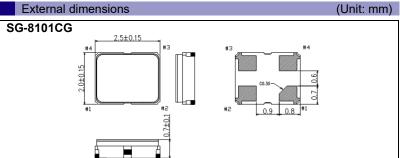
②Pa	ckage type
CG	2.5 mm × 2.0 mm
CE	3.2 mm × 2.5 mm
СВ	5.0 mm × 3.2 mm
CA	7.0 mm × 5.0 mm

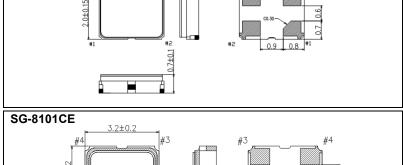
⑤Frequency tolerance / ⑥Operating temperature		
BG	±15 × 10 ⁻⁶ / -40 °C to +85 °C	
СН	±20 × 10 ⁻⁶ / -40 °C to +105 °C	
JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C	

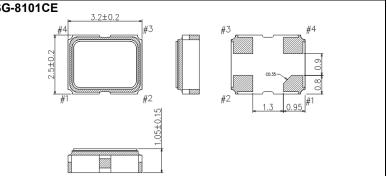
⑦Fui	nction
Р	Output enable
S	Standby

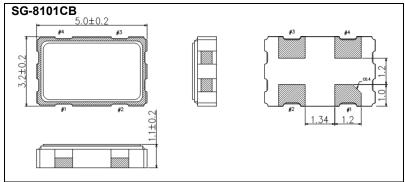
®Rise time/Fall time		
Α	Default	
В	Fast	
C*	Slow	

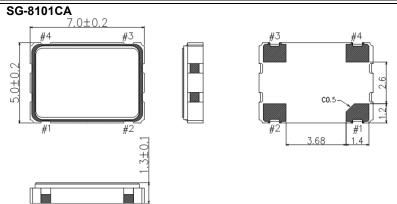
* Available only when fo ≤ 20 MHz

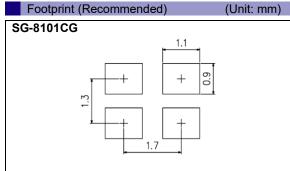


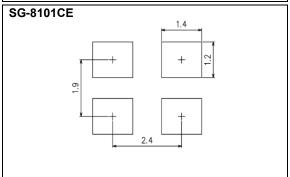


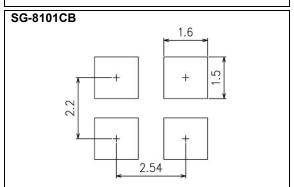


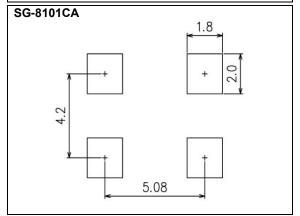












■Notes:

In order to achieve optimum jitter performance, the 0.1 µF capacitor between Vcc and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

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All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

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(Contains Pb in sealing glass, high melting temperature type solder or other.)







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