- Quad Versions of 'AS805B
- Offers High-Capacitive Drive Capability
- **Package Options include Plastic Small** Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil
- Dependable Texas Instruments Quality and Reliability

description

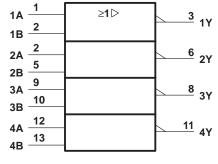
These devices contain four independent 2-input NOR drivers. They perform the Boolean functions $Y = \overline{A + B}$ or $Y = \overline{A} \cdot \overline{B}$ in positive logic.

The SN54AS1036A is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74AS1036A is characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT		
Α	В	Υ		
Н	Χ	L		
Х	Н	L		
L	L	Н		

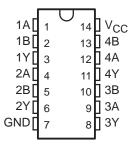
logic symbol †



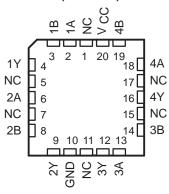
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54AS1036A . . . J PACKAGE SN74AS1036A . . . D OR N PACKAGE (TOP VIEW)

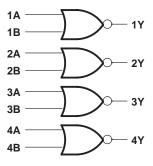


SN54AS1036A...FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram (positive logic)



SDAS244 - D2661, DECEMBER 1983 - REVISED MAY 1986

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}		7 V
Input voltage		7 V
Operating free-air temperature range:	SN54AS1036A	–55°C to 125°C
	SN74AS1036A	0°C to 70°C
Storage temperature range		_65°C to 150°C

recommended operating conditions

		SN54AS1036A		SN74AS1036A			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
٧ _{IH}	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			-40			-48	mA
lOL	Low-level output current			40			48	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS		SN5	SN54AS1036A			SN74AS1036A			
PARAMETER			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT	
VIK	$V_{CC} = 4.5 \text{ V},$	I _I = –18 mA			-1.2			-1.2	V	
	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2			V _{CC} -2				
V	$V_{CC} = 4.5 \text{ V},$	IOH = -3 mA	2.4	3.2		2.4	3.2		٦ ,, ١	
VOH	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -40 \text{ mA}$	2						V	
	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -48 \text{ mA}$				2				
V	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 40 \text{ mA}$		0.25	0.5				٧	
VOL	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 48 \text{ mA}$					0.35	0.5	V	
l _l	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA	
lіН	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ	
Ι _{ΙL}	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.5			-0.5	mA	
lo [‡]	V _{CC} = 5.5 V,	V _O = 2.25 V	-50		-200	-50		- 200	mA	
Iссн	$V_{CC} = 5.5 \text{ V},$	V _I = 0		4.3	7		4.3	7	mA	
ICCL	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		14	23		14	23	mA	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)		$C_L = 50$ $R_L = 50$ $T_A = Mi$	00 Ω, N to MAX	(UNIT
			SN54A	S1036A	SN74AS	1036A	
			MIN	MAX	MIN	MAX	
^t PLH	A or B	V	1	4.8	1	4.3	
^t PHL	AOIB	Ť	1	4.8	1	4.3	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, los.

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