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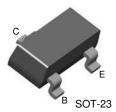


ON Semiconductor®

MMBT5401 PNP Epitaxial Silicon Transistor

Features

- PNP General-Purpose Amplifier
- This device is designed as a general-purpose amplifier and switch for applications requiring high voltage.



Ordering Information

Part Number	Marking	Package	Packing Method
MMBT5401	2L	SOT-23 3L	Tape and Reel, 3000 pcs, 7 inch Reel
MMBT5401-D87Z	2L	SOT-23 3L	Tape and Reel, 10000 pcs, 13 inch Reel

Absolute Maximum Ratings^{(1),(2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-150	V
V _{CBO}	Collector-Base Voltage	-160	V
V _{EBO}	Emitter-Base Voltage	-5.0	V
۱ _C	Collector Current - Continuous	-600	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150 $^{\circ}\text{C}.$
- 2. These are steady-state limits. ON Semiconductor should be consulted on applications involving pulsed or lowduty-cycle operations.

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
P _D	Total Device Dissipation	350	mW
	Derate Above 25°C	2.8	mW/°C
R _{0JA}	Thermal Resistance, Junction-to-Ambient	357	°C/W

Note:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

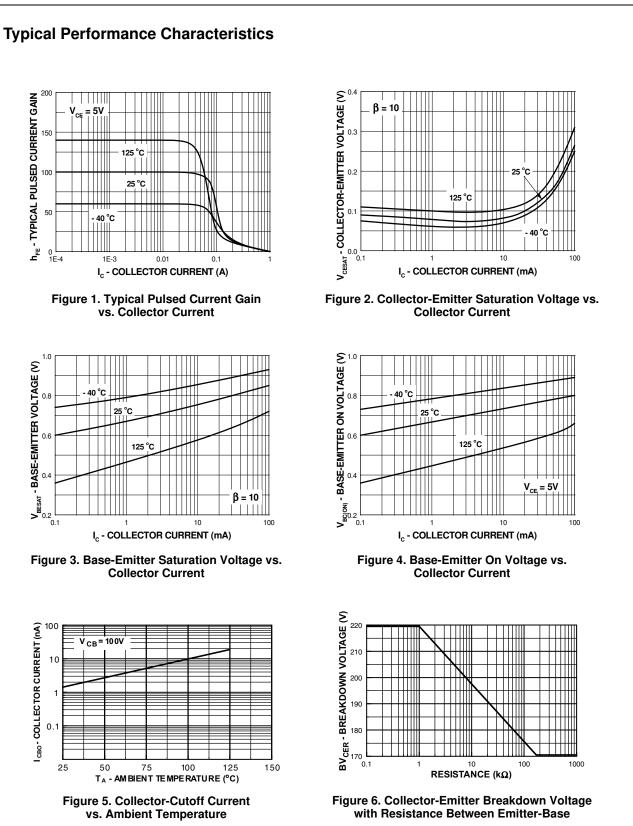
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage ⁽⁴⁾	I _C = -1.0 mA, I _B = 0	-150		V
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = -100 \ \mu A, I_{E} = 0$	-160		V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = -10 \ \mu A, \ I_{C} = 0$	-5.0		V
	Collector Cut-Off Current	$V_{CB} = -120 \text{ V}, \text{ I}_{E} = 0$		-50	nA
I _{CBO}		$V_{CB} = -120 \text{ V}, \text{ I}_{E} = 0,$ $T_{A} = 100^{\circ}\text{C}$		-50	μA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -3.0 \text{ V}, I_{C} = 0$		-50	nA
	DC Current Gain ⁽⁴⁾	I_{C} = -0.1 mA, V_{CE} = -5.0 V	50		
h _{FE}		I_{C} = -10 mA, V_{CE} = -5.0 V	60	240	
		$I_{C} = -50 \text{ mA}, V_{CE} = -5.0 \text{ V}$	50		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽⁴⁾	$I_{\rm C}$ = -10 mA, $I_{\rm B}$ = -1.0 mA		-0.2	- V
VCE(Sal)		$I_{\rm C}$ = -50 mA, $I_{\rm B}$ = -5.0 mA		-0.5	
V (act)	Base-Emitter Saturation Voltage ⁽⁴⁾	$I_{\rm C}$ = -10 mA, $I_{\rm B}$ = -1.0 mA		-1.0	- V
V _{BE} (sat)		$I_{C} = -50 \text{ mA}, I_{B} = -5.0 \text{ mA}$		-1.0	
f _T	Current Gain Bandwidth Product	$I_{C} = -10 \text{ mA}, V_{CE} = -10 \text{ V},$ f = 100 MHz	100	300	MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0,$ f = 1 MHz		6.0	pF
N _F	Noise Figure	$\begin{array}{l} {\sf I}_{C}=-250 \; \mu {\sf A}, \; {\sf V}_{CE}=-5.0 \; {\sf V}, \\ {\sf R}_{S}=1.0 \; {\sf k}\Omega, \\ {\sf f}=10 \; {\sf Hz} \; to \; 15.7 \; {\sf kHz} \end{array}$		8.0	dB

Note:

4. Pulse test: Pulse width $\leq 300~\mu s,$ duty cycle $\leq 2\%$



MMBT5401 — PNP Epitaxial Silicon Transistor



0.01

I_c - COLLECTOR CURRENT (A)

0.1

h_{FE} - TYPICAL PULSED CURRENT GAIN

100

0 L 1E-4

= 5V

40 °C ÌΨ

1E-3

125 °C

25 °C

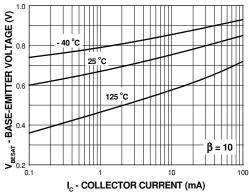
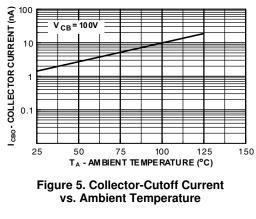
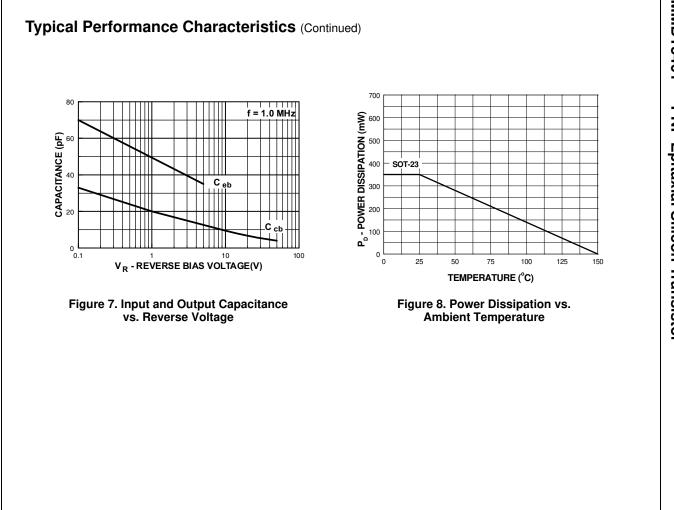
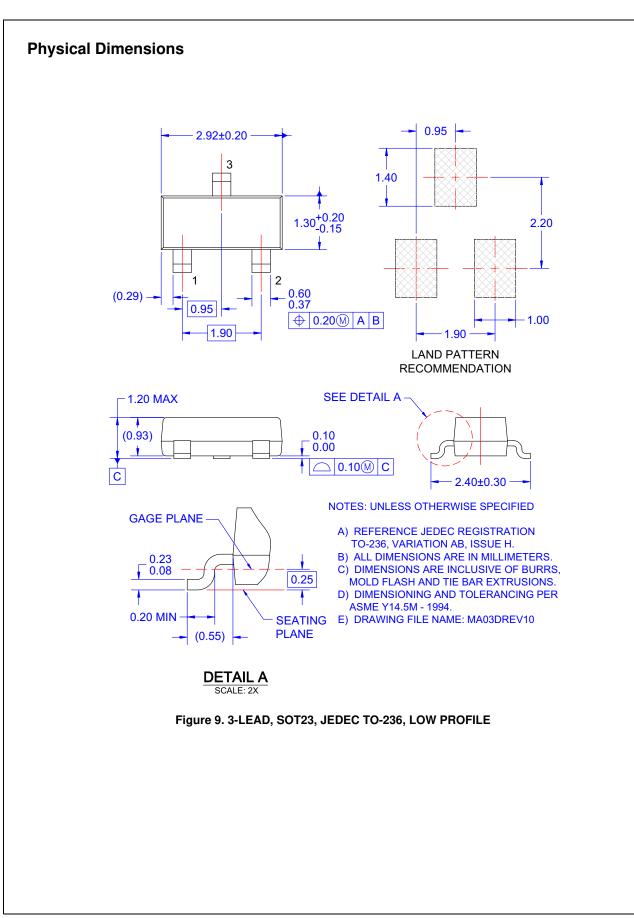


Figure 3. Base-Emitter Saturation Voltage vs. **Collector Current**







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