

### KSD1273

**High h**<sub>FE</sub>, **AF Power Amplifier**• "Full PAK" Package for Simplified Mounting Only by a Screw, Requires no Insulator.



### **NPN Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	80	V
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>C</sub>	Collector Current (DC)	3	Α
I <sub>CP</sub>	Collector Current (Pulse)	6	Α
I <sub>B</sub>	Base Current	1	Α
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)	2	W
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	40	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Voltage	$I_C = 25 \text{mA}, I_B = 0$	60			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 80V, I_{E} = 0$			100	μА
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = 60V, I_{B} = 0$			100	μА
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 6V, I_{C} = 0$			100	μА
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 4V, I_{C} = 0.5A$	500		2500	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.05A$			1	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 12V, I_{C} = 0.2A$		30		MHz

### **h**<sub>FE</sub> Classification

Classification	Q	Р	0
h <sub>FE</sub>	500 ~ 1000	800 ~ 1500	1200 ~ 2500

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# **Typical Characteristics**

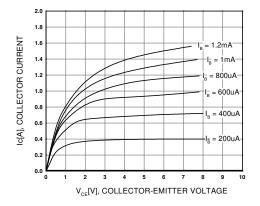


Figure 1. Static Characteristic

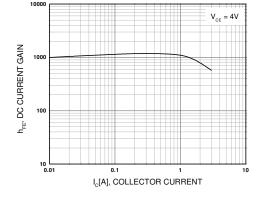


Figure 2. DC current Gain

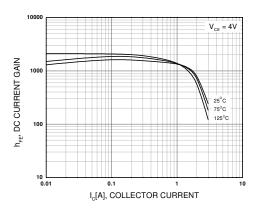


Figure 3. DC current Gain

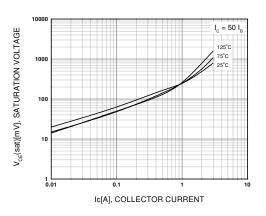


Figure 4. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

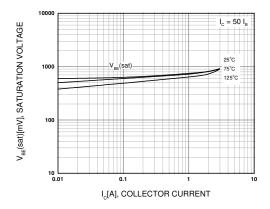


Figure 5. Collector-Base Saturation Voltage

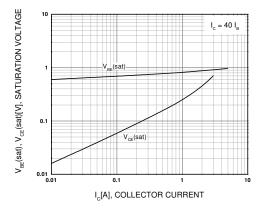


Figure 6. Base-Emitter Saturation Voltage

# Typical Characteristics (Continued)

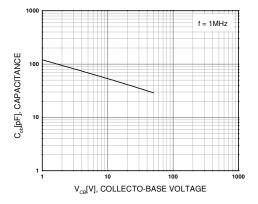


Figure 7. Collector Output Capacitance

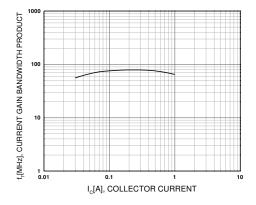


Figure 8. Current Gain Bandwidth Product

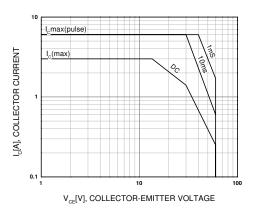


Figure 9. Safe Operating Area

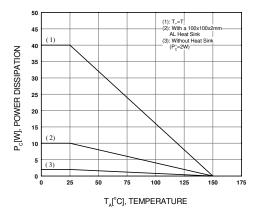
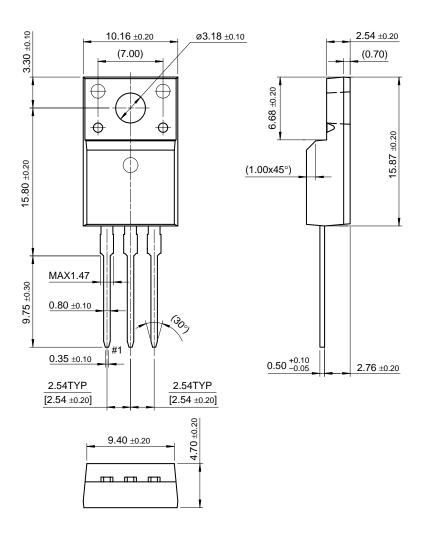


Figure 10. Power Derating

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# **Package Demensions**

# TO-220F



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