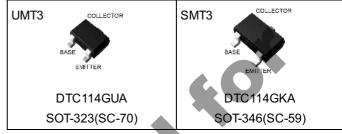


NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
$V_{\sf CEO}$	50V
I _C	100mA
R	10kΩ

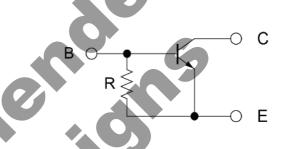
Outline



Features

- 1) Built-In Biasing Resistor
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Complementary PNP Types: DTA114G series
- 5) Lead Free/RoHS Compliant.

•Inner circuit



B: BASE

C: COLLECTOR

E: EMITTER

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC114GUA	UMT3	2021	T106	180	8	3000	K24
DTC114GKA	SMT3	2928	T146	180	8	3000	K24

• Absolute maximum ratings ($T_a = 25$ °C)

Parameter			Values	Unit
Collector-base voltage			50	V
Collector-emitter voltage			50	V
Emitter-base voltage			5	V
Collector current	Collector current		100	mA
Down dissinction	DTC114GUA	P _D *1	200	
Power dissipation	DTC114GKA	LD.	200	mW
Junction temperature		T _j	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = 720μΑ	5	-	-	٧
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	0.5	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	300	-	580	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C / I _B = 10mA / 0.5mA	-	-	0.3	٧
DC current gain	h _{FE}	$V_{CE} = 5V, I_{C} = 5mA$	30	-	-	-
Emitter-base resistance	R	-	7	10	13	kΩ
Transition frequency	f _T *2	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz

^{*1} Each terminal mounted on a reference footprint

^{*2} Characteristics of built-in transistor

● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded emitter propagation characteristics

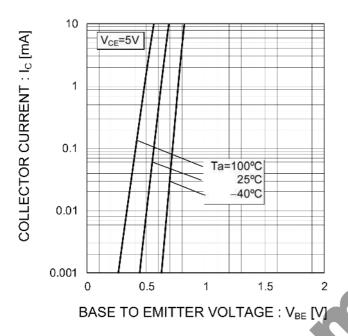


Fig.2 Grounded emitter output characteristics

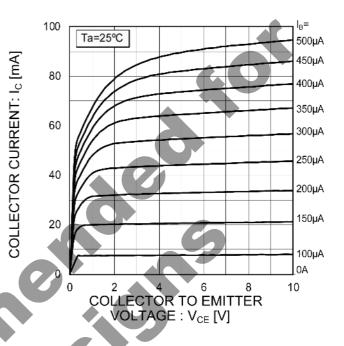


Fig.3 DC Current gain vs. Collector Current

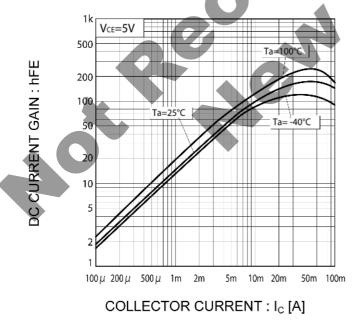
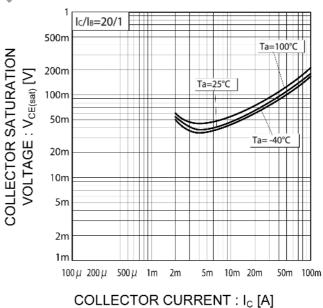


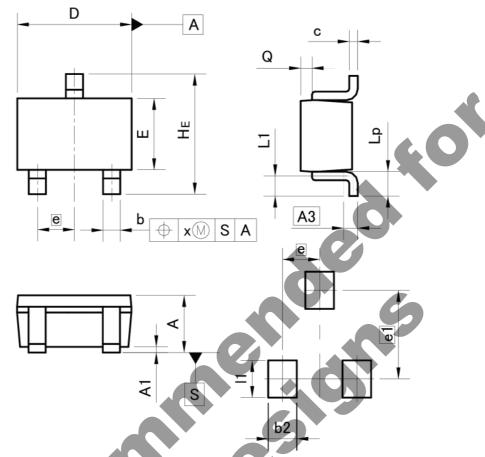
Fig.4 Collector-emitter saturation voltage vs.

Collector Current



Dimensions

UMT3



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIM	MILIMETERS		HES	
DIM	MIN	MAX	MIN	MAX	
A	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.	25	0.0	10	
b	0.15	0.30	0.006	0.012	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.026		
HE	2.00	2.20	0.079	0.087	
L1	0.20	0.50	0.008	0.020	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
×	-	0.10	_	0.004	

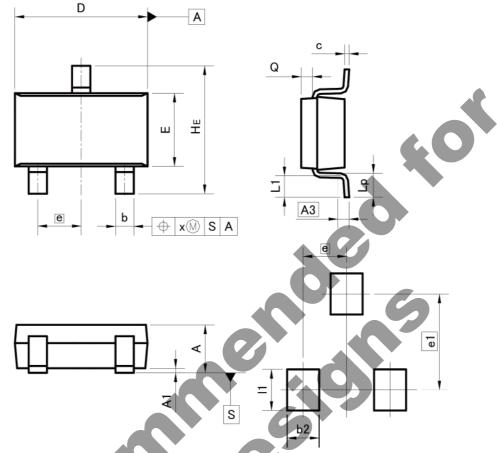
DIM	MILIMETERS		INCHES		
	MIN	MAX	MIN	MAX	
b2	_	0.50		0.020	
e1	1.55		0.0	061	
11	-	0.65	_	0.026	

Dimension in mm/inches



Dimensions

SMT3



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.2	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.95		0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
X	2	0.10	K.	0.004	
У	46	0.10	100	0.004	
DIM	MILIM	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
b2		0.60	-	0.024	
e1	2 10		0.0	83	

Dimension in mm/inches

11



0.035

0.90

Notes

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