

ECS-3951M/3953M Series

SMD Clock Oscillator

The ECS-3951M (5V) and ECS 3953M (3.3V) Series are miniature, crystal controlled, low current clock oscillators in a ceramic SMD package. Package is seam welded with a metal lid. The low profile package is ideal for today's advanced portable PC and instrumentation designs.

Request a Sample



OPERATING CONDITIONS / ELECTRICAL CHARACTERISTICS

ECS-3951M/3953M

- 3.3 or 5.0V version
- Low Power Consumption
- Standby function
- Seam welded package
- Tape & Reel (1,000 pcs)
- PbFree/RoHS Compliant

| Parameters | Conditions | ECS-3951M (5V) | | | ECS-3953M (3.3V)* | | | |
|-----------------------|-------------------|----------------|------|-------|-------------------|------|-------|-------|
| | | MIN | TYP | MAX | MIN | TYP | MAX | Units |
| Frequency Range | | 1.000 | | 125.0 | 1.000 | | 125.0 | MHz |
| Temperature Range | Operating | -10 | | +70 | -10 | | +70 | °C |
| | Storage | -55 | | +125 | -55 | | +125 | °C |
| Supply Voltage | | +4.5 | +5.0 | +5.5 | +3.0 | +3.3 | +3.6 | V DC |
| Frequency Stability** | Standard | -100 | ±40 | +100 | -100 | ±40 | +100 | PPM |
| | Option (B) | -50 | | +50 | -50 | | +50 | PPM |
| | Option (C) | -25 | | +25 | -25 | | +25 | PPM |
| Input Current | 1.0 ~ 36.0 MHz | | | 20 | | | 15 | mA |
| | 36.1 ~ 70.0 MHz | | | 55 | | | 25 | mA |
| | 70.1 ~ 100.0 MHz | | | 60 | | | 30 | mA |
| | 100.1 ~ 125.0 MHz | | | 65 | | | 30 | mA |
| Output Symmetry | @ ½ VCC Level | 40/60 | | 60/40 | 40/60 | | 60/40 | % |
| | @ ½ VCC Level | | | 45/55 | | | 45/55 | % |
| | (T Option) | | | | | | | |
| Rise and Fall Times | 1.0 ~ 70.0 MHz | | | 15 | | | 5 | nS |
| | 70.1 ~ 125.0 MHz | | | 5 | | | 5 | nS |
| Logic "0" Level | VCC x 0.1V max. | | | | | | | |
| Logic "1" Level | VCC x 0.9V min. | | | | | | | |
| Load | HCMOS | | | 30 | | | 15 | pF |
| Start-Up Time | 1.0 ~ 36.0 MHz | | | 5 | | | 5 | ms |
| | 36.1 ~ 70.0 MHz | | | 10 | | | 10 | ms |
| | 70.1 ~ 125.0 MHz | | | 15 | | | 15 | ms |
| Output Current (IOL) | VOL=0.5V/0.33 | | | 4 | | | 4 | mA |
| (IOH) | VOL=4.5V/2.97V | | | -4 | | | -4 | mA |
| Enable/Disable Time | | | | 100 | | | 100 | ns |

^{*}ECS-3953M is also compatible with a supply voltage of $+3.0 \text{V DC} \pm 0.3 \text{V}$

Note: A 0.01 µF bypass capacitor should be placed between VCC (Pin 4) and GND (Pin2) to minimize power line noise.

Part Numbering Guide: Example ECS-3951M-500-B-TR

ECS -Series Frequency Abbreviation - Stability Tolerance -Temperature -**Output Symmetry Packaging** Blank = $-10 \sim 70^{\circ}$ C 3951M +5V $A = \pm 100 \text{ ppm}$ **ECS** 500 = 50 MHzBlank = 60/40Tape & Reel $M = -20 \sim +70^{\circ}C$ T= 45/55 3953M +3.3V $B = \pm 50 \text{ ppm}$ $N = -40 \sim +85^{\circ}C$ 1K/Reel $C = \pm 25 \text{ ppm}$ U = -55 ~ +125°C $D = \pm 20 \text{ ppm}$

^{**}Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging shock and vibration.

^{***}An internal pullup resistor from pin 1 to 4 allows active output if pin 1 is left open.



ECS-3951M/3953M Series SMD Clock Oscillator



Package Dimensions (mm)

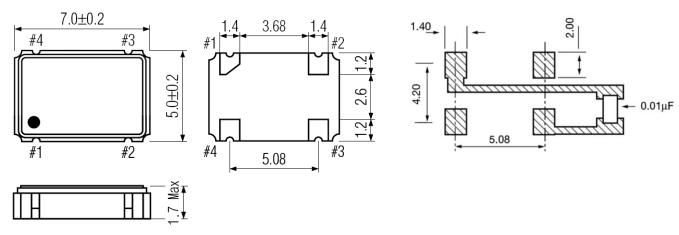


Figure 1) Top, Side, and Bottom views

Figure 2) Land Pattern

| Pin Connections | | | | |
|-----------------|--------------|--|--|--|
| #1 | Tri-State*** | | | |
| #2 | Ground | | | |
| #3 | Output | | | |
| #4 | VCC | | | |

| ECS-3951M/3953M Standby Control Voltage | | | | |
|---|---------------------|--|--|--|
| Pin #1 = Open*** | #3 = Oscillation | | | |
| Pin #1 = +2.2V Min. | #3 = Oscillation | | | |
| Pin #1 = 0.8V Max | #3 = High Impedance | | | |