

# APPROVAL SHEET

## MULTILAYER CERAMIC CAPACITORS

Low Profile Series

0402 to 1210 Sizes

X7R & X5R Dielectrics

Halogen Free & RoHS Compliance



\*Contents in this sheet are subject to change without prior notice.



## 1. DESCRIPTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

WTC TT series MLCC is used in product having thickness concerned generally have high capacitance and thinner product thickness. The high dielectric constant material X7R and X5R are used for this series product.

## 2. FEATURES

- a. Standard size with thin thickness.
- b. Small size with high capacitance.
- c. Capacitor with lead-free termination (pure Tin).

## 3. APPLICATIONS

- a. For LCD panels.
- b. For PCMCA cards.
- c. For IC packaging and modules.
- d. Any thickness concerned products.

## 4. HOW TO ORDER

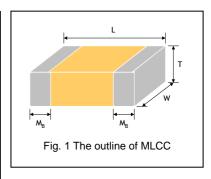
<u>TT</u>	<u>15</u>	<u>X</u>	<u>475</u>	<u>M</u>	<u>6R3</u>	<u>C</u>	Ī
<u>Series</u>	Size	<u>Dielectric</u>	Capacitance /	<u>Tolerance</u>	Rated voltage	<u>Termination</u>	<u>Packaging</u>
			<b>展展</b>	R &			
TT=Low profile	<b>15</b> =0402 (1005)	<b>B</b> =X7R	Two significant	<b>K</b> =±10%	Two significant	C=Cu/Ni/Sn	T=7" reeled
	<b>18</b> =0603 (1608)	<b>X</b> =X5R	digits followed by	M=±20%	digits followed by		G=13" reeled
	<b>21</b> =0805 (2012)	1774/	no. of zeros. And	ZF.	no. of zeros. And		
	<b>31</b> =1206 (3216)		R is in place of		R is in place of		
	<b>32</b> =1210 (3225)		decimal point.	_ ^ \	decimal point.		
				DA			
		8	eg.: PASSIVE SYS	TEM ALLIANCE	6R3=6.3 VDC		
		19	475=47x10 <sup>5</sup>		<b>100</b> =10 VDC		
		COPYRIGH	=4,700,000pF		<b>160</b> =16 VDC		
		132	=4.7µF	, c	<b>250</b> =25 VDC		
		C	Mar Col	970	<b>500</b> =50 VDC		
			Show	Ogy	<b>101</b> =100 VDC		

# **PSA**

Approval Sheet

## **5. EXTERNAL DIMENSIONS**

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol		M <sub>B</sub> (mm)
0402 (1005)	1.00±0.2	0.5±0.2	0.30±0.03	L	0.25±0.10
0603 (1608)	1.6+0.15/-0.10	0.8+0.15/-0.10	0.50±0.10	Н	0.40±0.15
0805 (2012)	2.00±0.20	1.25±0.20	0.85±0.10	Т	0.50±0.20
4000 (0040)	2.20.0.20	4.00.000	0.85±0.10	Т	0.00.000
1206 (3216)	3.20±0.20	1.60±0.20	1.15±0.15	J	0.60±0.20
4040 (200E)	2 20 . 0 20	2.50.0.20	0.85±0.10	Т	0.75 . 0.25
1210 (3225)	3.20±0.30	2.50±0.20	2.00±0.20	K	0.75±0.25



## **6. GENERAL ELECTRICAL DATA**

Dielectric	X7R	X5R					
Size	0402, 0603, 08	305, 1206, 1210					
Capacitance range*	1μF to 10μF	0.22μF to 22μF					
Capacitance tolerance**	K (±10%), M (±20%)						
Rated voltage (WVDC)	10V, 16V, 25V, 50V, 100V	6.3V, 10V, 16V, 25V					
Operating temperature	-55 to +125℃	-55 to +85℃					
Capacitance characteristic	10000000000000000000000000000000000000	5%					
Termination	Ni/Sn (lead-fre	ee termination)					

<sup>\*</sup> Measured at 1.0±0.2Vrms, 1.0kHz±10%, 30~70% related humidity, 25°C ambient temperature for X7R, X5R.

<sup>\*</sup> Reflow soldering process only is recommended.

<sup>\*\*</sup> Preconditioning for Class II MLCC: Perform a heat treatment at 150±10°C for 1 hour, then leave in a mbient condition for 24±2 hours before measurement.

## 7. CAPACITANCE RANGE

## 7-1 X7R dielectric

	Dielectric	X7R										
	Size		08	05			12	06			1210 16 100 K	
Rated voltage (VDC)		10	16	25	50	10	16	25	50	10	16	100
	1.0µF (105)							Т				
	1.5µF (155)											
ဗို	2.2µF (225)		Т	Т					Т			K
ij	3.3µF (335)											
ခွင	4.7μF (475)	Т						Т				
Capacitance	6.8µF (685)											
ပ	10μF (106)					Т						
	22μF (226)											

## 7-2 X5R dielectric

	Dielectric								)	K5R								
	Size		0402		06	03		08	05				1206				1210	
Rate	ed voltage (VDC)	6.3	10	25	10	16	6.3	10	16	25	6.3	10	16	25	50	10	16	25
	0.22uF (224)			L	Н	Н												
	0.47uF (474)	L		L														
	1.0µF (105)	L			Н	Н		Т	Т	Т		Т	Т	T	Т			
မ္ပ	1.5µF (155)							Т	Т			Т	Т	Т				
aŭ	2.2µF (225)	L					H	T	T	Т		Т	Т	Т	Т			
댢	3.3µF (335)						K-7	$\exists$	信	3		Т	Т	Т		Т		
Capacitance	4.7µF (475)	L			Н	日世	7)7	T	TR	7 <b>\$</b>		Т	Т	Т		Т		
ပိ	6.8µF (685)				/1/	TE	يلما	配4	$^{\downarrow}$	1	11							
	10μF (106)				1		メイ	八下し	U IS	PA	7	J/T		Т		Т		Т
	22uF (226)				1/ Jym	11	^T	Т		ZZ	1	1/	Т				Т	
	47uF (476)				LITTI	7				N'-	> T<	17						

## **8. PACKAGING STYLE AND QUANTITY**

Size	Thickness May (m)	n)/Symbol	7" r	eel
Size	Thickness Max (mi	ii)/Sylliboi	Paper tape	Plastic tape
0402 (1005)	0.33	LIVIL	5 15k	-
0603 (1608)	0.60	CHINOHOGY C	ARDORANOW 4k	-
0805 (2012)	0.95	- Troi C	4k	-
1006 (2016)	0.95	Т	4k	-
1206 (3216)	1.30	J	-	3k
1210 (3225)	0.95	Т	-	3k
1210 (3225)	2.00	K	-	1k

Unit: pieces



## 9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements
1.	Visual and Mechanical		* No remarkable defect.  * Dimensions to conform to individual specification sheet.
	Capacitance	* Test temp.: Room Temperature. Cap≤10µF, 1.0±0.2Vrms, 1kHz±10%	* Shall not exceed the limits given in the detailed spec.  X7R/X5R:
3.	Q/ D.F. (Dissipation Factor)	Cap>10µF, 0.5±0.2Vrms, 120Hz±20%** ** Test condition: 0.5±0.2Vrms $\cdot$ 1KHz±10% TT18X $\ge$ 475(10V) , TT15X series *Before initial measurement (Class II only): To apply deat 150°C for 1hr then set for 24±2 hrs at room temp .	Rated vol.       D.F.         100V       ≤5%         50V, 25V, 16V, 10V       ≤10%         6.3V       ≤15%
	Dielectric Strength	* To apply voltage: 250% rated voltage.  * Duration: 1 to 5 sec.  * Charge and displaying gurrent less than 50mA	* No evidence of damage or flash over during test.
	Insulation Resistance	* Charge and discharge current less than 50mA.  * Test temp.: Room Temperature.  * To apply rated voltage for max. 120 sec.	≥10GΩ or RxC≥100Ω-F whichever is smaller.
6.	Temperature Coefficient	With no electrical load.   T.C.   Operating Temp   X7R   -55~125°C at 25°C   X5R   -55~85°C at 25°C     To apply de-aging at 150°C for 1hr then set for 24±2 hrs room temp.   *Measurement voltage for Class II:	v
7.	Adhesive Strength of	* Pressurizing force : 5N (≤0603) and 10N (>0603) * Test time: 10±1 sec.	* No remarkable damage or removal of the terminations.
8.	Termination Vibration Resistance	* Vibration frequency: 10~55 Hz/min.  * Total amplitude: 1.5mm  * Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)  * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs room temp.  * Cap./DF(Q) Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.	
9.	Solderability	* Solder temperature: 235±5℃ * Dipping time: 2±0.5 sec.	95% min. coverage of all metalized area.
10.	Bending Test	* The middle part of substrate shall be pressurized by m of the pressurizing rod at a rate of about 1 mm per second the deflection becomes 1 mm and then the pressure shamaintained for 5±1 sec.  * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs room temp.  * Measurement to be made after keeping at room temp. 24±2 hrs.	ond until   Cap change:  X7R/X5R: within ±12.5%  (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before



## **Multilayer Ceramic Capacitors**

#### **Approval Sheet**

11	. Resistance to	* Solder temperature: 260±5°C	* No remarkable damage.
	Soldering Heat	* Dipping time: 10±1 sec * Preheating: 120 to 150°C for 1 minute before imme rse the	* Cap change:
		capacitor in a eutectic solder.	X7R/X5R: within ±7.5%
		*Before initial measurement (Class II only): To apply de-aging	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.
		lat 150°C for 1nr then set for 24±2 hrs at room temp.	* OFO/ may leaching an application
		150 C for this then sector 24±2 hrs at room temp.	

 $<sup>^{\</sup>star}$  "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.





## **Multilayer Ceramic Capacitors**

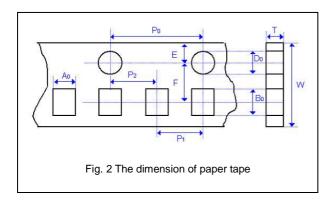
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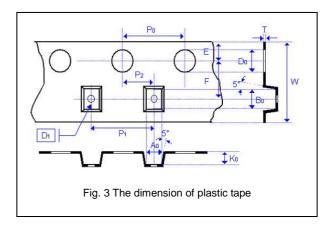
No.	Item	Test Condition	Requirements
12.	Temperature	* Conduct the five cycles according to the temperatures and	* No remarkable damage.
	Cycle	time.	* Cap change :
		Step Temp. (℃) Time (min.)	X7R/X5R: within ±7.5%
		1 Min. operating temp. +0/-3 30±3	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.
		2 Room temp. 2~3	
		3 Max. operating temp. +3/-0 30±3	
		4 Room temp. 2~3	
		* Before initial measurement (Class II only): To apply de-agir	ng
		at 150°C for 1hr then set for 24±2 hrs at room temp.	
		* Cap. / DF(Q) / I.R. Measurement to be made after de-aging	9
4.0		at 150℃ for 1hr then set for 24±2 hrs at room temp.	*No remarkable damage.
13.	Humidity	* Test temp.: 40±2℃	*Cap change: X7R/X5R: within ±25%
	(Damp Heat)	* Humidity: 90~95% RH	*Q/D.F. value:
	Steady State	* Test time: 500+24/-0hrs.	X7R/X5R:
		* Before initial measurement (Class II only): To apply de-agir	
		at 150°C for 1hr then set for 24±2 hrs at room temp.	100V ≤7.5%
		* Cap. / DF(Q) / I.R. Measurement to be made after de-aging	
		at 150℃ for 1hr then set for 24±2 hrs at room temp.	10V ≤20% 50V, 6.3V ≤30%
4.4		T 10.000	*I.R.: 1GΩ or RxC ≥ 10 Ω-F whichever is smaller.  *No remarkable damage.
14.	Humidity	* Test temp.: 40±2°C	*Cap change: X7R/X5R: within ±25%
	(Damp Heat)	* Humidity: 90~95%RH	*Q/D.F. value:
	Load	163t tillle: 500+24/-0 1ll3.	X7R/X5R:
		* To apply voltage : Rated voltage.	Rated vol. D.F.
		* Before initial measurement (Class II only): To apply de-agir at 150°C for 1hr then set for 24±2 hrs at room temp.	ng 100V ≤7.5% 25V, 16V ≤15%
		* Cap. / DF(Q) / I.R. Measurement to be made after de-aging	
		at 150℃ for 1hr then set for 24±2 hrs at room temp.	50V, 6.3V ≤30%
		at 130 C for the then set for 24±2 his at 100m temp.	*I.R.: $500M\Omega$ or $RxC \ge 5 \Omega$ -F whichever is smaller.
15	High	* Test temp. : PASSIVE SYSTEM A	*No remarkable damage.
13.	Temperature	X7R: 125±3℃	*Cap change: X7R/X5R: within ±25%
	•	X5R: 85±3℃	*Q/D.F. value:
	Load	* Test time: 1000+24/-0 hrs.  * To apply voltage: 150% of rated voltage.	X7R/X5R:
	(Endurance)	**100% of rated voltage for below range.	Rated vol. D.F.
		Size Dielectric Rated Capacitance	100V ≤7.5%
		Voltage range	25V, 16V ≤15%
		TT15   X5R   6.3V   C≥1.0μF   TT21   X5R/X7R/X6S   ≤10V   C≥10μF	\\\_\10V ≤20%
		THE MOISTANCE ENGINEERING SECOND	50V, 6.3V ≤30%
		*Before initial measurement (Class II only): To apply de-agin at 150℃ for 1hr then set for 24±2 hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to ©r de-aging at 150℃ fo 1hr then set for 24±2 hrs at room temp.	
	l	<u>.                                    </u>	i .

 $<sup>^{\</sup>star}$  "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

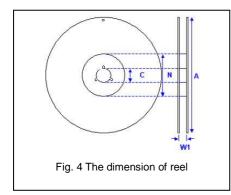
## **APPENDIXES**

## **■ Tape & reel dimensions**





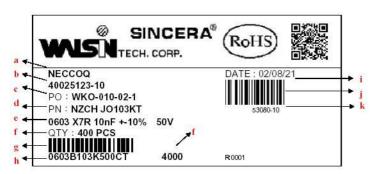
Size	0402	0603	0805	12	06	12	10
Thickness	L	Н	T	Т	J	T	K
$A_0$	0.70 +/-0.20	1.05 +/-0.30	1.50 +/-0.20	1.90 +/-0.50	< 2.00	< 3.05	< 3.05
$B_{0}$	1.20 +/-0.20	1.80 +/-0.30	2.30 +/-0.20	3.50 +/-0.50	< 3.70	< 3.80	< 3.80
Т	≦0.80	≦1.20	≦1.20	≦1.20	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1
K <sub>0</sub>	-	1.17/1/1	-	- \\	< 2.00	< 1.50	< 2.50
w	8.00 +/-0.30	8.00 +/-0.30	8.00 +/-0.30	8.00	8.00 +/-0.30	8.00 +/-0.30	8.00 +/-0.30
P <sub>0</sub>	4.00 +/-0.10	4.00 +/-0.10	PASS 4.00 SYSTEM +/-0.10	4.00 = +/-0.10	4.00 +/-0.10	4.00 +/-0.10	4.00 +/-0.10
10xP <sub>0</sub>	40.00 +/-0.10	40.00 +/-0.20	40.00 +/-0.20	40.00 +/-0.20	40.00 +/-0.20	40.00 +/-0.20	40.00 +/-0.20
P <sub>1</sub>	2.00 +/-0.05	4.00 +/-0.10	4.00 +/-0.10	4.00 +/-0.10	4.00 +/-0.10	4.00 +/-0.10	4.00 +/-0.10
P <sub>2</sub>	2.00 +/-0.05	2.00 +/-0.05	2.00	2.00 +/-0.05	2.00 +/-0.05	2.00 +/-0.05	2.00 +/-0.05
D <sub>0</sub>	1.50 +0.1/-0	1.50 +0.1/-0	1.50 40.17-0 GV M	1.50 +0.1/-0	1.50 +0.1/-0	1.50 +0.1/-0	1.50 +0.1/-0
D <sub>1</sub>	-	-			1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10
E	1.75 +/-0.10	1.75 +/-0.10	1.75 +/-0.10	1.75 +/-0.10	1.75 +/-0.10	1.75 +/-0.10	1.75 +/-0.10
F	3.50 +/-0.05	3.50 +/-0.05	3.50 +/-0.05	3.50 +/-0.05	3.50 +/-0.05	3.50 +/-0.05	3.50 +/-0.05



Size	0402, 0603, 0805, 1206, 1210						
Reel size	7"	13"					
С	13.0±0.5	13.0±0.5	13.0±0.5				
<b>W</b> <sub>1</sub>	10.0±1.5	10.0±1.5	10.0±1.5				
Α	178.0±2.0	250.0±2.0	330.0±2.0				
N	60.0+1.0/-0	50 min	50 min				

## **■** Example of customer label



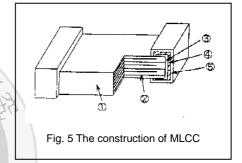


<sup>\*</sup>Customized label is available upon request

- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label

#### Constructions

No.	Name		X7R, X5R
①	Ceramic material		BaTiO <sub>3</sub> based
2	Inner electrode		NI (1 X
3		Inner layer	Cu
4	Termination	Middle layer	Ni
(5)		Outer layer	Sn (Matt)



PASSIVE SYSTEM ALLIANCE

## Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70%, related humidity conditions; MSL Level 1.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

#### Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

#### **Multilayer Ceramic Capacitors**

## Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of  $N_2$  within oven are recommended.

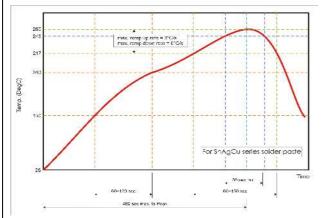


Fig. 6 Recommended reflow soldering profile for SMT process with  ${\sf SnAgCu}$  series solder paste.

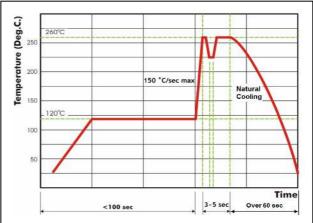


Fig. 7 Recommended wave soldering profile for SMT process with SnAgCu series solder.

