



PJQ2461-AU

60V P-Channel Enhancement Mode MOSFET

Voltage **-60 V** Current **-2.4 A**

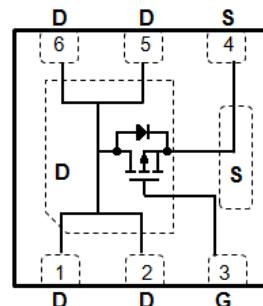
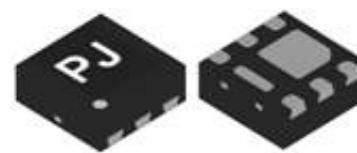
Features

- $R_{DS(ON)}$, $V_{GS} @ -10V$, $I_D @ -2A < 170m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ -4.5V$, $I_D @ -1.5A < 220m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^(Note 4)	I_D	-2.4	A
		-1.9	
Pulsed Drain Current ^(Note 1)	I_{DM}	-9.6	W
Power Dissipation	P_D	2	W
		1.3	
Single Pulse Avalanche Energy ^(Note 6)	E_{AS}	32	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 4,5)	$R_{\theta JA}$	62.5	°C/W



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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-60	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=-250\mu\text{A}$	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{on})}$	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-2\text{A}$	-	140	170	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-1.5\text{A}$	-	190	220	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-60\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=-30\text{V}, \text{I}_D=-2\text{A}, \text{V}_{\text{GS}}=-10\text{V}$ (Note 1,2)	-	8.3	-	nC
Gate-Source Charge	Q_{gs}		-	1.8	-	
Gate-Drain Charge	Q_{gd}		-	1.6	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=-30\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1\text{MHZ}$	-	430	-	pF
Output Capacitance	C_{oss}		-	33	-	
Reverse Transfer Capacitance	Crss		-	29	-	
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=-30\text{V}, \text{I}_D=-1\text{A}, \text{V}_{\text{GS}}=-10\text{V}, \text{R}_G=6\Omega$ (Note 1,2)	-	5.1	-	ns
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	36	-	
Turn-Off Fall Time	tf		-	11	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	-1.5	A
Diode Forward Voltage	V_{SD}	$\text{I}_s=-1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-0.78	-1	V

NOTES :

1. Pulse width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $\text{T}_{\text{J}(\text{MAX})}=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $\text{T}_j = 25^\circ\text{C}$.
4. The maximum current rating is package limited.
5. R_{eJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. The test condition is $L=1\text{mH}, \text{I}_{\text{AS}}=-8\text{A}, \text{V}_{\text{DD}}=-25\text{V}, \text{V}_{\text{GS}}=-10\text{V}$
7. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

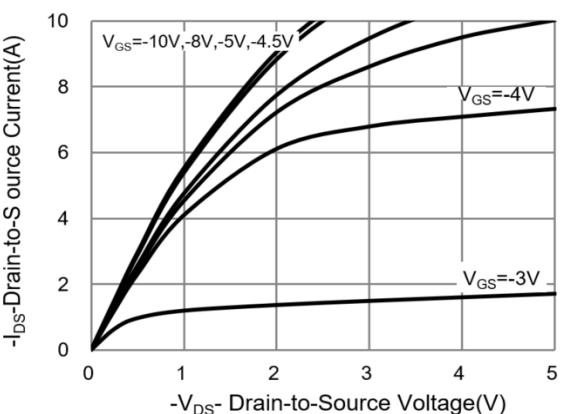


Fig.1 On-Region Characteristics

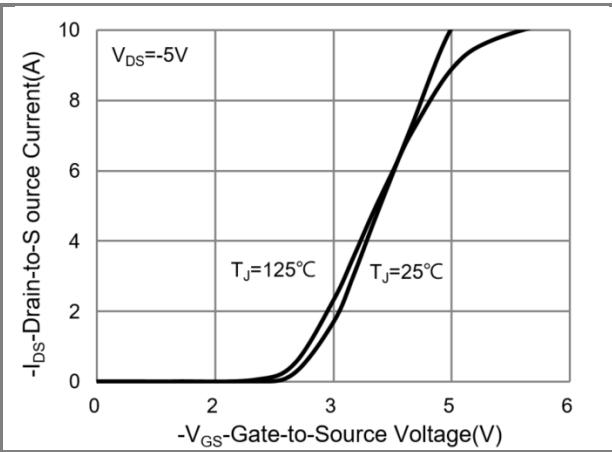


Fig.2 Transfer Characteristics

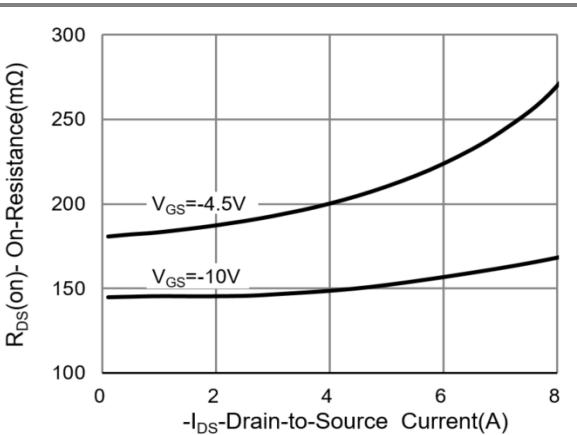


Fig.3 On-Resistance vs. Drain Current

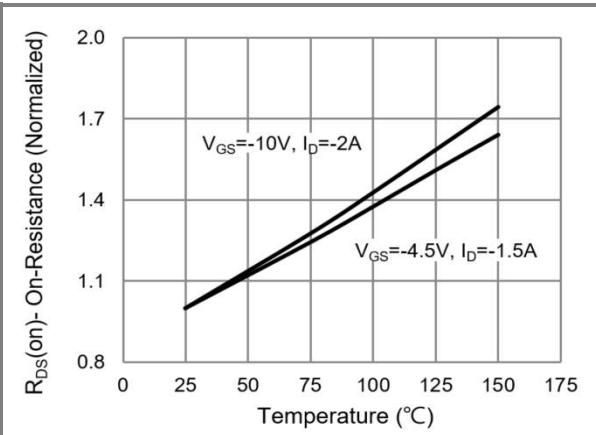


Fig.4 On-Resistance vs. Junction temperature

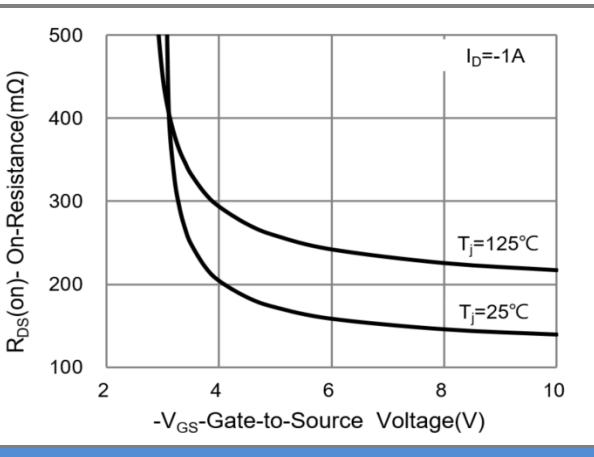


Fig.5 On-Resistance Variation with Vgs

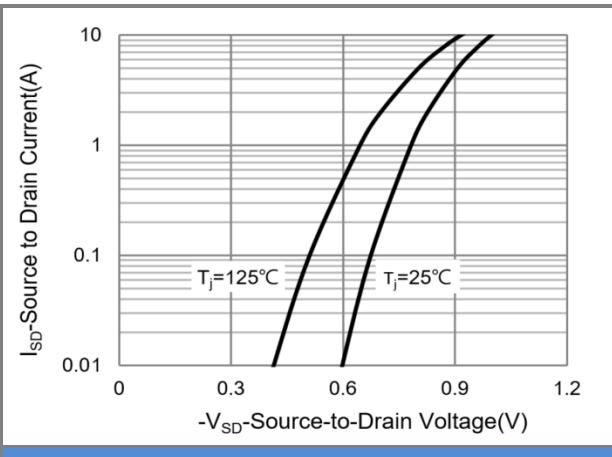


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

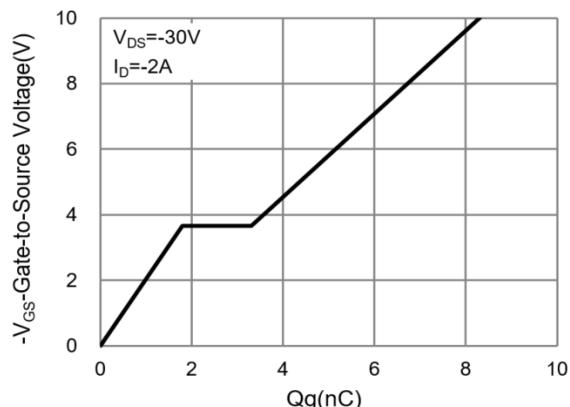


Fig.7 Gate-Charge Characteristics

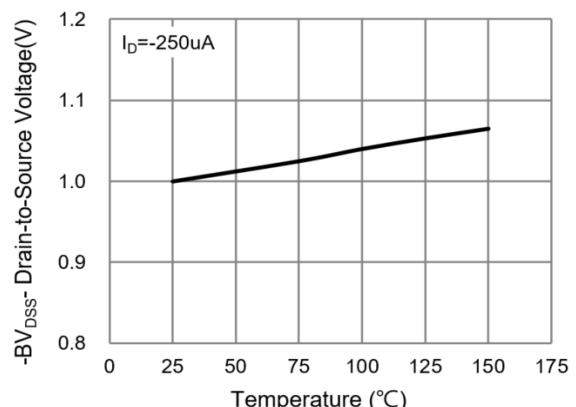


Fig.8 Breakdown Voltage Variation vs. Temperature

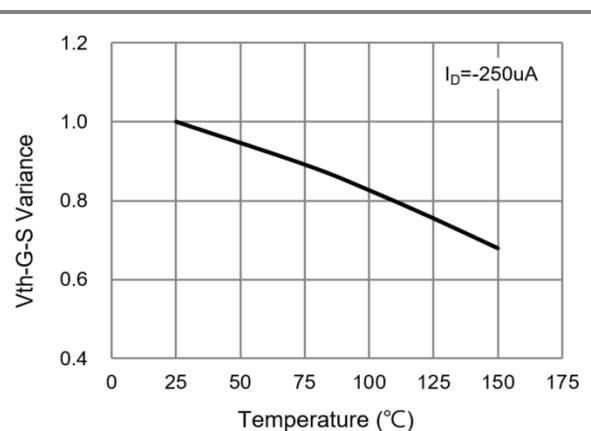


Fig.9 Threshold Voltage Variation with Temperature

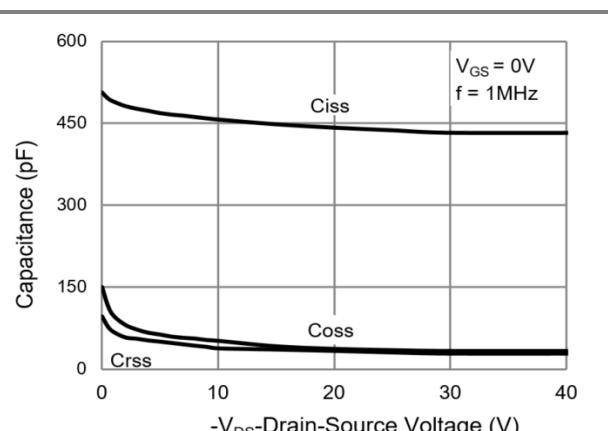


Fig.10 Capacitance vs. Drain-Source Voltage

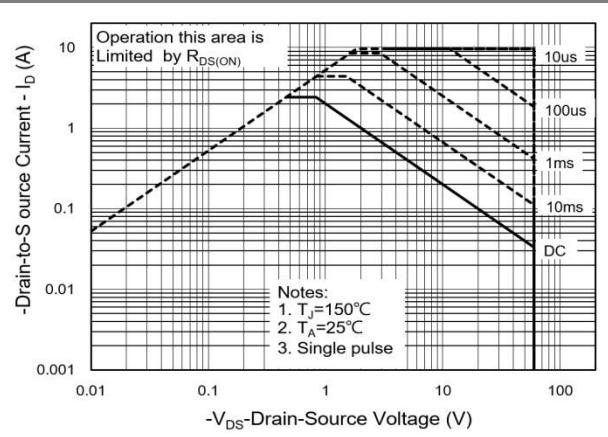


Fig.11 Maximum Safe Operating Area.

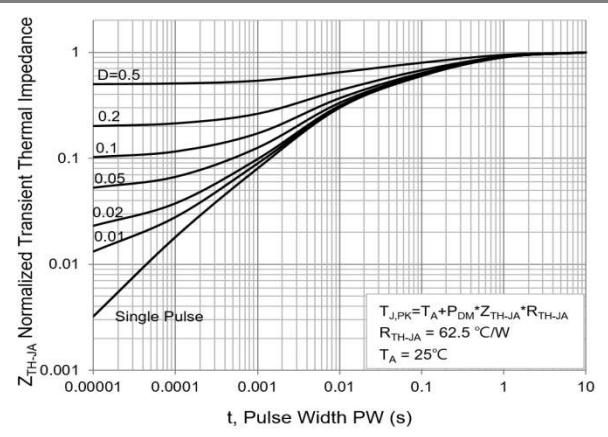


Fig.12 Normalized Transient Thermal Impedance

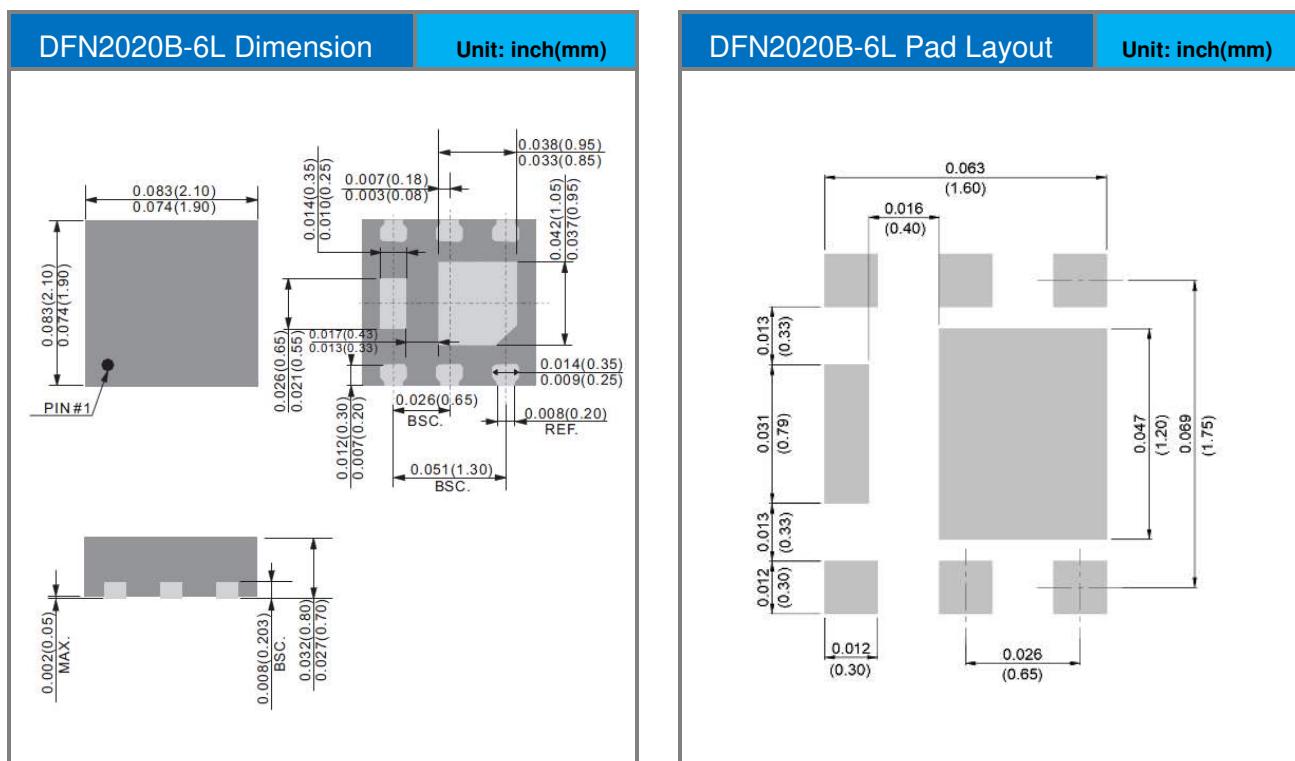


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Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2461-AU_R1_000A1	DFN2020B-6L	3K pcs / 7" reel	461	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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