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Darlington Transistor

NPN Silicon

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CES}	60	Vdc
Emitter-Base Voltage	V _{EBO}	10	Vdc
Collector Current - Continuous	Ι _C	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

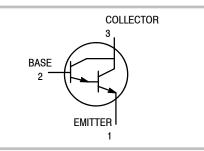
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W

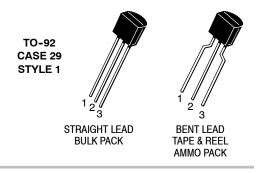
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



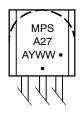
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MARKING DIAGRAM



= Assembly Location

= Year

A Y

- WW = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MPSA27G	TO-92 (Pb-Free)	5000 Units/Bulk
MPSA27RLRA	TO-92	2000/Tape & Reel
MPSA27RLRAG	TO-92 (Pb-Free)	2000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MPSA27

ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Symbol	Min	Тур	Max	Unit
	•		•	•
V _{(BR)CES}	60	-	-	Vdc
V _{(BR)CBO}	60	-	-	Vdc
I _{CBO}	-	-	100	nAdc
ICES	-	-	500	nAdc
I _{EBO}	-	-	100	nAdc
			•	
h _{FE}	10,000 10,000	-	-	-
V _{CE(sat)}	-	-	1.5	Vdc
V _{BE(on)}	-	-	2.0	Vdc
			•	
h _{fe}	1.25	2.4	-	-
	V(BR)CES V(BR)CBO ICBO ICBO ICES ILEBO IEBO VCE(sat) VBE(on)	V(BR)CES 60 V(BR)CBO 60 ICBO - ICES - ICES - IEBO - VCE(sat) - VCE(sat) - VCE(sat) -	$\begin{array}{ c c c c c } & V_{(BR)CES} & 60 & - & & \\ \hline V_{(BR)CBO} & 60 & - & & \\ \hline I_{CBO} & - & & & \\ \hline I_{CBO} & - & & & \\ \hline I_{CES} & - & & & \\ \hline I_{EBO} & - & & & \\ \hline I_{EBO} & - & & & \\ \hline V_{CE(sat)} & - & & & \\ \hline V_{DE(on)} & - & & & \\ \hline \end{array}$	$\begin{array}{ c c c c c c } \hline V_{(BR)CES} & 60 & - & - \\ \hline V_{(BR)CBO} & 60 & - & - \\ \hline I_{CBO} & - & - & 100 \\ \hline I_{CBO} & - & - & 100 \\ \hline I_{CES} & - & - & 500 \\ \hline I_{EBO} & - & - & 500 \\ \hline I_{EBO} & - & - & 100 \\ \hline \end{array}$

1. Pulse Test: Pulse Width < 300 $\mu s,$ Duty Cycle < 2.0%.

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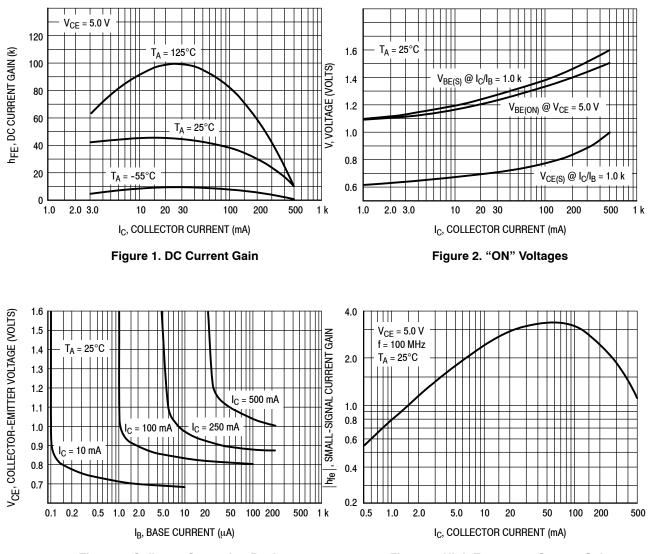


Figure 3. Collector Saturation Region

Figure 4. High Frequency Current Gain

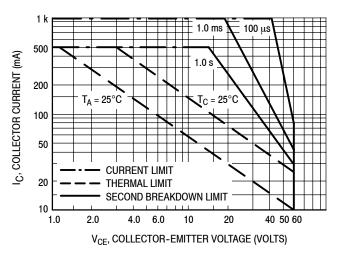
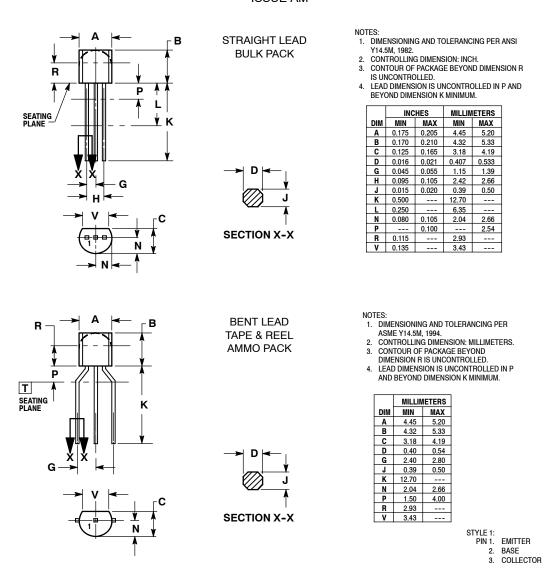


Figure 5. Active Region - Safe Operating Area

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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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