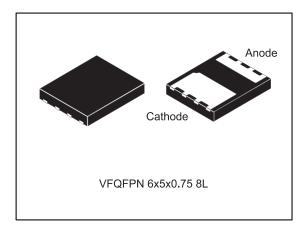


### 12 V reverse voltage cool bypass switch

Datasheet - preliminary data



#### **Features**

- Maximum forward current I<sub>F</sub> = 16 A
- Maximum reverse recovery mode V<sub>R</sub> up to 12 V
- Very low forward voltage drop:
   V<sub>F</sub> = 120 mV @ I<sub>F</sub> = 10 A, T<sub>AMB</sub>= 125 °C
- Ultra low reverse leakage current:
  - I<sub>R</sub> = 100  $\mu$ A @ V<sub>R</sub> = 12 V, T<sub>AMB</sub>= 125 °C
- ESD HBM level (JESD22-A114) up to 8 kV
- Surge test level (IEC61000-4-5) up to 2 kV

Junction temperature range T<sub>J</sub> from -40 °C to 150 °C

#### **Applications**

- Photovoltaic panels
- Solar farm

#### **Description**

The SPV1512 is a cool bypass switch with a very low forward voltage drop and ultra low reverse leakage current. The former drastically reduces the power dissipation in bypass mode and prolongs the lifetime of the device, by reducing maintenance costs and shutdown due to a device failure. The latter allows the device to work at very high temperature avoiding thermal runaway phenomenon. These are clearly key benefits for all those applications requiring low power consumption to increase the system lifetime and maximize the power transfer from harvesting source to the load. For all these reasons and the strong ESD robustness, the cool bypass switch is the significant evolution with respect to the traditional standard Schottky diode.

Table 1: Device summary

Order code	Operating temperature range	Package	Packing	
SPV1512N	-40 to 125 °C	VFQFPN (6x5x0.75) 8L	Tape and reel	

October 2015 DocID023620 Rev 2 1/12

Contents SPV1512

### **Contents**

1	Electric	eal ratings	3
2	Electric	eal characteristics	4
3	Recomi	mended footprint on the application board	6
4	Packag	e information	7
	4.1	VFQFPN (6x5x0.75) package information	8
	4.2	VFQFPN (6x5x0.75) packing information	9
5	Revisio	n history	11

SPV1512 Electrical ratings

# 1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>R</sub>	Max. DC reverse voltage	12	V
I <sub>F</sub>	Max. forward current	16	Α
TJ	Junction temperature range	-40 to 150	°C
T <sub>STG</sub>	Storage temperature range	-40 to 175	°C
R <sub>TH(j-c)</sub>	R <sub>TH(j-c)</sub> Thermal resistance, junction-to-case		°C/W
ESD	ESD Human body model		kV

### 2 Electrical characteristics

T<sub>AMB</sub> = 25 °C unless otherwise specified

**Table 3: Electrical characteristics** 

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
VF		1 - 1 1	T <sub>J</sub> = 25 °C	-	100 -		
		I <sub>F</sub> = 1 A	T <sub>J</sub> = 125 °C	-	110	-	mV
		I = 40 A	T <sub>J</sub> = 25 °C	-	115	-	
	Forward voltage drop	I <sub>F</sub> = 10 A	T <sub>J</sub> = 125 °C	-	130	-	
		1 - 4C A	T <sub>J</sub> = 25 °C	-	140	-	
		I <sub>F</sub> = 16 A	T <sub>J</sub> = 125 °C	-	160	-	
I <sub>R</sub>	Reverse leakage current	\/ - 10 \/	T <sub>J</sub> = 25 °C	-	10	-	μA
		V <sub>R</sub> = 12 V	T <sub>J</sub> = 125 °C	-	100	-	

Figure 1: Forward power dissipation vs. forward current

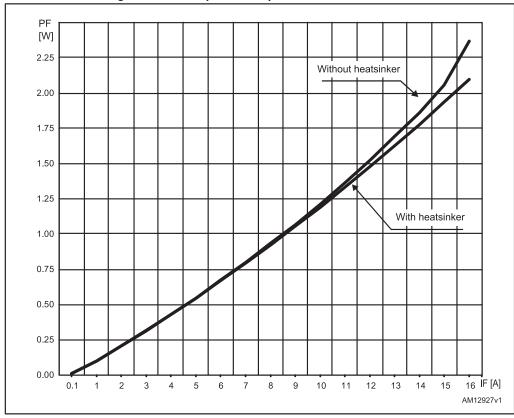


Figure 2: Reverse current vs. reverse voltage

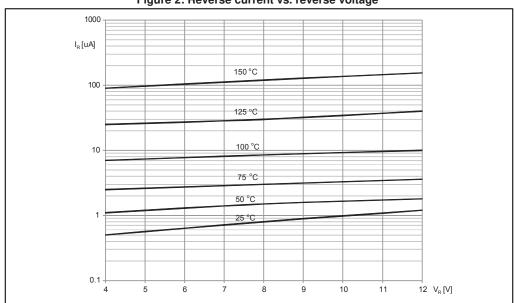
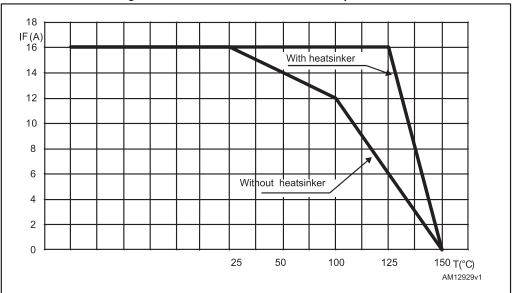


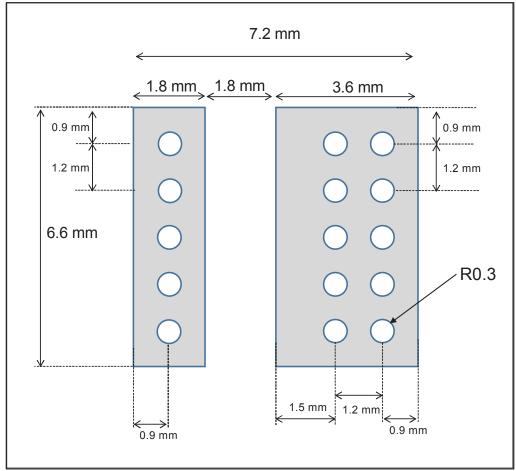
Figure 3: Forward current vs. ambient temperature



# 3 Recommended footprint on the application board

The below figure shows the suggested footprint on the board, in order to improve heat dissipation.

Figure 4: Recommended footprint



6/12 DocID023620 Rev 2

SPV1512 Package information

# 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.



Package information SPV1512

# 4.1 VFQFPN (6x5x0.75) package information

Figure 5: VFQFPN (6x5x0.75) package outline

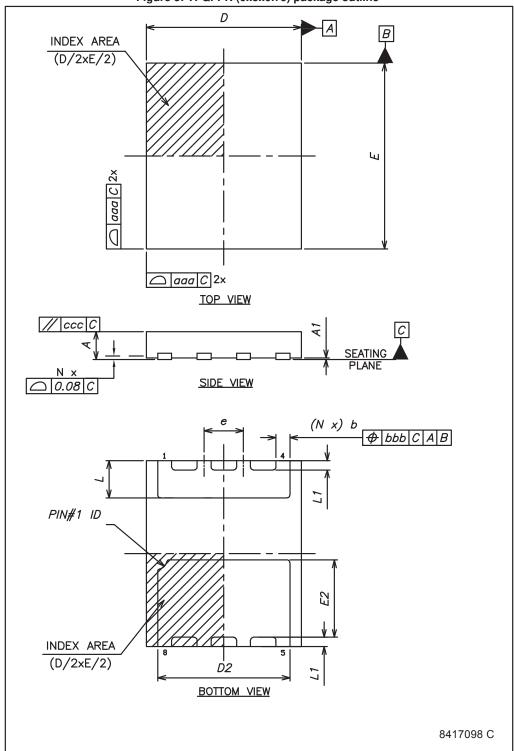
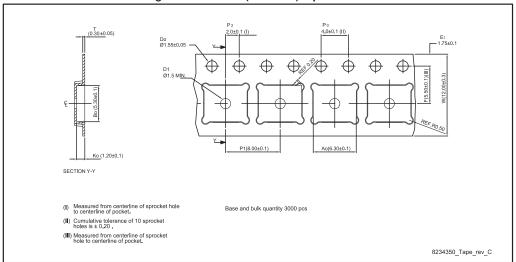


Table 4: VFQFPN (6x5x0.75) package mechanical data

Dim	, ,	mm	
Dim.	Min.	Тур.	Max.
А	0.70	0.75	0.85
A1	0	0.02	0.05
D		5.00	
D2	4.11	4.26	4.36
Е		6.00	
E2	2.35	2.50	2.60
е		1.27	
L	1.10	1.20	1.30
L1		0.30	
b	0.40	0.45	0.50
aaa		0.05	
bbb		0.10	
ccc		0.10	
N	_	8	

## 4.2 VFQFPN (6x5x0.75) packing information

Figure 6: VFQFPN (6x5x0.75) tape outline



Package information SPV1512

Figure 7: VFQFPN (6x5x0.75) carrier tape outline

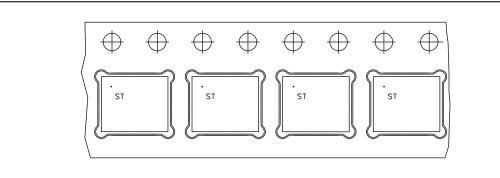
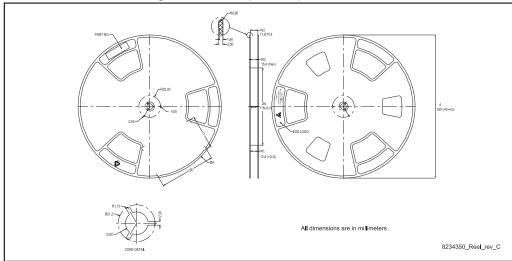


Figure 8: VFQFPN (6x5x0.75) reel outline



SPV1512 Revision history

# 5 Revision history

Table 5: Document revision history

Date	Revision	Changes
31-Aug-2012	1	First release.
08-Oct-2015	2	Updated the cover image, features, applications, description and device summary table. Changed the title. Changed figure titled "Reverse current vs. reverse voltage" and updated the rest of figures relative to section titled "Electrical characteristics". Added a new section titled: "Recommended footprint on the application board".

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