TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra-High-Speed U-MOSIII)

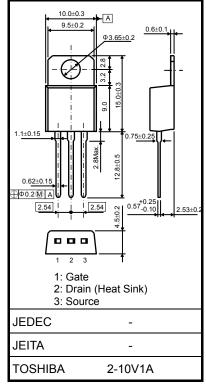
TK55D10J1

Switching Regulator Applications

- High-Speed switching
- Low gate charge: Q_g = 110 nC (typ.)
- Low drain-source ON resistance: $R_{DS (ON)}$ = 8.4 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 110 \text{ S} (typ.)$
- Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 100 V)
- Enhancement mode: V_{th} = 1.1 to 2.3 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage	1	V _{DSS}	100	V
Drain-gate voltage (F	R _{GS} = 20 kΩ)	V _{DGR}	100	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1) I _D	55	А
	Pulse (Note 1) I _{DP}	210	A
Drain power dissipat	ion (Tc = 25° C)	PD	140	W
Single pulse avalance	he energy (Note 2) E _{AS}	382	mJ
Avalanche current		I _{AR}	55	А
Repetitive avalanche	energy (Note 3) E _{AR}	9.4	mJ
Channel temperature	;	T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 50	°C



Weight: 1.35 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.89	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Internal Connection



Note 1: Ensure that the channel and lead temperatures do not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C, L = 200 $\mu H,~I_{AR}$ = 55 A , R_G = 1 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

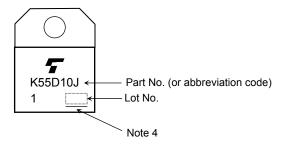
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA
Drain cut-OFF current		IDSS	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		_	10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	100		_	v
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	55		_	
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	1.1		2.3	V
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 27 \text{ A}$		9.0	12.0	mΩ
			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 27 \text{ A}$	_	8.4	10.5	
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 27 \text{ A}$	55	110	_	S
Input capacitance		C _{iss}	V _{DS} = 10V, V _{GS} = 0 V, f = 1 MHz	_	5700	_	pF
Reverse transfer capacitance		C _{rss}		_	390	_	
Output capacitance		C _{oss}		_	1000		
Switching time	Rise time	tr	$V_{GS}^{10 V} \downarrow I_{D} = 27 \text{ A} \\ 0 V \downarrow I_{$		7		ns
	Turn-ON time	t _{on}			30		
	Fall time	t _f			20		
	Turn-OFF time	t _{off}			130		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD}\simeq 80~V,~V_{GS}=5~V,~I_D=55A$		63		
			$V_{DD}\simeq 80$ V, $V_{GS}=10$ V, $I_{D}=55A$	_	110		
Gate-source charge 1		Q _{gs1}			17		nC
Gate-drain ("miller") charge		Q _{gd}	$V_{DD}\simeq 80~V,~V_{GS}=10~V,~I_{D}=55A$	_	32	_	
Gate switch charge		Q _{SW}		_	38	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	55	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	220	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 55 A, V _{GS} = 0 V	_	-0.9	-1.2	V
Reverse recovery time	t _{rr}	I _{DR} = 55 A, V _{GS} = 0 V,	_	67	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/µs		84		nC

Marking

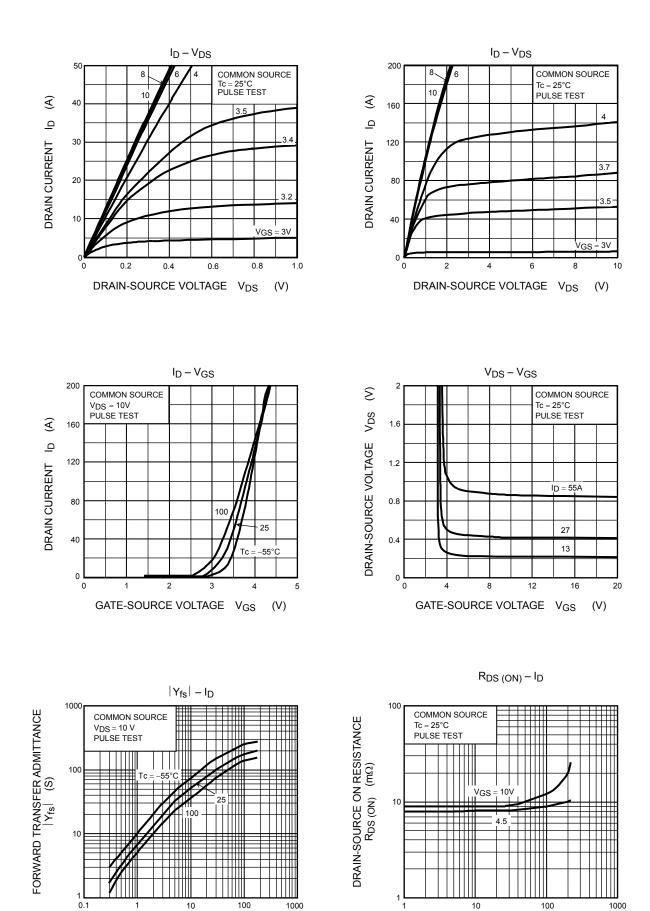


Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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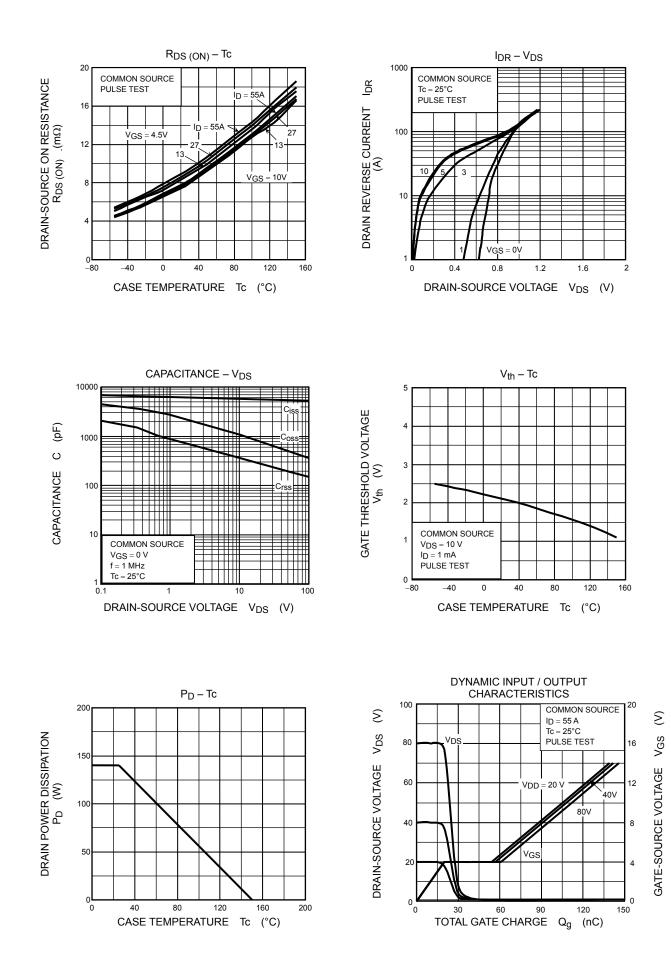
DRAIN CURRENT ID

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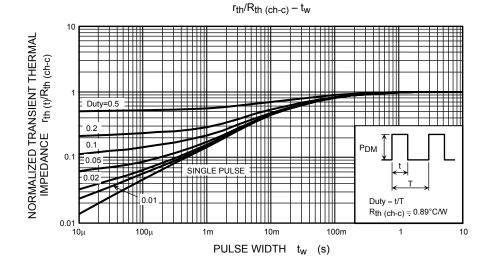
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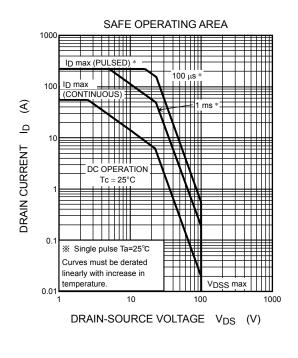
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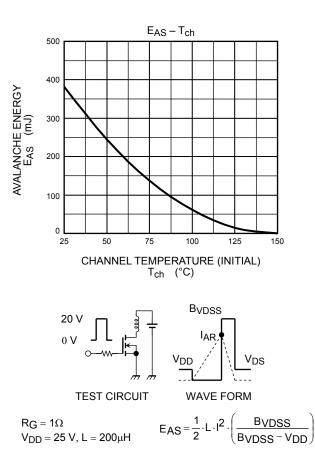
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