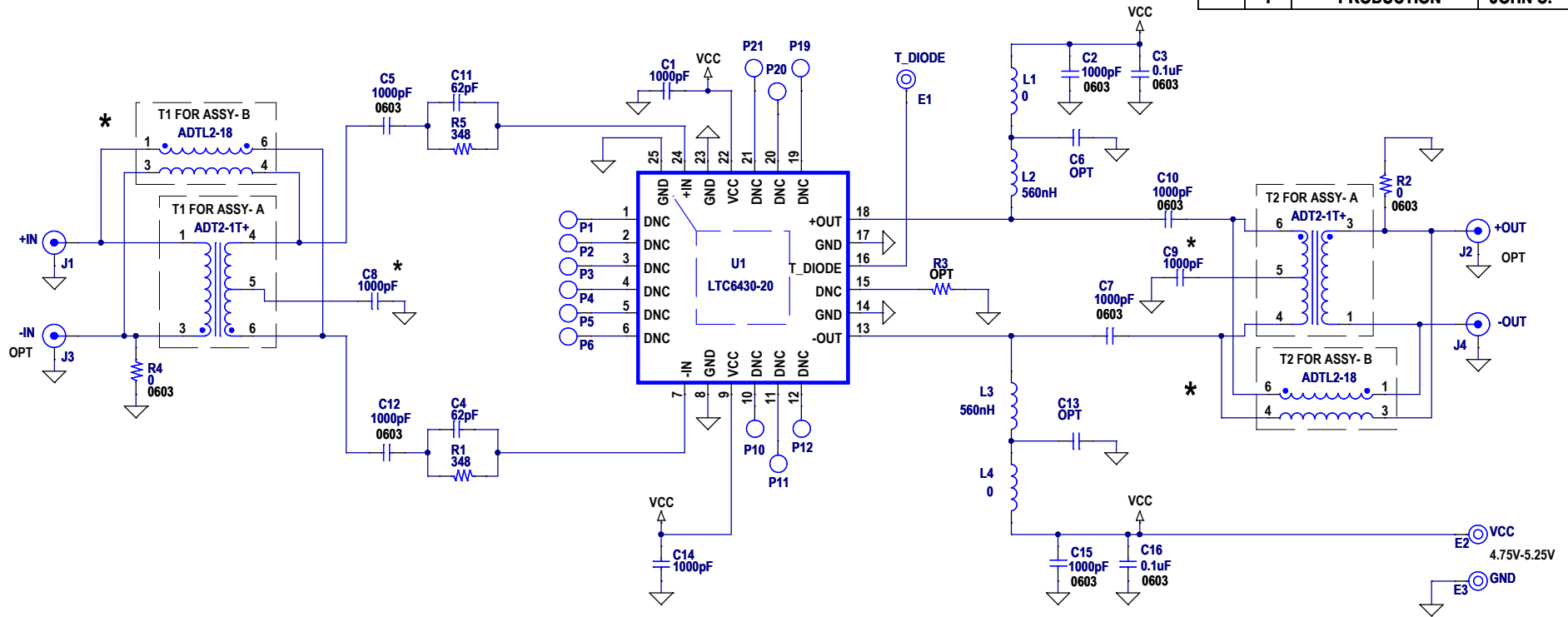
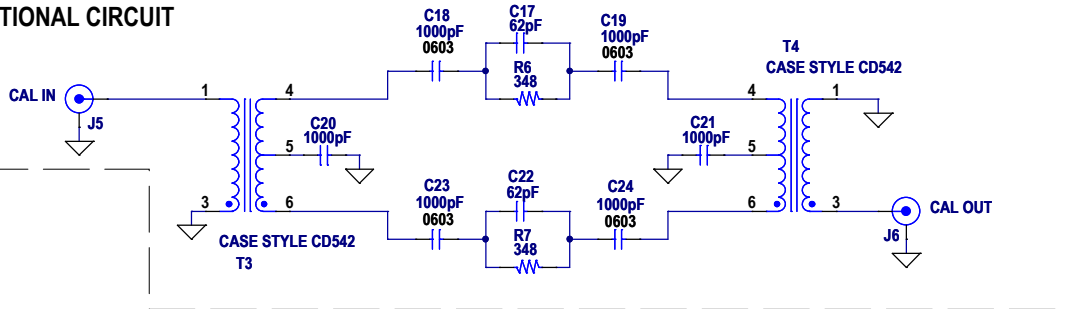


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION	JOHN C.	10-13-14



OPTIONAL CIRCUIT



ASSY	U1	FREQ.	T1, T2	C8, C9
-A	LTC6430-20	100-350 MHz	ADT2-1T+	1000pF, 0402
-B	LTC6430-20	300-1000 MHz	ADTL2-18	OPT

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE 0402.
2. ALL DNC PINS ON U1 ARE FOR LINEAR USE ONLY

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	AK
APP ENG.	JOHN C.
SCALE	NONE



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TITLE: SCHEMATIC
RF/IF AMP/ADC DRIVER

SIZE	IC NO.	REV.
N/A	LTC6430AIUF-20 DEMO CIRCUIT 2076A	1

DATE: Monday, October 13, 2014 SHEET 1 OF 1