Available at Digi-Key** www.digikey.com





Telecom Performance 5x7mm TCXO / VCTCXO T / TV Series



2111 Comprehensive Drive Aurora, Illinois 60505 Phone: 630-851-4722 Fax: 630-851-5040 www.conwin.com

Description:

Connor-Winfield's Txxx and TVxxx series are 5x7mm TCXO and VCTCXO products with exceptional frequency stability and low phase noise. Through the use of analog temperature

compensation, these products are capable of holding Stratum 3 level temperature stabilities of ± 0.28 ppm over the commercial and industrial temperature ranges. Available in 4-pad or 10-pad surface mount footprints.

These products are designed for such applications as IEEE 1588 PTP and Synchronous Ethernet.

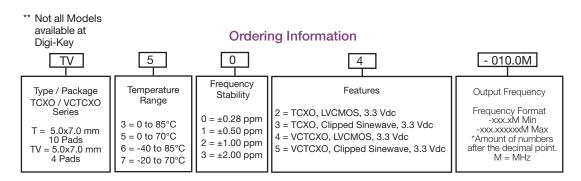
All models will meet ±4.6 ppm accuracies for twenty years

Applications:

- IEEE 1588 Applications
- Synchronous Ethernet slave clocks, ITU-T G.8262 EEC options 1 & 2
- Compliant to Stratum 3, GR-1244-CORE & GR-253-CORE
- Wireless Communications
- Small Cells
- Test and Measurement
- GPS

Standard Frequencies Available *

* 6.4, 9.72, 10, 10.24, 12.5, 12.8, 13.5, 19.2, 19.44, 20, 20.48, 25, 27, 38.88, 40 MHz Available frequencies from the factory for small quantity orders or quick delivery. Additional frequencies are available.



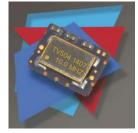
Example Part Numbers

TV504-010.0M = 5x7mm 4 pad package, ±0.28 ppm, 0 to 70 ℃, 3.3 Vdc, LVCMOS Output, VCTCXO T715-012.8M = 5x7mm 10 pad package, ±0.50 ppm, -20 to 70 ℃, 3.3 Vdc, Clipped Sinewave Output, VCTCXO T522-050.0M = 5x7mm 10 pad package, ±1.0 ppm, 0 to 70 ℃, 3.3 Vdc, LVCMOS Output, TCXO TV602-010.0M = 5x7mm 4 pad package, ±0.28 ppm, -40 to 85 ℃, 3.3 Vdc, LVCMOS Output, TCXO

Note: Models T622 and TV622 have recommended replacement parts TJ6F and TVJ6F for the following frequencies 10, 12.8, 18.432, 20, 25, 25.6, 27, 38.88, 40.0, 49.152, and 50 MHz. Please refer to the product data sheet TX452 for more information on these models.



Tx176
1 of 7
26
12 July 2023



Features:

- Frequency Stabilities Available: +/-0.28 ppm (6.4 to 50 MHz) +/-0.50 ppm (6.4 to 50 MHz) +/-1.00 ppm or +/-2.00 ppm (6.4 to 54 MHz)
- Temperature Ranges Available: 0 to 85°C, 0 to 70°C, -40 to 85°C or
- -20 to 70°C Packages Available:
- T Series: 5 x 7mm 10 Pad
- TV Series: 5 x 7mm 4 Pad
- 3.3 Vdc Operation
- Output Logic: LVCMOS or Clipped Sinewave
- Fixed Frequency TCXO
- Voltage Controlled VCTCXO
- Low Jitter <0.50 ps RMS
- Low Phase Noise
- Tri-State Enable/Disable: (T Model Series Only)
- Tape and Reel Packaging
- RoHS Compliant / Lead Free <a>RoHS



Deremeter	Minima	Moreiral	Maxim	11	NIStee
Parameter	Minimum	Nominal	Maximum	<u>Units</u> °C	Notes
Storage Temperature	<u>-55</u> -0.5	-	<u>95</u> 6.0	-	
Supply Voltage (Vcc)		-		Vdc	
Input Voltage	-0.5	-	Vcc + 0.5	Vdc	
	Operating Sp	pecifications			
Parameter	Minimum	Nominal	Maximum	Units	Notes
Output Frequency (Fo)					
Models Tx0x, TVx0x	6.4	-	50	MHz	
Models Tx1x, TVx1x	6.4	-	50	MHz	
Models Tx2x, TVx2x	6.4	-	54	MHz	
Models Tx3x, TVx3x	6.4	-	54	MHz	
Operating Temperature Range	(See Orc	lering Information	for full part number		
Models T3xx, TV3xx	0	-	85	°C	
Models T5xx, TV5xx	0	-	70	°C	
Models T6xx, TV6xx	-40	-	85	°C	
Models T7xx, TV7xx	-20	_	70	°Č	
Frequency Calibration @ 25 °C	-1.0	-	1.0	ppm	1
Frequency Stability (See Ordering Informatic) Per STRATUM 3		ppm	
Frequency Stability ±0.28 ppm is only ava					
Models Tx0x, TVx0x	-0.28	-	0.28	ppm	2
Holdover Stability	-0.32	-	0.32	ppm	3
Constant Temperature Stability	-40	_	40	pph	Over 24 Hrs
	ring Information for fu	- Il part numbor)	40	μμυ	000124113
Models Tx1x, TVx1x	-0.50	ii part number)	0.50	nom	2
Models Tx2x, TVx2x	-1.00	-	1.00	ppm	2
Models Tx2x, TVx2x Models Tx3x, TVx3x	-2.00	-	2.00	ppm	2
Frequency vs. Load Stability	-0.05		0.05	ppm	±5%
Frequency vs. Voltage Stability	-0.05	-	0.05	ppm	±5%
	-0.05	-	0.40	ppm	<u>±3 %</u> 4
Static Temperature Hysteresis Freq. shift after reflow soldering				ppm	
	-1.0		1.0	ppm	5
Long Term Stability	-1.0	-	1.0	ppm	6
Aging	2.0		2.0	2022	
per Life (20 Years)	-3.0	-	3.0	ppm	
per Day	-40	-	40	ppb	7
Total Frequency Tolerance	-4.6	-		ppm	7
Supply Voltage (Vcc)	3.135	3.30	3.465	Vdc	
Supply Current (Icc) LVCMOS	-	2.1	6.0	mA	
Clipped Sinewave	-	1.3	2.9	mA	
Jitter:		0.0	5.0	DMO	
	-	3.0	5.0	ps RMS	0
Integrated Phase Jitter (12K to Fo/2)	-	0.3	1.0	ps RMS	8
Allan Deviation (1s)	-	1.0E-10	-		
G-sensitivity	-	-	2.0	ppb/g	
Typical SSB Phase Noise					
For Fo	10.0 MHz	25.0 MHz	50.0 MHz		
@ 10 Hz offset @ 100 Hz offset	-98	-90	-73	dBc/Hz	
@ 100 Hz offset	-125	-120	-103	dBc/Hz	
@ 1 KHz offset	-143	-140	-134	dBc/Hz	
@ 10 KHz offset @ 100 KHz offset	-151 -152	-151 -152	-151 -152	dBc/Hz dBc/Hz	
@ 1 MHz offset	-152	-152 -154	-152	dBc/Hz dBc/Hz	
Start-Up Time	- 100	- 154	10		
	-	-	IU	ms	

Bulletin	Tx176
Page	2 of 7
Revision	26
Date	12 July 2023



Control Voltage Input Characteristics

Minimum	Nominal	Maximum	Units	Notes
0.3	1.65	3.0	V	
±10	±12	-	ppm	
-	8.00	-	ppm/V	
	Positive Slope			
-	-	5	%	
100K	-	-	Ohm	
10	-	-		KHz
	0.3 ±10 -	0.3 1.65 ±10 ±12 - 8.00 Positive Slope 100K -	0.3 1.65 3.0 ±10 ±12 - - 8.00 - Positive Slope 5 100K	0.3 1.65 3.0 V ±10 ±12 - ppm - 8.00 - ppm/V Positive Slope - 5 % 100K - - Ohm

Parameter	Minimum	Nominal	Maximum	Units	Notes
Enable Input Voltage -(Vih)	70%Vcc	-	-	Vdc	9
Disable Input Voltage - (Vil)	-	-	30%Vcc	Vdc	9
unction	Output				
_OW:	Disabled (High Impedanc	ce)			
High or Open:	Enabled	·			

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load (CL)	-	15	-	pF	10
Voltage (High) (Voh)	90%Vcc	-	-	Vdc	
(Low) (Vol)	-	-	10%Vcc	Vdc	
Current (High) (Ioh)	-4	-	-	mA	
(Low) (IoI)	-	-	4	mA	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time 10% to 90%	-	4	8	ns	

	Clipped Sinewave Ou	tput Character	istics		
Parameter	Minimum	Nominal	Maximum	Units	Notes
Load (RC)					11
Output Load Resistance	-	10K	-	Ohm	12
Output Load Capacitance	-	10	-	pF	
OutputVoltage (< 40 MHz)	1.0	1.2	-	V	pk-pk
OutputVoltage (=>40 MHz)	0.8	1.0	-	V	pk-pk
Output Impedance	-	200	-	Ohms	

Package	Hermetically sealed ceramic package with grounded metal cover

Environmental Characteristics

Vibration:	Vibration per Mil Std 883E Method 2007.3 Test Condition A.
Shock:	Mechanical Shock per Mil Std 883E Method 2002.4 Test Condition B.
Soldering Process:	RoHS compliant lead free. See soldering profile on page 4.

Notes:

1. Initial calibration @ 25°C. ±2°C, for VCTCXO's Vc = 1.65V. Specifications at time of shipment

2. Frequency stability vs. change in temperature. [±(Fmax-Fmin)/(2*Fo]). For VCTCXO's - Vc -= 1.65V

3. Inclusive of frequency stability, supply voltage change (±1%), aging, for 24 hours. Per STRATUM 3 GR-1244-CORE. 4. Frequency change after reciprocal temperature ramped over the operating range. Frequency measured before and after at 25°C

5. Two consecutive solder reflows after 1 hour recovery @ 25°C.

6. Frequency drift over 1 year @ 25°C.

7. Inclusive of calibration @ 25°C, frequency vs. change in temperature, change in supply voltage (±5%), load change (±5%), reflow soldering process and 20 years aging.

8. BW = 12 KHz to 20 MHz

9. Leave Pad 8 on the T Series unconnected if enable / disable function is not required. When tri-stated, the output stage is disabled but the oscillator and compensation circuit are still active (current consumption < 1 mA).

10. Attention: To achieve optimal frequency stability, and in some cases to meet the specification stated on this data sheet, it is required that the circuit connected to this TCXO output must have the equivalent input capacitance that is specified by the nominal load capacitance. Deviations from the nominal load capacitance will have a graduated effect on the stability of approximately 20 ppb per pF load difference.

11. Load components are required for proper operation of the device.

12. Output is AC coupled.

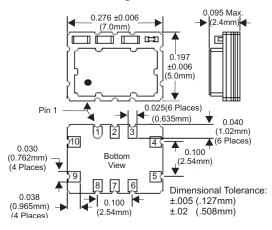
Bulletin	Tx176
	1/1/0
Page	3 of 7
Revision	00
	26
Date	12 July 2023



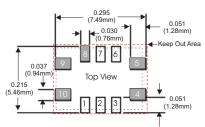
2111 Comprehensive Drive Aurora, Illinois 60505 Phone: 630-851-4722 Fax: 630-851-5040

www.conwin.com

T Series Package Outline



T Series Suggested Pad Layout

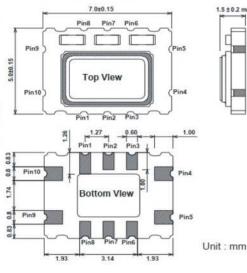


* Do not route any traces in the keep out area. It is recommended the next layer under the keep out area is to be ground plane.

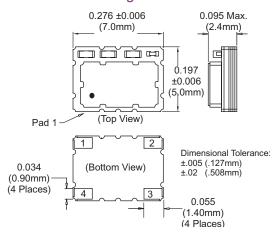
T Series Pad Connections

1: Do Not Connect
2: Do Not Connect
3: Do Not Connect
4: Ground
5: Output
6: Do Not Connect
7: Do Not Connect
8: Enable / Disable (OE)
9: Supply Voltage (Vcc
10: VCTCXO: Control Voltage (Vc)
TCXO: N/C

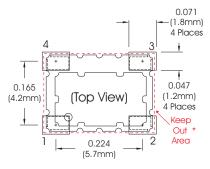
Alternate T-series package outline for T602-010.0M, T602-019.2M, T602-020.0M, and T602-030.72M



TV Series Package Outline

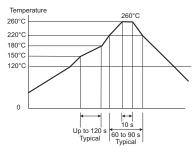


TV Series Suggested Pad Layout



* Do not route any traces in the keep out area. It is recommended the next layer under the keep out area is to be ground plane.

Solder Profile



Meets IPC/JEDEC J-STD-020C

TV Series Pad Connections

1: VCTCXO: Voltage Control (Vc)

TCXO: N/C
2: Ground
3: Output
4: Supply (Vcc)

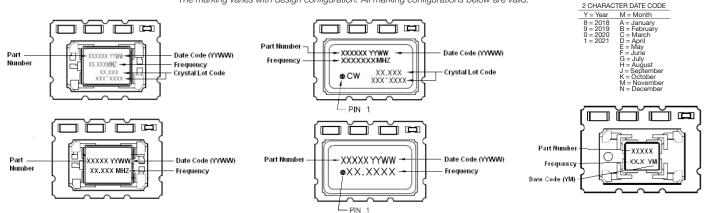
Bulletin	Tx176
Page	4 of 7
Revision	26
Date	12 July 2023



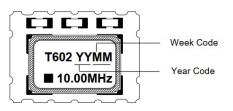
Marking Information

The following are examples of possible marking configurations

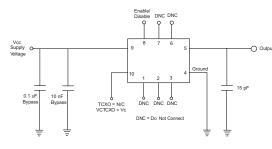
The marking varies with design configuration. All marking configurations below are valid.



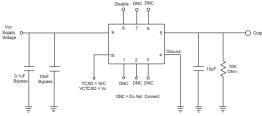
Marking for Alternate T-series package outline models T602-010.0M, T602-019.2M, T602-020.0M, and T602-030.72M



T Series LVCMOS Test Circuit

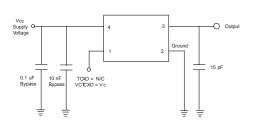


T Series Clipped Sinewave Test Circuit

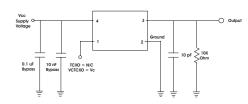


Note: The clipped sinewave output is AC coupled

TV Series LVCMOS Test Circuit



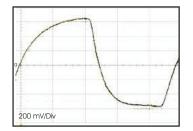
TV Series Clipped Sinewave Test Circuit



Clipped Sinewave Output Waveform

1V/Div

LVCMOS Output Waveform

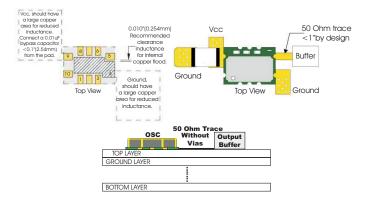


Bulletin	Tx176
Page	5 of 7
Revision	26
Date	12 July 2023

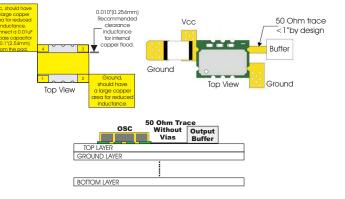


2111 Comprehensive Drive Aurora, Illinois 60505 Phone: 630-851-4722 Fax: 630-851-5040 www.conwin.com

T Series Design Recommendations

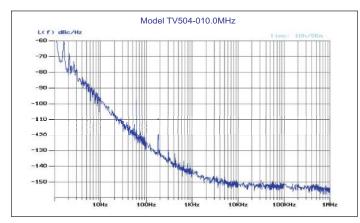


TV Series Design Recommendations

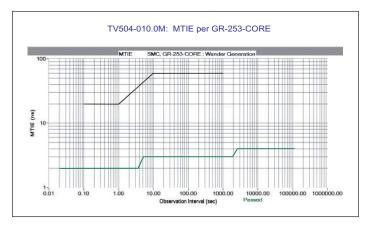


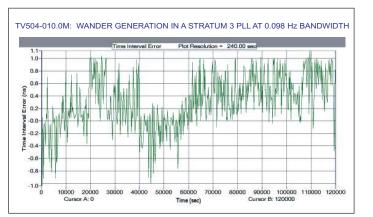
TIE

Phase Noise Information

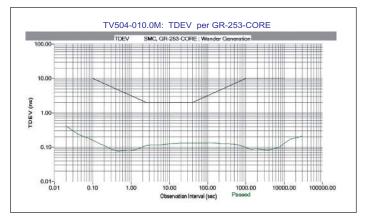






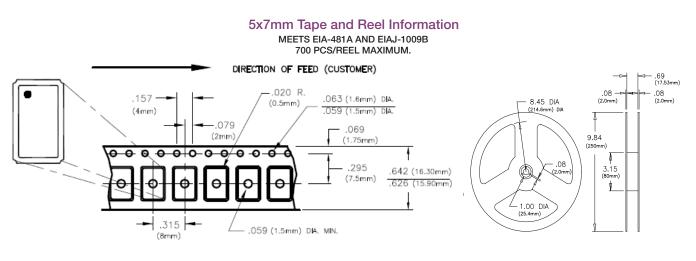


TDEV

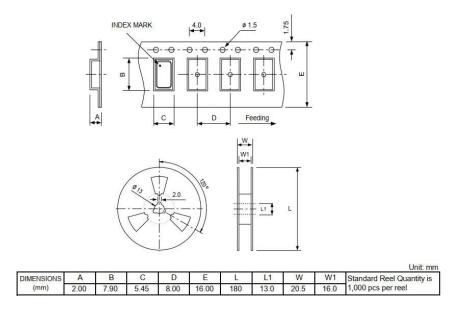


Bulletin	Tx176
Page	6 of 7
Revision	26
Date	12 July 2023





Tape and Reel Information for Alternate T-series package outline models T602-010.0M, T602-019.2M, T602-020.0M, and T602-030.72M Meets EIA-481A and EIAJ-1009B 1000 PCS/Reel Maximum



Revision History

19 04/01/15 Updated Frequency Stabilities 20 07/27/16 Extended operating frequency range, and updated standard frequency list 21 05/10/17 Added marking variations 22 08/02/18 Height change to 2.4mm Max and added additional marking variation 23 11/07/19 Added G-sensitivity specification. 24 11/17/22 Model numbers updated 25 03/02/23 Added 40.0 to frequency listing in note on page 1.	Revision	Date	Action
2105/10/17Added marking variations2208/02/18Height change to 2.4mm Max and added additional marking variation2311/07/19Added G-sensitivity specification.2411/17/22Model numbers updated	19	04/01/15	Updated Frequency Stabilities
2208/02/18Height change to 2.4mm Max and added additional marking variation2311/07/19Added G-sensitivity specification.2411/17/22Model numbers updated	20	07/27/16	Extended operating frequency range, and updated standard frequency list
2311/07/19Added G-sensitivity specification.2411/17/22Model numbers updated	21	05/10/17	Added marking variations
24 11/17/22 Model numbers updated	22	08/02/18	Height change to 2.4mm Max and added additional marking variation
	23	11/07/19	Added G-sensitivity specification.
25 03/02/23 Added 40.0 to frequency listing in note on page 1.	24	11/17/22	Model numbers updated
	25	03/02/23	Added 40.0 to frequency listing in note on page 1.
26 07/12/23 Added alternate package for specific T602 models.	26	07/12/23	Added alternate package for specific T602 models.

Bulletin	Tx176
Page	7 of 7
Revision	26
Date	12 July 2023