

## SIOV metal oxide varistors

Leaded varistors, AdvanceD-MP, S14 series

Series/Type: B722\*

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Leaded varistors B722<sup>2</sup>

#### AdvanceD-MP, S14 series

#### Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned wire

#### **Features**

- Wide operating voltage range 130 ... 680 V<sub>RMS</sub>
- All types duty cycle @ 6 kV/ 3 kA = >10 pulses, according to IEC 62368-1; G.8.2 and IEC 60950-1; Annex Q. IEC 61051-2
- All types I<sub>n</sub> @ 3 kA = >15 impulses according to UL 1449, 4<sup>th</sup> edition surge current generator (8/20 µs), type 5 listed
- Multiple pulse handling capability

#### **Approvals**

- UL
- CSA
- VDF
- IFC

#### **Delivery mode**

- Bulk (standard), taped versions on reel or in Ammo pack upon request.
- For further details refer to chapter "Taping, packaging and lead configuration" for leaded varistors.

#### General technical data

Climatic category	to IEC 60068-1	40/105/56	
Operating temperature	to IEC 61051	-40 +105	°C
Storage temperature		-40 +125	°C
Electric strength	to IEC 61051	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to IEC 61051	≥ 100	$M\Omega$



### AdvanceD-MP, S14 series



## Electrical specifications and ordering codes Maximum ratings (T $_{\!A}$ = 105 $^{\circ}\text{C})$

Ordering code	Туре	$V_{RMS}$	$V_{DC}$	i <sub>max</sub>	I <sub>n</sub> 1)	W <sub>max</sub>	P <sub>max</sub>
	(untaped)			(8/20 µs)	(8/20 µs)	(2 ms)	
	SIOV-			1 time	15 times		
		V	V	Α	Α	J	W
B72214P2131K101	S14K130E2K1	130	170	6000	3000	60.0	0.60
B72214P2141K101	S14K140E2K1	140	180	6000	3000	65.0	0.60
B72214P2151K101	S14K150E2K1	150	200	6000	3000	70.0	0.60
B72214P2171K101	S14K175E2K1	175	225	6000	3000	80.0	0.60
B72214P2211K101	S14K210E2K1	210	270	6000	3000	95.0	0.60
B72214P2231K101	S14K230E2K1	230	300	6000	3000	105.0	0.60
B72214P2251K101	S14K250E2K1	250	320	6000	3000	115.0	0.60
B72214P2271K101	S14K275E2K1	275	350	6000	3000	130.0	0.60
B72214P2301K101	S14K300E2K1	300	385	6000	3000	140.0	0.60
B72214P2321K101	S14K320E2K1	320	420	6000	3000	150.0	0.60
B72214P2351K101	S14K350E2K1	350	460	6000	3000	165.0	0.60
B72214P2381K101	S14K385E2K1	385	505	6000	3000	180.0	0.60
B72214P2421K101	S14K420E2K1	420	560	6000	3000	190.0	0.60
B72214P2461K101	S14K460E2K1	460	615	6000	3000	200.0	0.60
B72214P2511K101	S14K510E2K1	510	670	6000	3000	200.0	0.60
B72214P2551K101	S14K550E2K1	550	745	6000	3000	220.0	0.60
B72214P2621K101	S14K625E2K1	625	825	6000	3000	240.0	0.60
B72214P2681K101	S14K680E2K1	680	895	6000	3000	260.0	0.60

 $<sup>^{1)}\</sup>mbox{Note:}$  Nominal discharge current  $\mbox{I}_{\mbox{\tiny n}}$  according to UL 1449,  $4^{th}$  edition.



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### Leaded varistors

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### Characteristics ( $T_A = 25$ °C)

Ordering code	Туре	$V_{v}$	$\Delta V_{v}$	$V_{c,max}$	i <sub>c</sub>	C <sub>typ</sub>
	(untaped)	(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	SIOV-	V	%	V	Α	pF
B72214P2131K101	S14K130E2K1	205	±10	340	50.0	1100
B72214P2141K101	S14K140E2K1	220	±10	360	50.0	1000
B72214P2151K101	S14K150E2K1	240	±10	395	50.0	900
B72214P2171K101	S14K175E2K1	270	±10	455	50.0	800
B72214P2211K101	S14K210E2K1	330	±10	545	50.0	690
B72214P2231K101	S14K230E2K1	360	±10	595	50.0	630
B72214P2251K101	S14K250E2K1	390	±10	650	50.0	580
B72214P2271K101	S14K275E2K1	430	±10	710	50.0	530
B72214P2301K101	S14K300E2K1	470	±10	775	50.0	485
B72214P2321K101	S14K320E2K1	510	±10	840	50.0	445
B72214P2351K101	S14K350E2K1	560	±10	910	50.0	410
B72214P2381K101	S14K385E2K1	620	±10	1025	50.0	390
B72214P2421K101	S14K420E2K1	680	±10	1120	50.0	355
B72214P2461K101	S14K460E2K1	750	±10	1240	50.0	320
B72214P2511K101	S14K510E2K1	820	±10	1355	50.0	300
B72214P2551K101	S14K550E2K1	910	±10	1500	50.0	265
B72214P2621K101	S14K625E2K1	1000	±10	1650	50.0	240
B72214P2681K101	S14K680E2K1	1100	±10	1815	50.0	220

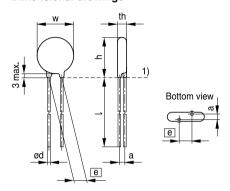


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### AdvanceD-MP, S14 series



### **Dimensional drawings**



1) Seating plane to IEC 60717

VAR0408-C-E

### Weight

Nominal diameter	$V_{RMS}$	Weight
mm	V	g
14	130 680	1.8 5.4

The weight of varistors in between these voltage classes can be interpolated.

#### **Dimensions**

Ordering code	[e] ±1	a (typical)	W <sub>max</sub>	th <sub>max</sub>	h <sub>max</sub>	I <sub>min</sub>	d ±0.05
	mm	mm	mm	mm	mm	mm	mm
B72214P2131K101	7.5	2.0	16.0	4.7	20.0	25.0	0.8
B72214P2141K101	7.5	2.1	16.0	4.8	20.0	25.0	0.8
B72214P2151K101	7.5	2.2	16.0	4.9	20.0	25.0	0.8
B72214P2171K101	7.5	2.4	16.0	5.1	20.0	25.0	0.8
B72214P2211K101	7.5	2.7	16.0	5.4	20.0	25.0	0.8
B72214P2231K101	7.5	2.9	16.0	5.6	20.0	25.0	0.8
B72214P2251K101	7.5	3.0	16.0	5.7	20.0	25.0	0.8
B72214P2271K101	7.5	3.2	16.0	5.9	20.0	25.0	0.8
B72214P2301K101	7.5	3.5	16.0	6.1	20.0	25.0	0.8
B72214P2321K101	7.5	3.7	16.0	6.3	20.0	25.0	0.8
B72214P2351K101	7.5	3.9	16.5	6.7	20.5	25.0	0.8
B72214P2381K101	7.5	4.2	16.5	7.7	20.5	25.0	0.8
B72214P2421K101	7.5	4.5	16.5	8.2	20.5	25.0	0.8
B72214P2461K101	7.5	4.7	16.5	8.5	20.5	25.0	0.8
B72214P2511K101	7.5	4.8	17.0	8.9	21.0	25.0	0.8
B72214P2551K101	7.5	4.9	17.0	9.4	21.0	25.0	0.8
B72214P2621K101	7.5	5.2	17.0	9.9	21.0	25.0	0.8
B72214P2681K101	7.5	5.4	17.0	10.5	21.0	25.0	8.0



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### Leaded varistors

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### Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_{\rm V}$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT  After having continuously applied the maximum allowable AC voltage at UCT ±2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h.  Thereafter, the change of V <sub>V</sub> shall be measured.	IΔV/V (1 mA)I ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 μs), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 μs	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2  Metal balls method, 2500 V <sub>RMS</sub> , 60 s  The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding.  The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown



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Tank	To ak ya akha aka/a wakiki aya	Da su dina sa a sat
Test	Test methods/conditions	Requirement
Climatic sequence	The specimen shall be subjected to:	∆V/V (1 mA)  ≤10%
	a) dry heat at UCT, 16 h, IEC	$R_{ins} \ge 100 M\Omega$
	60068-2-2, test Ba	
	b) damp heat, 1st cycle: 55 °C, 93% r. H., 24 h, IEC	
	60068-2-30, test Db	
	c) cold, LCT, 2 h, IEC 60068-2-1, test	
	Aa	
	d) damp heat, additional 5 cycles:	
	55 °C/25 °C, 93% r. H., 24 h/cycle,	
	IEC 60068-2-30, test Db.	
	Then the specimen shall be stored at	
	room temperature and normal humidity	
	for 1 to 2 h.	
	Thereafter, the change of $V_V$ shall be	
	measured. Thereafter, insulation resis-	
	tance $R_{ins}$ shall be measured at $V = 500$ $V$ .	
Rapid change of	IEC 60068-2-14, test Na, LCT/UCT,	  ΔV/V (1 mA)  ≤5%
temperature	dwell time 30 min, 5 cycles	No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca	ΔV/V (1 mA)  ≤10%
	The specimen shall be subjected to	$R_{ins} \ge 100 M\Omega$
	40 ±2 °C, 90 to 95% r. H. for 56 days	
	without load / with 10% of the maxi-	
	mum continuous DC operating voltage	
	V <sub>DC</sub> . Then stored at room temperature	
	and normal humidity for 1 to 2 h.	
	Thereafter, the change of V <sub>V</sub> shall be	
	measured. Thereafter, insulation resis-	
	tance $R_{ins}$ shall be measured at $V = 500$	
	V (insulated varistors only).	



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Test	Test methods/conditions	Requirement
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of 260 $\pm$ 5 °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for 10 $\pm$ 1 s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V <sub>V</sub> shall be measured and the specimen shall be visually examined.	I∆V/V (1 mA)I ≤5% No visible damage
Tensile strength	IEC 60068-2-21, test Ua1 After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage. Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	I∆V/V (1 mA)I ≤5% No break of solder joint, no wire break



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Test	Test methods/conditions	Requirement
Vibration	IEC 60068-2-6, test Fc, method B4	∆V/V (1 mA)  ≤5%
	Frequency range: 10 55 Hz Amplitude: 0.75 mm or 98 m/s² Duration: 6 h ( $3 \cdot 2$ h) Pulse: sine wave After repeatedly applying a single harmonic vibration according to the table above. The change of $V_V$ shall be measured and the specimen shall be visually examined.	No visible damage
Bump	IEC 60068-2-29, test Eb	∆V/V (1 mA)  ≤5%
	Pulse duration: 6 ms	No visible damage
	Max. acceleration: 400 m/s <sup>2</sup>	_
	Number of bumps:4000	
	Pulse: half sine	
Fire hazard	IEC 60695-11-5 (needle flame test)	5 s max.
	Severity: vertical 10 s	

#### Note:

UCT = Upper category temperature LCT = Lower category temperature

 $R_{ins}$  = Insulation resistance



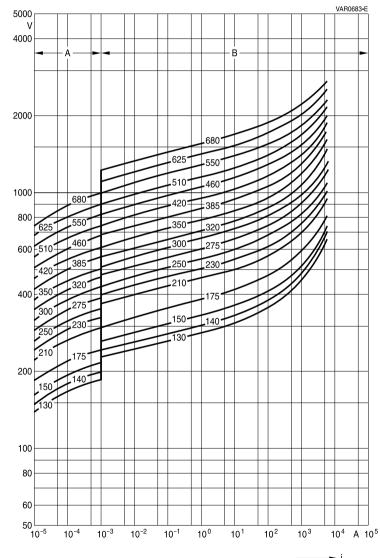


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#### v/i characteristics

v = f (i) - for explanation of the characteristics refer to "General technical information", 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances





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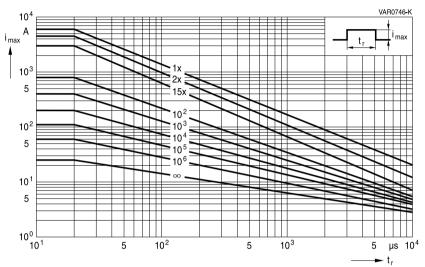
#### AdvanceD-MP, S14 series



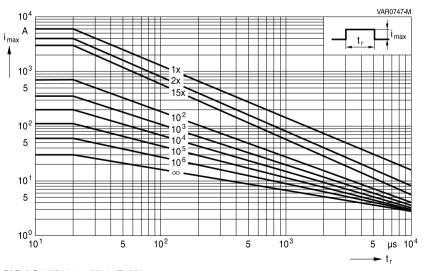
#### **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



#### SIOV-S14K130 ... K460E2K1



#### SIOV-S14K510 ... K680E2K1





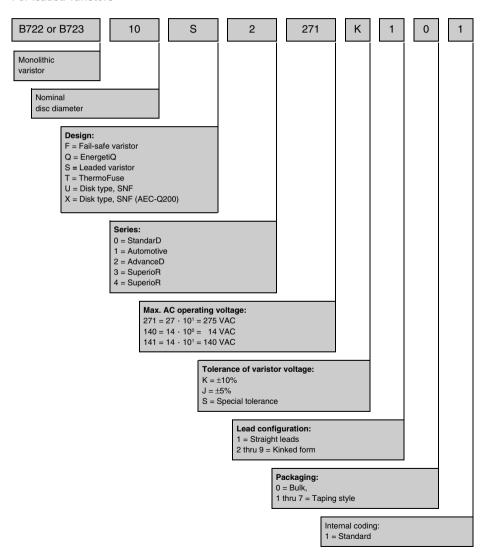
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#### AdvanceD-MP, S14 series

#### Taping, packaging and lead configuration

#### 1 EPCOS ordering code system

#### For leaded varistors





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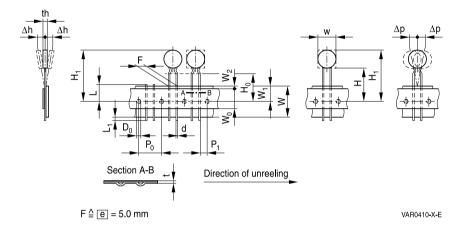
## AdvanceD-MP, S14 series



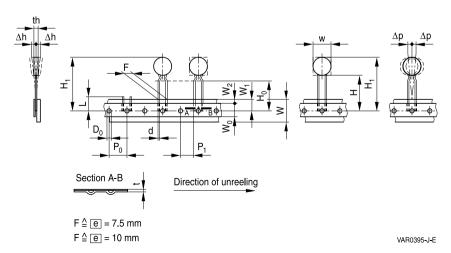
#### 2 Taping and packaging of leaded varistors

Tape packaging for lead spacing  $\boxed{e}$  = 5 fully conforms to IEC 60286-2, while for lead spacings  $\boxed{e}$  = 7.5 and 10 the taping mode is based on this standard.

#### 2.1 Taping in accordance with IEC 60286-2 for lead spacing 5.0 mm



#### 2.2 Taping based on IEC 60286-2 for lead spacing 7.5 and 10 mm





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#### Leaded varistors

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### 2.3 Tape dimensions (in mm)

Sym-	<i>e</i> = 5.0	Tolerance	<i>e</i> = 7.5	Tolerance	<i>e</i> = 10.0	Tolerance	Remarks
bol							
W		max.		max.		max.	see tables in
							each series
th		max.		max.		max.	under
							"Dimensions"
d	0.6	±0.05	0.8	±0.05	1.0	±0.05	
$P_0$	12.7	±0.3	12.71)	±0.3	12.7	±0.3	±1 mm/20
							sprocket holes
P <sub>1</sub>	3.85	±0.7	8.95	±0.8	7.7	±0.8	
F	5.0	+0.6/-0.1	7.5	±0.8	10.0	±0.8	
Δh	0	±2.0	depends of	n s	depends on	S	measured at
$\Delta p$	0	±1.3	0	±2.0	0	±2.0	top of compo-
							nent body
W	18.0	±0.5	18.0	±0.5	18.0	±0.5	
$W_0$	5.5	min.	11.0	min.	11.0	min.	Peel-off
							force ≥ 5 N
$W_1$	9.0	±0.5	9.0	+0.75/-0.5	9.0	+0.75/-0.5	
$W_2$	3.0	max.	3.0	max.	3.0	max.	
Н	18.0	+2.0/-0	18.0	+2.0/-0	18.0	+2.0/-0	2)
$H_0$	16.0	±0.5	16.0	±0.5	16.0	±0.5	3)
	(18.0)		(18.0)				
H <sub>1</sub>	32.2	max.	45.0	max.	45.0	max.	
$D_0$	4.0	±0.2	4.0	±0.2	4.0	±0.2	
t	0.9	max.	0.9	max.	0.9	max.	without lead
L	11.0	max.	11.0	max.	11.0	max.	
L <sub>1</sub>	0.5	max.					

<sup>1)</sup> Taping with  $P_0 = 15.0$  mm upon request

<sup>2)</sup> Applies only to uncrimped types

<sup>3)</sup> Applies only to crimped types (H<sub>0</sub> = 18 upon request)



15.0

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#### AdvanceD-MP, S14 series



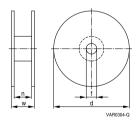
#### 2.4 Taping mode

Example: B72210S0271K1 5 1

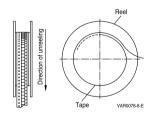
Digit 14

Digit 14	Taping	Reel type	Seating plane height H <sub>0</sub>	Seating plane height H	Pitch distance
	mode		for crimped types	for uncrimped types	$P_0$
			mm	mm	mm
0	_	Bulk	_	_	_
1	G	1	16	18	12.7
2	G2	I	18	_	12.7
3	G3	H	16	18	12.7
4	G4	H	18	_	12.7
5	G5	Ш	16	18	12.7
6	GA	Ammo pack	16	18	12.7
7	G2A	Ammo pack	18	_	12.7
Internal	coding fo	r special tapin	g		
	G6	Ш	18	_	12.7
	G10	H	16	18	15.0
	G11	П	18	_	15.0
	G10A	Ammo pack	16	18	15.0

#### 2.5 Reel dimension



G11A Ammo pack 18



#### Dimensions (in mm)

Reel type	d	f	n	w
I	360 max.	31 ±1	approx. 45	54 max.
II	360 max.	31 ±1	approx. 55	64 max.
III	500 max.	23 ±1	approx. 59	72 max.

If reel type III is not compatible with insertion equipment because of its large diameter, nominal disk diameter 10 mm and 14 mm can be supplied on reel II upon request (taping mode G3).

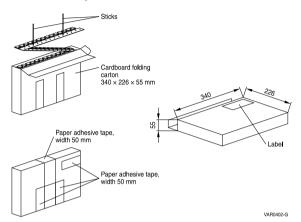




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#### AdvanceD-MP, S14 series

#### 2.6 Ammo pack dimensions



#### 3 Lead configuration

Straight leads are standard for disk varistors. Other lead configurations as crimp style or customer-specific lead wire length according to 3.1, 3.2, 3.3 and 3.4 are optional. Crimped leads (non-standard) are differently crimped for technical reasons; the individual crimp styles are denoted by consecutive numbers (S, S2 through S5) as shown in the dimensional drawings below.

The crimp styles of the individual types can be seen from the type designation in the ordering tables.

#### 3.1 Crimp style mode

Example: B72210S0271K 5 01 Digit 13

Digit 13 of ordering code	Crimp style	Figure
1	Standard, straight leads	1
2	S2	2
3	S3	3
5	S5	4
Available upon request		
Internal coding	1	5



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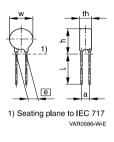
#### AdvanceD-MP, S14 series



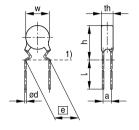
#### 3.2 Standard leads and non-standard crimp styles

The basic dimensions in figure 1 to 5 are valid for types with either round or square (EnergetiQ series) component head.

#### Standard, straight leads

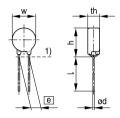


## Non-standard, crimp style S2



1) Seating plane to IEC 60717 VAR0411-F-E

Non-standard, crimp style S3



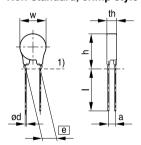
1) Seating plane to IEC 60717 VAR0396-R-E

Figure 1

Figure 2

Figure 3

#### Non-standard, crimp style S5



1) Seating plane to IEC 60717 VAR0726-M-E

Figure 4





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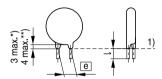
### AdvanceD-MP, S14 series

#### 3.3 Trimmed leads (non-standard)

Varistors with cut leads available upon request.

Lead length tolerances:

Straight leads +/-0.8 mmCrimped leads +/-0.5 mm Minimum lead length 3.0 mm



- 1) Seating plane to IEC 60717
- \*) For round component head
- \*\*) For EnergetiQ series, square component head VAR0642-U-E

#### Figure 5



## Leaded varistors B722 AdvanceD-MP, S14 series

#### Cautions and warnings

#### General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

#### Storage

- 1. Store SIOVs only in original packaging. Do not open the package prior to processing.
- 2. Recommended storage conditions in original packaging:

Storage temperature: −25 °C ... +45 °C,

Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Dew precipitation: is to be avoided.

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- The SIOV type series should be soldered after shipment from EPCOS within the time specified:

SIOV-S, -Q, -LS, -B, -SNF 24 months ETFV/ T series. -CU 12 months.

#### Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

#### Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.
- Temperatures of all preheat stages and the solder bath must be strictly controlled especially for T series (T14 and T20).





B722

#### AdvanceD-MP, S14 series

#### Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

#### Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.

#### Display of ordering codes for EPCOS products

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes



# Leaded varistors B722\* AdvanceD-MP, S14 series



#### Symbols and terms

Symbol	Term
С	Capacitance
$C_{typ}$	Typical capacitance
i	Current
i <sub>c</sub>	Current at which V <sub>c, max</sub> is measured
I <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
I <sub>max</sub>	Maximum discharge current
I <sub>n</sub>	Nominal discharge current to UL 1449
LCT	Lower category temperature
$L_{typ}$	Typical inductance
$P_{max}$	Maximum average power dissipation
$R_{ins}$	Insulation resistance
$R_{min}$	Minimum resistance
$T_A$	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
V	Voltage
$V_{clamp}$	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current i <sub>c</sub>
$V_{DC}$	DC operating voltage
$V_{jump}$	Maximum jump start voltage
$V_{\text{max}}$	Maximum voltage
$V_{op}$	Operating voltage
$V_{RMS}$	AC operating voltage, root-mean-square value
$V_{RMS,\;op,\;max}$	Root-mean-square value of max. DC operating voltage incl. ripple current
$V_{\text{surge}}$	Super imposed surge voltage
$V_{v}$	Varistor voltage
$\Delta V_{V}$	Tolerance of varistor voltage
$W_{LD}$	Maximum load dump
$W_{max}$	Maximum energy absorption
е	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



#### Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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#### Important notes

8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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