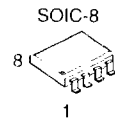


# SOIC-8 Dual/Single DMOS



- Second source of Siliconix Little Foot™ SI9xxx Series.

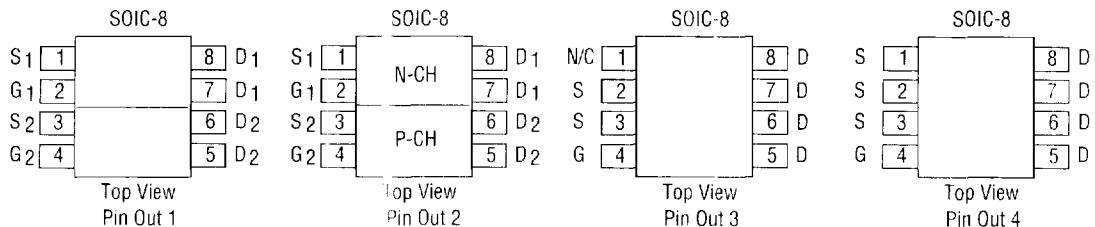
## N Channel

(Vds) Min	Device	I <sub>DMOS</sub> Max (A)			I <sub>Q</sub> Max	P <sub>D</sub> Max	Configuration	Pin Out
		V <sub>GS</sub> = 0V	V <sub>GS</sub> = 4.5V	V <sub>GS</sub> = 2.7V				
30	NDS9410	0.03	0.05		7	2	Single	3
30	NDS9936	0.05	0.08		5.0	2	Dual	1
60	NDS9945	0.1	0.2		3.5	2	Dual	1
50	NDS9955	0.13	0.2		3	2	Dual	1
20	NDS9956	0.1	0.2		3.5	2	Dual	1
50	NDS9959	0.3	0.5		2.0	2	Dual	1

## P Channel

(Vds) Min	Device	I <sub>DMOS</sub> Max (A)			I <sub>Q</sub> (A) Max	P <sub>D</sub> (Watts) Max	Configuration	Pin Out
		V <sub>GS</sub> = 10V	V <sub>GS</sub> = 4.5V	V <sub>GS</sub> = 2.7V				
-20	NDS9400	0.25	0.4		-2.5	2	Single	3
-20	NDS9405	0.1	0.16		-4.3	2	Single	3
-60	NDS9407*	0.15	0.24		-3.3	2	Single	4
-20	NDS9430	0.06	0.1		-5.3	2	Single	4
-12	NDS9933		0.13	0.21	3.2	2	Dual	1
-30	NDS9435	0.07	0.1		-5.3	2	Single	4
-12	NDS9433		0.075	0.11	5.1	2	Single	4
-20	NDS9947	0.11	0.19		-3.5	2	Dual	1
-60	NDS9948*	0.25	0.5		-2.3	2	Dual	1
-20	NDS9953	0.25	0.4		-2.3	2	Dual	1

\* Advance Information.



# SOIC-8 Dual/Single DMOS (continued)

## Complementary N-P Channel

(Volts) Min	Part No.	R <sub>DS(on)</sub> Max (Ω)			V <sub>GS</sub>	I <sub>D</sub>	Pin Out
		V <sub>GS</sub> = 10V	V <sub>GS</sub> = 10V	V <sub>GS</sub> = 10V			
20	NDS9942	0.125	0.25		3	2	N Channel
-20			0.2	0.35	-2.5		P Channel
20	NDS9943	0.125	0.25		3	2	N Channel
-20			0.16	0.3	-2.8		P Channel
25	NDS9952	0.1	0.15		3	2	N Channel
-25			0.25	0.4	-2.3		P Channel
20	NDS9958	0.1	0.15		3.5	2	N Channel
-20			0.11	0.19	-3		P Channel

