

Features

- · High Current Load Applications
- · Load Switching
- · Hard Switched and High Frequency Circuits
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

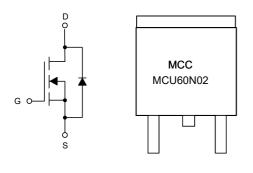
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 4.3°C/W Junction to Case^(Note3)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Volltage	V_{GS}	±10	V
Drain Current (T _C =25°C)	I _D	60	Α
Drain Current (T _C =100°C)	I _D	42	Α
Pulsed Drain Current ^(Note 1)	I _{DM}	210	Α
Total Power Dissipation (T _C =25°C)	P_{D}	35	W
Total Power Dissipation (T _C =100°C)	P _D	18	W
Single Pulsed Avalanche Energy ^(Note2)	E _{AS}	195	mJ

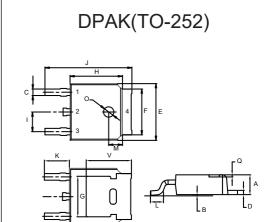
Note:

- 1.Pulse Test: Pulse Width≤300us, Duty cycle ≤2%.
- $2.T_i$ =25°C, VDD=15V, VG=10V, L=0.5mH, Rg=25 Ω
- $3.R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

Internal Structure and Marking Code



N-CHANNEL MOSFET



- 1. Gate 2,4. Drain
- 3. Source

DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOIL
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
ı	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

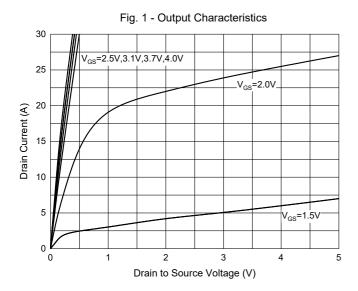


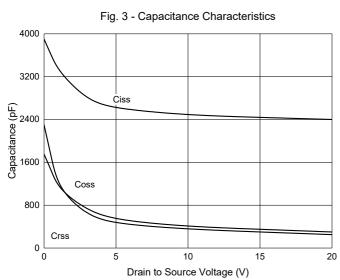
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

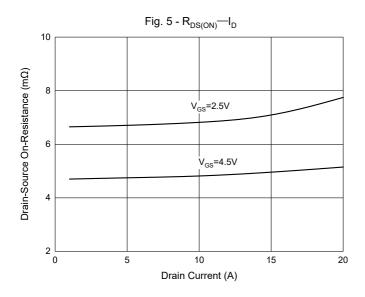
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	20			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±10V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.4	0.62	1	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =20A		4.5	6.0	mΩ	
		V _{GS} =2.5V, I _D =15A		5.5	8.8	mΩ	
		V _{GS} =1.8V, I _D =10A		8.0	14	mΩ	
Dynamic Characteristics							
Input Capacitance	C _{iss}			2450		pF	
Output Capacitance	C _{oss}	V_{DS} =10V, V_{GS} =0V,f=1MHz		430			
Reverse Transfer Capacitance	C _{rss}			205			
Turn-On Delay Time	t _{d(on)}			12		ns	
Turn-On Rise Time	t _r	V _{GS} =4.5V,V _{DD} =10V,		26			
Turn-Off Delay Time	t _{d(off)}	$I_D=10A,R_L=1\Omega,R_{GEN}=3\Omega$		35			
Turn-Off Fall Time	t _f			10			
Total Gate Charge	Q_g			65			
Gate-Source Charge	Q_{gs}	V_{DS} =10V, V_{GS} =4.5V, I_{D} =15A		15		nC	
Gate-Drain Charge	Q_{gd}			13			
Body Diode Characteristics							
Diode Forward Current	Is				60	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V	
Reverse Recovery Time	t _{rr}	I _F =15A, di/dt=100A/μs		35		ns	
Reverse Recovery Charge	Q _{rr}	- 15A, αι/αι-100A/μ5		39		nC	

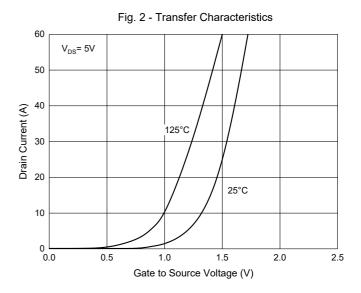


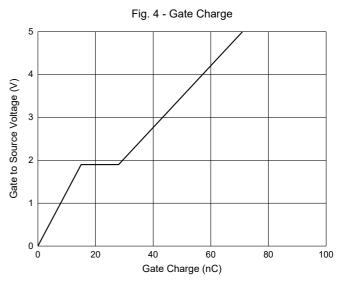
Curve Characteristics

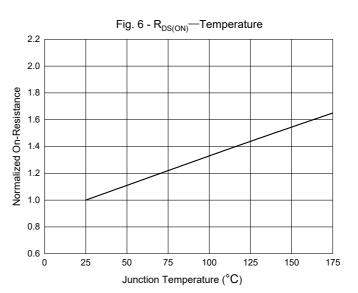














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note : Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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