# VS-35MT120PB

**Vishay Semiconductors** 



HEXFRED<sup>®</sup> Ultrafast Diodes, 30 A (Single Phase Bridge MTP Power Modules)



PRIMARY CHARACTERISTICS			
V <sub>R</sub>	1200 V		
$V_F$ (typical) at $I_F = 30$ A	2.46 V		
I <sub>O</sub> at 88 °C	30 A		
Q <sub>rr</sub> (typical)	720 nC		
I <sub>RRM</sub> (typical)	12 A		
t <sub>rr</sub> (typical)	121 ns		
dl <sub>(rec)M</sub> /dt (typical)	300 A/µs		
Package	MTP		
Circuit configuration	Single phase bridge		

#### **FEATURES**

- Low profile package
- Low t<sub>rr</sub> and Q<sub>rr</sub>
- Soft reverse recovery
- Direct mounting to heatsink
- · Round pin with PCB solderable terminals
- UL approved file E78996 😱
- Low junction to case thermal resistance
- 3500 V<sub>BMS</sub> insulation voltage
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

A range of extremely compact single-phase rectifier bridges offering efficient and reliable operation.

The low profile package has been specifically conceived to maximize space saving and optimize the electrical layout of the application specific power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Cathode to anode voltage	V <sub>R</sub>		1200	V
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 88 °C	30	
Single pulse forward current per diode	I <sub>FSM</sub>	10 ms sine or 6 ms rectangular pulse, $T_J$ = 25 °C	300	А
Maximum repetitive forward current per diode	I <sub>FRM</sub>		200	
Maximum power dissipation per diode	PD	T <sub>C</sub> = 88 °C	85	W
Operating junction temperature range	TJ		-40 to +150	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	1200	-	-	V
Forward voltage		I <sub>F</sub> = 30 A	-	2.46	3.34	V
	V <sub>F</sub>	I <sub>F</sub> = 60 A	-	3.11	4.45	
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	-	2.32	2.96	
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 125 °C	-	3.07	3.96	
Reverse leakage current		V <sub>R</sub> = 1200 V	-	2.8	50	μA
	I <sub>R</sub>	V <sub>R</sub> = 1200 V, T <sub>J</sub> = 125 °C	-	2	10	mA
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	50	75	pF

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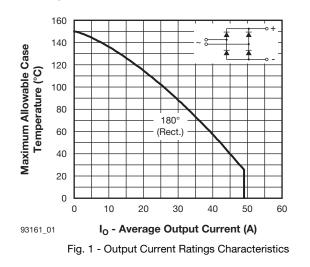




<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T <sub>J</sub> = 25 °C		-	121	170	ns
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	180	260	115
Book receivery ourrent	I <sub>RR</sub>	T <sub>J</sub> = 25 °C	l <sub>F</sub> = 30 A dl⊧/dt = 200 A/µs	-	12	16	A
Peak recovery current		T <sub>J</sub> = 125 °C		-	17	24	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	$V_{\rm R} = 200 \text{ V}$	-	720	1350	nC
neverse recovery charge		$T_{\rm J} = 125 \ ^{\circ}{\rm C}$		-	1540	2310	ne
Book rate of fall of recovery ourrent during t	t <sub>b</sub> dl <sub>(rec)M</sub> /dt	T <sub>J</sub> = 25 °C		-	300	-	A∕µs
Peak rate of fall of recovery current during $t_b$		T <sub>J</sub> = 125 °C		-	265	-	γγµs

INSULATION TABLE				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
RMS insulation voltage	V <sub>INS</sub>	$T_J = 25 \text{ °C}$ , all terminals shorted, f = 50 Hz, t = 1 s	3500	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-40 to +150	°C	
Maximum thermal resistance, per module	- R <sub>thJC</sub>	DC operation	0.18		
junction to case per junction	nthJC	R <sub>thJC</sub> DC operation	0.73	°C/W	
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.06		
Approximate weight			65	g	
Mounting torque, ± 10 % to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound. Lubricated threads.	4	Nm	



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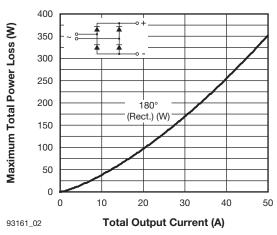


Fig. 2 - On-State Power Loss Characteristics

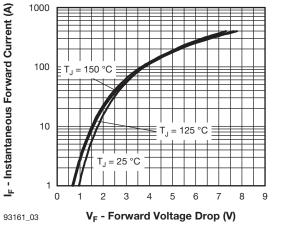
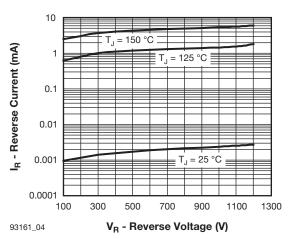
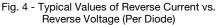
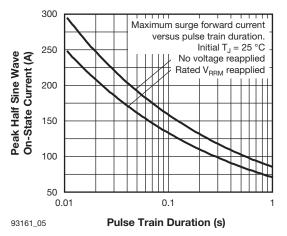


Fig. 3 - Typical Forward Voltage Drop Characteristics (Per Diode)









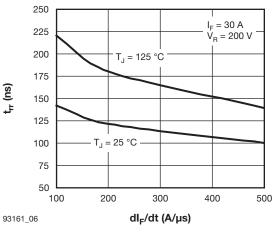


Fig. 6 - Typical Reverse Time vs. dl<sub>F</sub>/dt (Per Diode)

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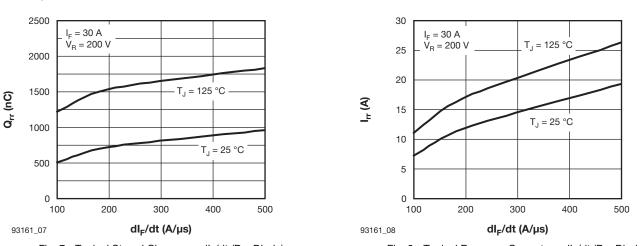


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt (Per Diode)

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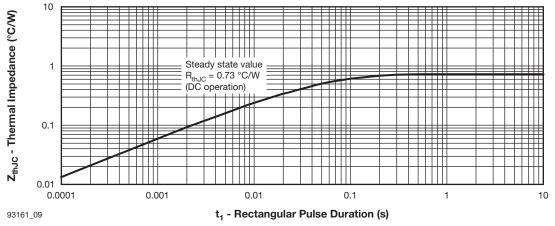
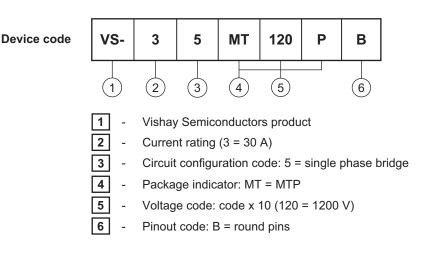


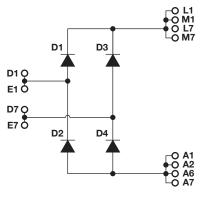
Fig. 9 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Diode)

#### **ORDERING INFORMATION TABLE**





#### **CIRCUIT CONFIGURATION**

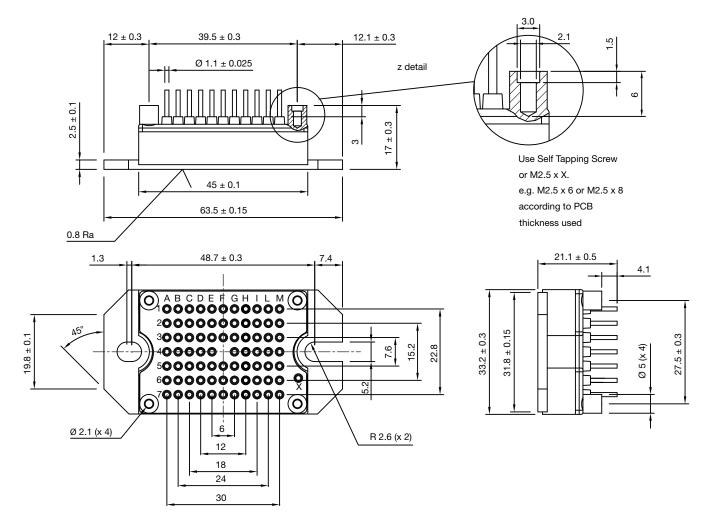


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95383			



**MTP - Full Pin** 

#### **DIMENSIONS** in millimeters



PINS POSITION WITH TOLERANCE 🗘 Ø 0.6



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