

## Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <http://www.nxp.com>, <http://www.philips.com/> or <http://www.semiconductors.philips.com/>, use <http://www.nexperia.com>

Instead of [sales.addresses@www.nxp.com](mailto:sales.addresses@www.nxp.com) or [sales.addresses@www.semiconductors.philips.com](mailto:sales.addresses@www.semiconductors.philips.com), use [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com) (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © **Nexperia B.V. (year). All rights reserved.**

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# PESD5V0V1BA; PESD5V0V1BB; PESD5V0V1BL

Very low capacitance bidirectional ESD protection diodes

Rev. 2 — 9 November 2012

Product data sheet

## 1. Product profile

### 1.1 General description

Very low capacitance bidirectional ElectroStatic Discharge (ESD) protection diodes in small Surface-Mounted Device (SMD) plastic packages designed to protect one signal line from the damage caused by ESD and other transients.

Table 1. Product overview

Type number	Package		Package configuration
	NXP	JEITA	
PESD5V0V1BA	SOD323	SC-76	very small
PESD5V0V1BB	SOD523	SC-79	ultra small and flat lead
PESD5V0V1BL	SOD882	-	leadless ultra small

### 1.2 Features and benefits

- Bidirectional ESD protection of one line
- Very low diode capacitance:  $C_d = 11$  pF
- Max. peak pulse power:  $P_{PP} = 45$  W
- Low clamping voltage:  $V_{CL} = 12.5$  V
- Ultra low leakage current:  $I_{RM} < 1$  nA
- ESD protection up to 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge);  $I_{PP} = 4.8$  A
- AEC-Q101 qualified

### 1.3 Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Subscriber Identity Module (SIM) card protection
- Communication systems
- Portable electronics
- 10/100 Mbit/s Ethernet
- FireWire

### 1.4 Quick reference data

Table 2. Quick reference data

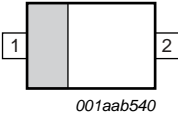
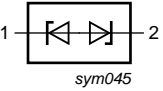

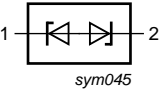
$T_{amb} = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage		-	-	5	V
$C_d$	diode capacitance	$f = 1$ MHz; $V_R = 0$ V	-	11	13	pF



## 2. Pinning information

**Table 3. Pinning**

Pin	Description	Simplified outline	Graphic symbol
<b>PESD5V0V1BA; PESD5V0V1BB</b>			
1	cathode (diode 1) <a href="#">[1]</a>	 <p>001aab540</p>	 <p>sym045</p>
2	cathode (diode 2)		
<b>PESD5V0V1BL</b>			
1	cathode (diode 1) <a href="#">[1]</a>	 <p>Transparent top view</p>	 <p>sym045</p>
2	cathode (diode 2)		

[1] The marking bar indicates pin 1.

## 3. Ordering information

**Table 4. Ordering information**

Type number	Package		
	Name	Description	Version
PESD5V0V1BA	SC-76	plastic surface-mounted package; 2 leads	SOD323
PESD5V0V1BB	SC-79	plastic surface-mounted package; 2 leads	SOD523
PESD5V0V1BL	-	leadless ultra small plastic package; 2 terminals; body 1.0 × 0.6 × 0.5 mm	SOD882

## 4. Marking

**Table 5. Marking codes**

Type number	Marking code
PESD5V0V1BA	1K
PESD5V0V1BB	Z9
PESD5V0V1BL	X1

## 5. Limiting values

**Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per diode</b>					
$P_{PP}$	peak pulse power	$t_p = 8/20 \mu s$ <a href="#">[1]</a>	-	45	W
$I_{PP}$	peak pulse current	$t_p = 8/20 \mu s$ <a href="#">[1]</a>	-	4.8	A

**Table 6. Limiting values ...continued**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per device</b>					
$T_j$	junction temperature		-	150	°C
$T_{amb}$	ambient temperature		-55	+150	°C
$T_{stg}$	storage temperature		-65	+150	°C

[1] Non-repetitive current pulse 8/20  $\mu$ s exponential decay waveform according to IEC 61000-4-5.

**Table 7. ESD maximum ratings**

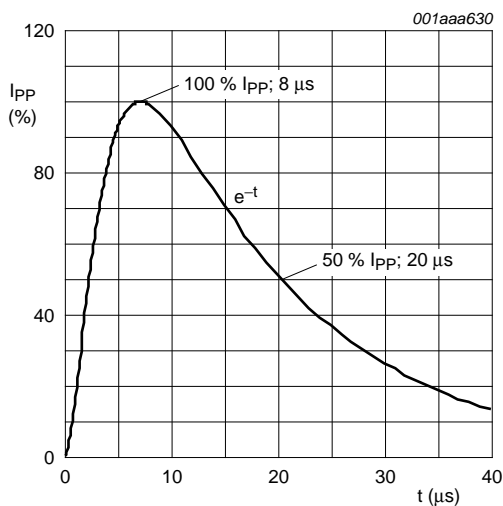
$T_{amb} = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{ESD}$	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	<a href="#">U</a> -	30	kV
		machine model	-	2	kV
		MIL-STD-883 (human body model)	-	16	kV

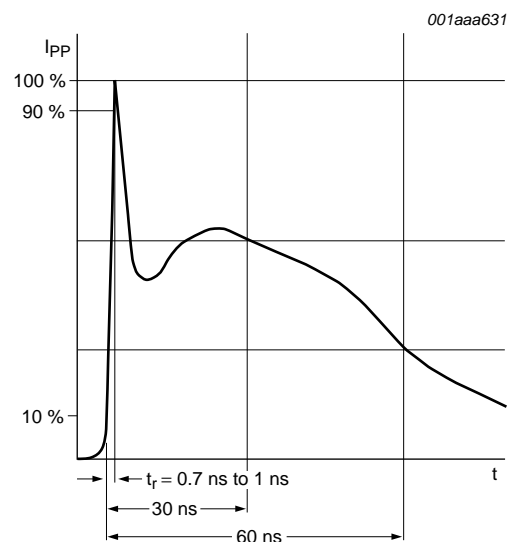
[1] Device stressed with ten non-repetitive ESD pulses.

**Table 8. ESD standards compliance**

Standard	Conditions
IEC 61000-4-2; level 4 (ESD)	> 15 kV (air); > 8 kV (contact)
MIL-STD-883; class 3B (human body model)	> 8 kV



**Fig 1. 8/20  $\mu$ s pulse waveform according to IEC 61000-4-5**



**Fig 2. ESD pulse waveform according to IEC 61000-4-2**

**6. Characteristics**

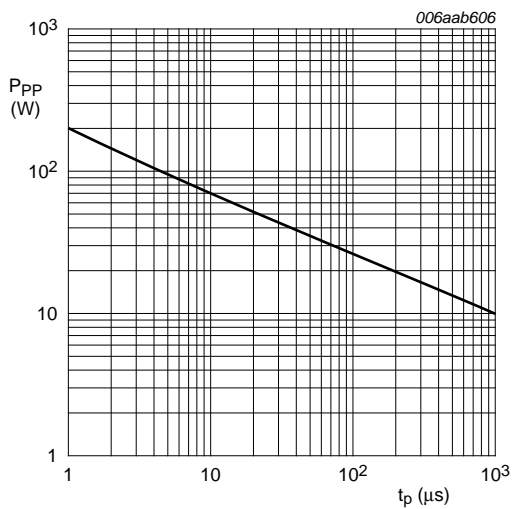
**Table 9. Characteristics**

*T<sub>amb</sub> = 25 °C unless otherwise specified.*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage		-	-	5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V	-	< 1	10	nA
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 5 mA	5.8	6.8	7.8	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V	-	11	13	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 4.8 A	[1] -	-	12.5	V
r <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A	[2] -	0.2	-	Ω
r <sub>dif</sub>	differential resistance	I <sub>R</sub> = 5 mA	-	-	35	Ω

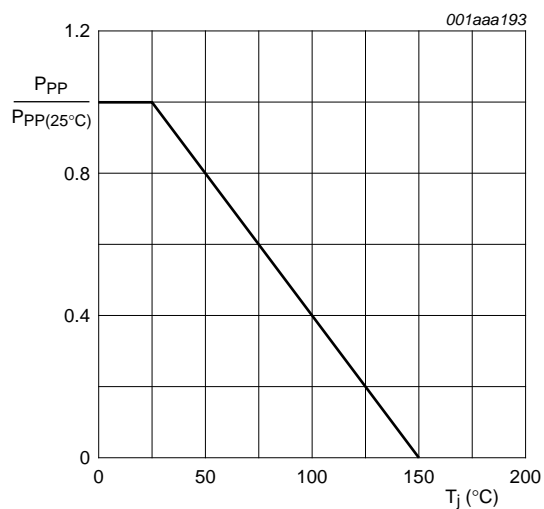
[1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.

[2] Non-repetitive current pulse, Transmission Line Pulse (TLP) t<sub>p</sub> = 100 ns; square pulse; ANS/IESD STM5.1-2008.

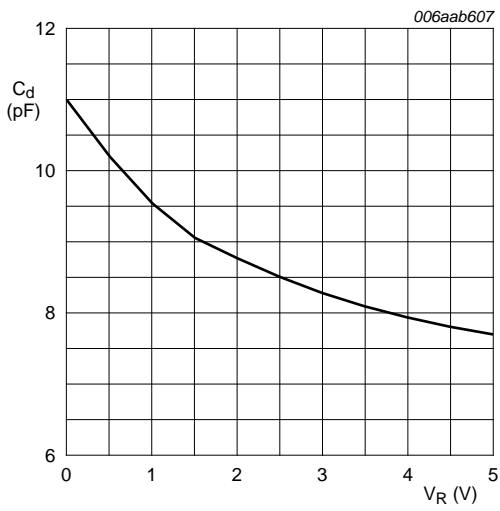


T<sub>amb</sub> = 25 °C

**Fig 3. Peak pulse power as a function of exponential pulse duration; typical values**



**Fig 4. Relative variation of peak pulse power as a function of junction temperature; typical values**



f = 1 MHz; T<sub>amb</sub> = 25 °C

Fig 5. Diode capacitance as a function of reverse voltage; typical values

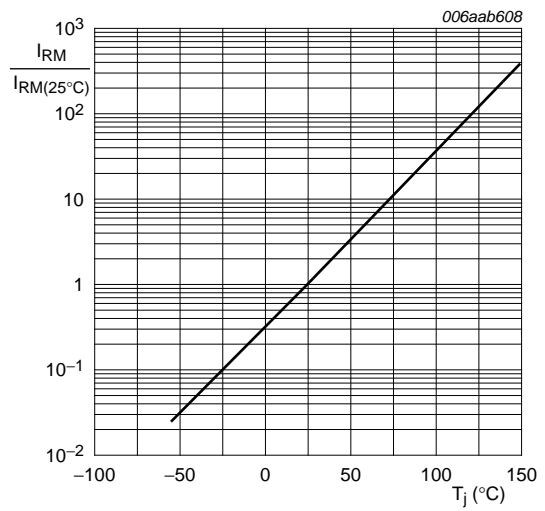
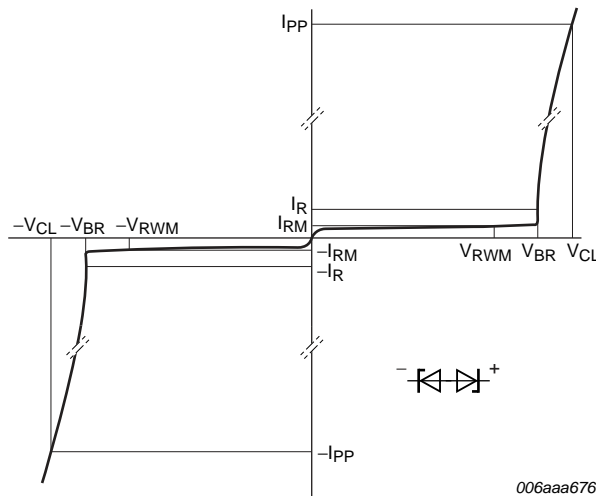
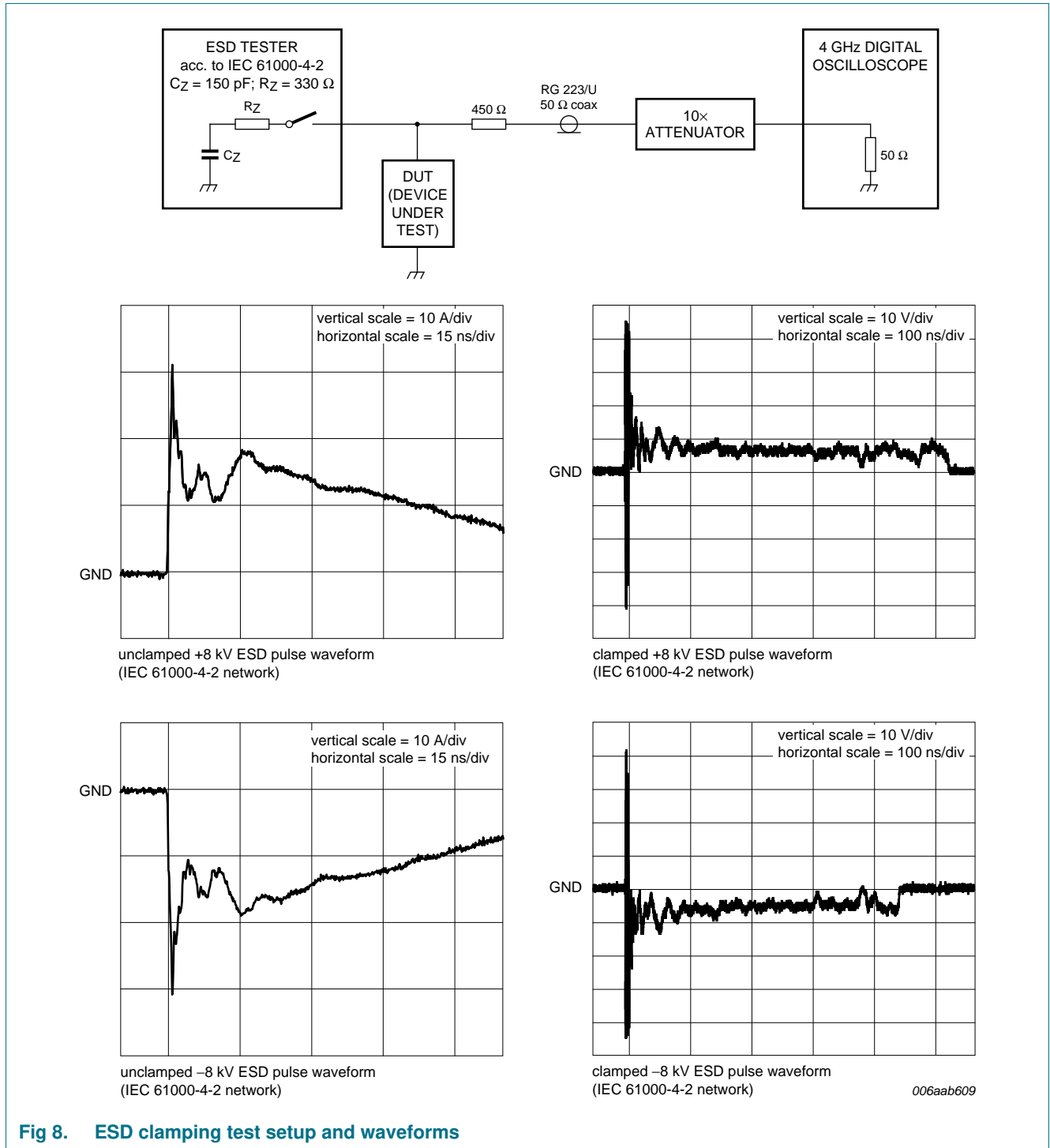


Fig 6. Relative variation of reverse leakage current as a function of junction temperature; typical values



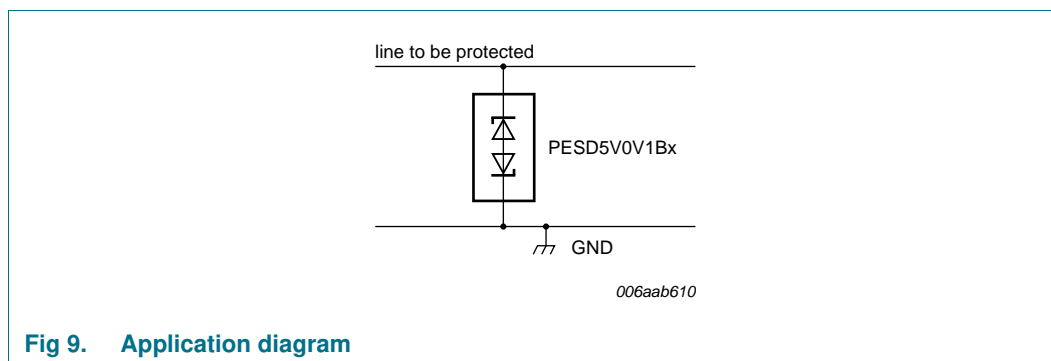
006aaa676

Fig 7. V-I characteristics for a bidirectional ESD protection diode



## 7. Application information

The PESD5V0V1Bx series is designed for the protection of one bidirectional data or signal line from the damage caused by ESD and surge pulses. The devices may be used on lines where the signal polarities are both, positive or negative with respect to ground. The PESD5V0V1Bx series provides a surge capability of 45 W per line for an 8/20  $\mu$ s waveform.



### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

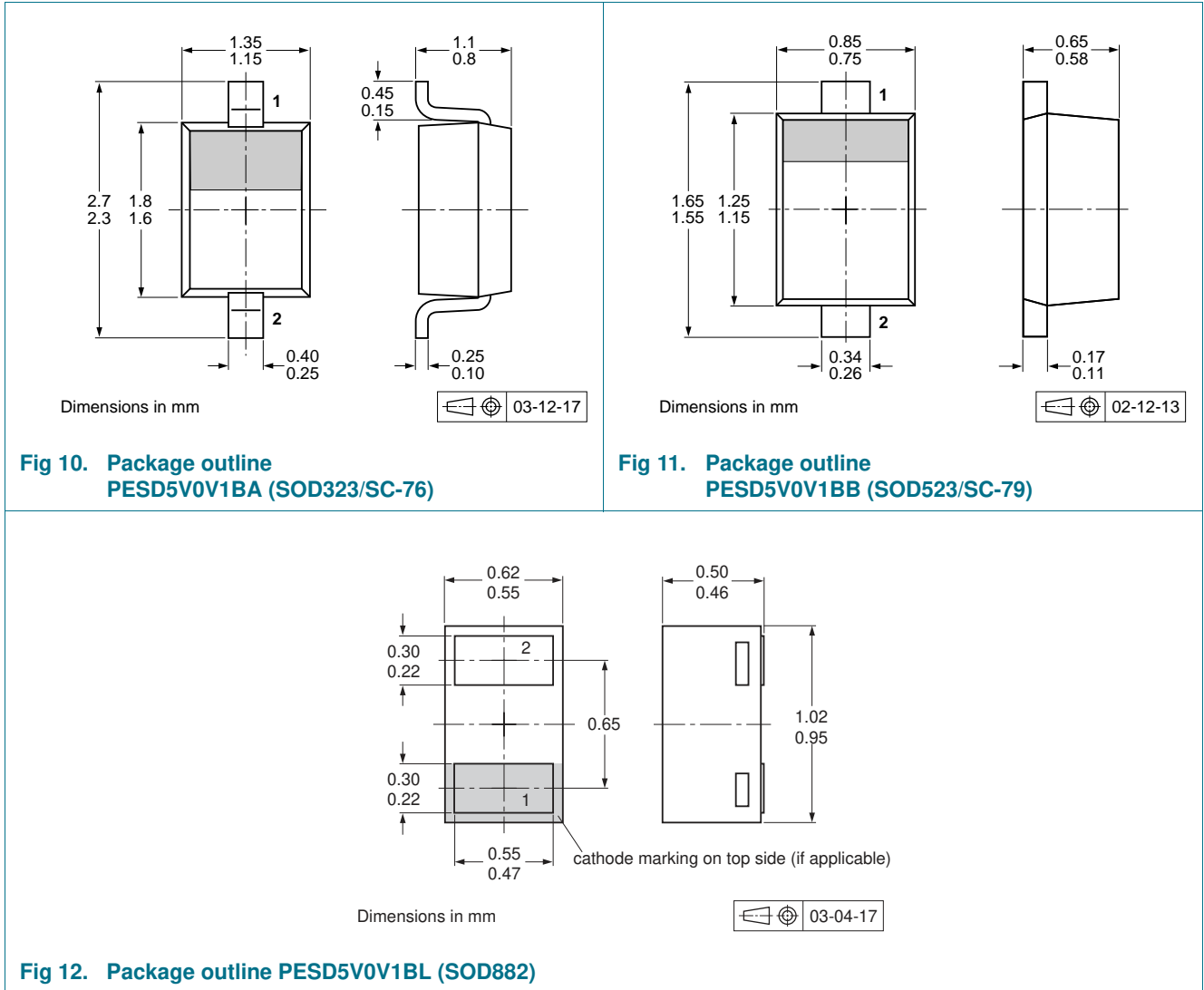
## 8. Test information

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.



**9. Package outline**



**10. Packing information**

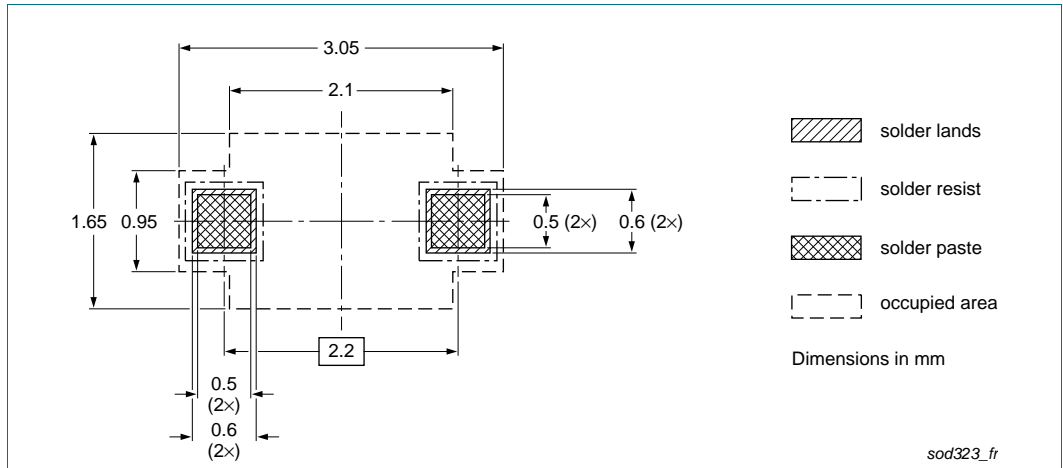
**Table 10. Packing methods**

The indicated -xxx are the last three digits of the 12NC ordering code.<sup>[1]</sup>

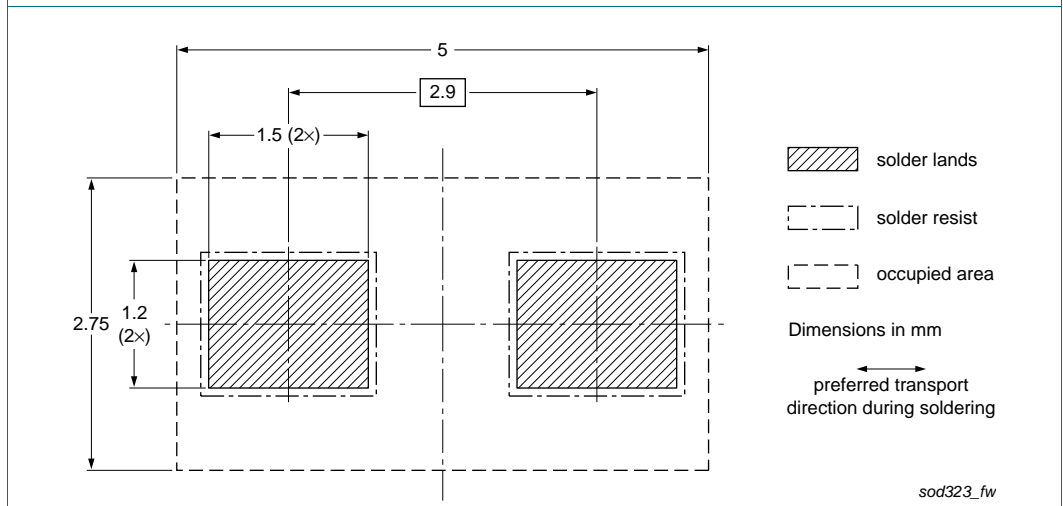
Type number	Package	Description	Packing quantity		
			3000	8000	10000
PESD5V0V1BA	SOD323	4 mm pitch, 8 mm tape and reel	-115	-	-135
PESD5V0V1BB	SOD523	2 mm pitch, 8 mm tape and reel	-	-315	-
		4 mm pitch, 8 mm tape and reel	-115	-	-135
PESD5V0V1BL	SOD882	2 mm pitch, 8 mm tape and reel	-	-	-315

[1] For further information and the availability of packing methods, see [Section 14](#).

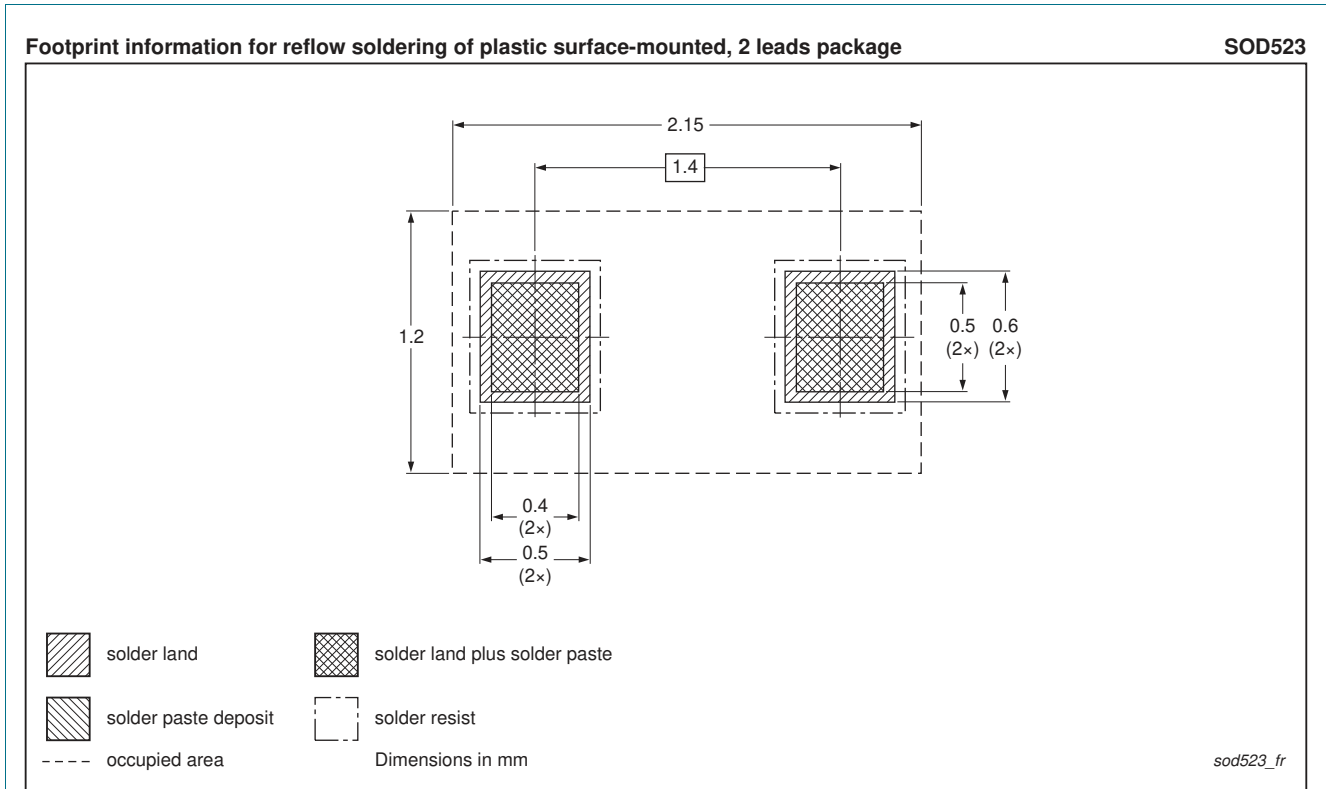
**11. Soldering**



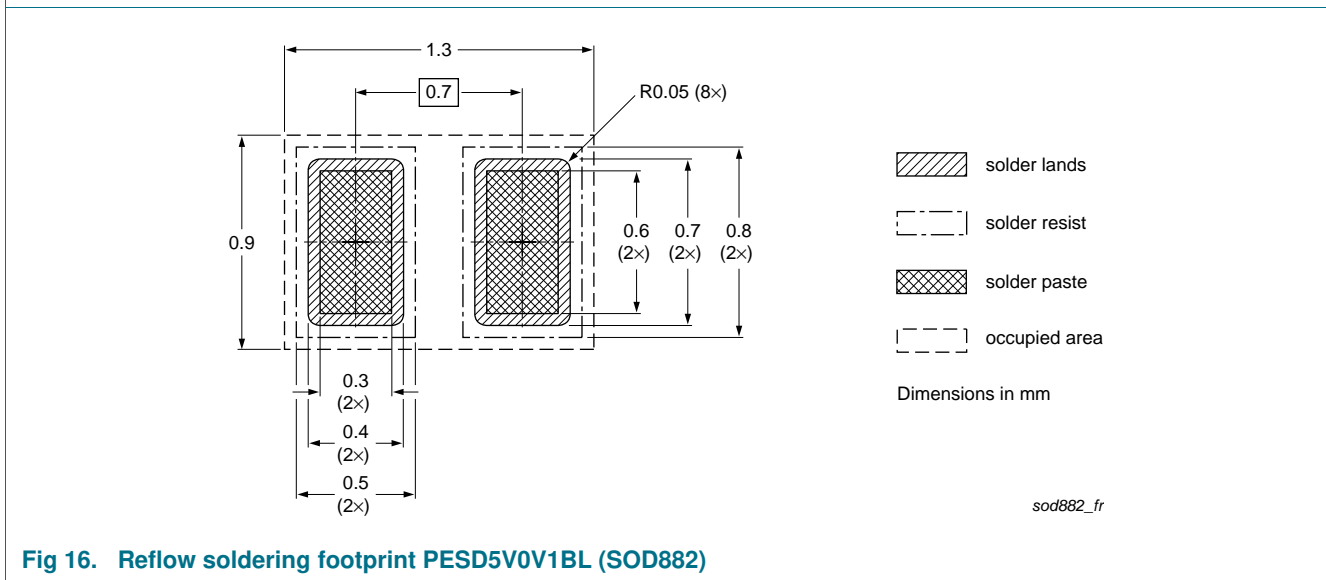
**Fig 13. Reflow soldering footprint PESD5V0V1BA (SOD323/SC-76)**



**Fig 14. Wave soldering footprint PESD5V0V1BA (SOD323/SC-76)**



**Fig 15. Reflow soldering footprint PESD5V0V1BB (SOD523/SC-79)**



**Fig 16. Reflow soldering footprint PESD5V0V1BL (SOD882)**

## 12. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0V1BA_BB_BL v.2	20121109	Product data sheet	-	PESD5V0V1BA_BB_BL v.1
Modifications:				
				<ul style="list-style-type: none"><li>• <a href="#">Table 9 “Characteristics”</a>: added dynamic resistance <math>r_{dyn}</math></li><li>• <a href="#">Figure 15</a>: updated</li><li>• <a href="#">Section 13 “Legal information”</a>: updated</li></ul>
PESD5V0V1BA_BB_BL v.1	20090728	Product data sheet	-	-

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 13.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 13.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use in automotive applications** — This NXP Semiconductors product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

### 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 14. Contact information

---

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 15. Contents

<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Applications . . . . .	1
1.4	Quick reference data . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Marking</b> . . . . .	<b>2</b>
<b>5</b>	<b>Limiting values</b> . . . . .	<b>2</b>
<b>6</b>	<b>Characteristics</b> . . . . .	<b>4</b>
<b>7</b>	<b>Application information</b> . . . . .	<b>7</b>
<b>8</b>	<b>Test information</b> . . . . .	<b>7</b>
8.1	Quality information . . . . .	7
<b>9</b>	<b>Package outline</b> . . . . .	<b>8</b>
<b>10</b>	<b>Packing information</b> . . . . .	<b>8</b>
<b>11</b>	<b>Soldering</b> . . . . .	<b>9</b>
<b>12</b>	<b>Revision history</b> . . . . .	<b>11</b>
<b>13</b>	<b>Legal information</b> . . . . .	<b>12</b>
13.1	Data sheet status . . . . .	12
13.2	Definitions . . . . .	12
13.3	Disclaimers . . . . .	12
13.4	Trademarks . . . . .	13
<b>14</b>	<b>Contact information</b> . . . . .	<b>13</b>
<b>15</b>	<b>Contents</b> . . . . .	<b>14</b>

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2012.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 9 November 2012

Document identifier: PESD5V0V1BA\_BB\_BL