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FJBE2150D ESBC[™] Rated NPN Silicon Transistor

ESBC Features (FDC655 MOSFET)

| V _{CS(ON)} | Ι _C | Equiv. R _{CS(ON)} |
|---------------------|----------------|----------------------------|
| 0.131 V | 0.5 A | 0.261 Ω ⁽¹⁾ |

- · Low Equivalent On Resistance
- Very Fast Switch: 150 kHz
- Squared RBSOA: Up to 1500 V
- Avalanche Rated
- Low Switching Losses
- Reliable HV Switch: No False Triggering due to High dv/dt Transients

Applications

- High-Voltage and High-Speed Power Switches
- Emitter-Switched Bipolar/MOSFET Cascode (ESBC[™])
- Smart Meters, Smart Breakers, HV Industrial Power Supplies
- Motor Drivers and Ignition Drivers

Description

The FJBE2150D is a low-cost, high-performance power switch designed to be used in an ESBC[™] configuration in applications such as: power supplies, motor drivers, smart grid, or ignition switches. The power switch is designed to operate up to 1500 volts and up to 3 amps, while providing exceptionally low on-resistance and very low switching losses.

The ESBC[™] switch is designed to be driven using off-theshelf power supply controllers or drivers. The ESBC[™] MOSFET is a low-voltage, low-cost, surface-mount device that combines low-input capacitance and fast switching. The ESBC[™] configuration further minimizes the required driving power because it does not have Miller capacitance.

The FJBE2150D provides exceptional reliability and a large operating range due to its square Reverse-Bias-Safe-Operating-Area (RBSOA) and rugged design. The device is avalanche rated and has no parasitic transistors, so is not prone to static dv/dt failures.

The power switch is manufactured using a dedicated high-voltage bipolar process and is packaged in high-voltage HV-D2PAK rated at 2500 V creepage and clearance.

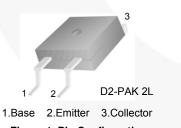
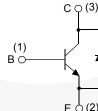


Figure 1. Pin Configuration



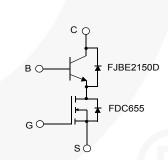


Figure 3. ESBC Configuration⁽²⁾

Ordering Information

| Part Number Marking | | Package | Packing Method | |
|---------------------|--------|-----------------------|----------------|--|
| FJBE2150DTU | J2150D | D2-PAK 2L (TO-263 2L) | Tube | |

Figure 2. Internal Schematic Diagram

Notes:

- 1. Figure of Merit.
- 2. Other Fairchild MOSFETs can be used in this ESBC application.

January 2016

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

| Symbol | Parameter | Value | Unit |
|------------------|--|--------------|------|
| V _{CBO} | Collector-Base Voltage | 1500 | V |
| V _{CEO} | Collector-Emitter Voltage | 800 | V |
| V _{EBO} | Emitter-Base Voltage | 12 | V |
| Ι _C | Collector Current | 2 | Α |
| I _{CP} | Collector Current (Pulse) | 3 | Α |
| Ι _Β | Base Current | 1 | Α |
| I _{BP} | Base Current (Pulse) | 2 | Α |
| PD | Power Dissipation $(T_C = 25^{\circ}C)$ | 110 | W |
| TJ | Operating and Junction Temperature Range | - 55 to +125 | °C |
| T _{STG} | Storage Temperature Range | - 65 to +150 | °C |
| EAS | Avalanche Energy (T _J = 25°C, 8 mH) | 3.5 | mJ |

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

| Symbol | Parameter | Max. | Unit | |
|------------------|--|------|------|--|
| R _{θjc} | Thermal Resistance, Junction to Case | 1.13 | °C/W | |
| R _{θja} | Thermal Resistance, Junction to Ambient 76.42 °C/W | | | |

Note:

3. Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm, land pattern 12.70 mm x 9.45 mm, trace size = 10 mil.

Electrical Characteristics(4)

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|-----------------------|--------------------------------------|--|------|------|------|------|
| BV _{CBO} | Collector-Base Breakdown Voltage | I _C = 0.5 mA, I _E = 0 | 1500 | 1689 | | V |
| BV _{CEO} | Collector-Emitter Breakdown Voltage | I _C = 5 mA, I _B = 0 | 800 | 870 | | V |
| BV_{EBO} | Emitter-Base Breakdown Voltage | I _E = 0.5 mA, I _C = 0 | 12.0 | 14.8 | | V |
| I _{CES} | Collector Cut-off Current | V _{CE} = 1500 V, V _{BE} = 0 | | 0.01 | 100 | μA |
| I _{CEO} | Collector Cut-off Current | V _{CE} = 800 V, I _B = 0 | | 0.01 | 100 | μA |
| I _{EBO} | Emitter Cut-off Current | V _{EB} = 12 V, I _C = 0 | | 0.05 | 500 | μA |
| h | DC Current Gain | V _{CE} = 3 V, I _C = 0.4 A | 20 | 29 | 35 | |
| h _{FE} | DC Current Gain | V _{CE} = 10 V, I _C = 5 mA | 20 | 43 | | |
| | | I _C = 0.25 A, I _B = 0.05 A | | 0.16 | | |
| V _{CE} (sat) | Collector-Emitter Saturation Voltage | I _C = 0.5 A, I _B = 0.167 A | | 0.12 | | V |
| | | I _C = 1 A, I _B = 0.33 A | | 0.25 | | |
| V (act) | Page Emitter Saturation Voltage | I _C = 500 mA, I _B = 50 mA | | 0.74 | 1.20 | V |
| V _{BE} (sat) | Base-Emitter Saturation Voltage | I _C = 2 A, I _B = 0.4 A | | 0.85 | 1.20 | v |
| CIB | Input Capacitance | V _{EB} = 10 V, I _C = 0, f = 1 MHz | | 745 | 1000 | pF |
| C _{OB} | Output Capacitance | V _{CB} = 200 V, I _E = 0, f = 1 MHz | | 15 | | pF |
| f _T | Current Gain Bandwidth Product | I _C = 0.1 A, V _{CE} = 10 V | | 5 | | MHz |
| V _F | Diada Canvard Valtage | I _F = 0.4 A | | 0.76 | 1.20 | V |
| | Diode Forward Voltage | I _F = 1 A | | 0.83 | 1.50 | v |

Note:

4. Pulse test: pulse width = 20 μ s, duty cycle≤ 10%.

FJBE2150D — ESBCTM Rated NPN Silicon Transistor

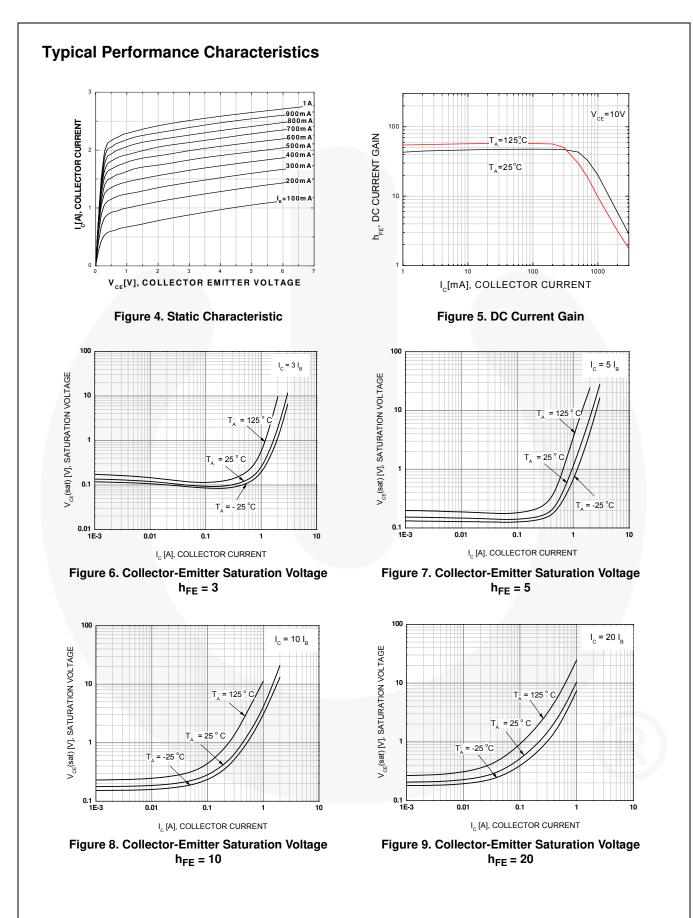
ESBC Configured Electrical Characteristics⁽⁵⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

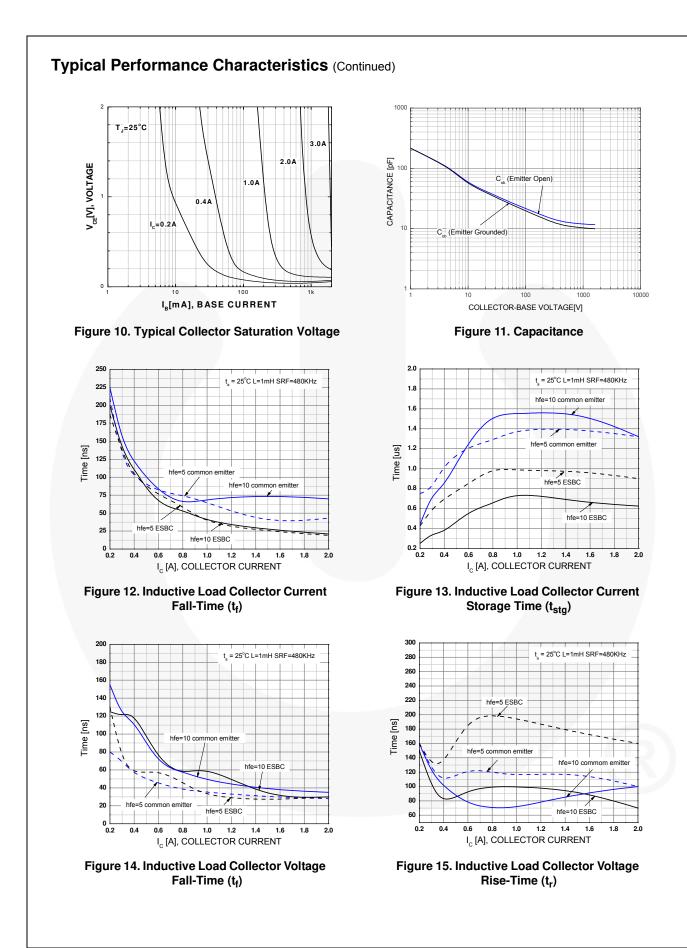
| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit | |
|----------------------|---|--|------|-------|------|------|--|
| f _T | Current Gain Bandwidth Product | I _C = 0.1 A,V _{CE} = 10 V | | 25 | | MHz | |
| lt _f | Inductive Current Fall Time | 10 V D 47 0 | | 137 | | ns | |
| t _s | Inductive Storage Time | V_{GS} = 10 V, R _G = 47 Ω, V _{Clamp} = 500 V, | | 350 | | ns | |
| Vt _f | Inductive Voltage Fall Time | ve Voltage Fall Time $t_p = 3.1 \mu s$, $I_C = 0.3 A$, | | 120 | | ns | |
| Vt _r | Inductive Voltage Rise Time | I _B = 0.03 A, L _C = 1 mH, SRF = 480 kHz | | 100 | | ns | |
| t _c | Inductive Crossover Time | | | 137 | | ns | |
| lt _f | Inductive Current Fall Time | | | 35 | | ns | |
| t _s | Inductive Storage Time | V _{GS} = 10 V, R _G = 47 Ω, V _{Clamp} = 500 V, | | 980 | | ns | |
| Vt _f | Inductive Voltage Fall Time | $t_p = 10 \ \mu s, \ I_C = 1 \ A,$ | | 30 | | ns | |
| Vt _r | Inductive Voltage Rise Time | $I_{\rm B} = 0.2 \text{ A}, L_{\rm C} = 1 \text{ mH},$ | | 195 | | ns | |
| t _c | Inductive Crossover Time | SRF = 480 kHz | | 210 | | ns | |
| V _{CSW} | Maximum Collector Source Volt- age at Turn-off without Snubber | h _{FE} = 5, I _C = 2 A | 1500 | | | V | |
| I _{GS(OS)} | Gate-Source Leakage Current | V _{GS} = ±20 V | | 1.0 | | nA | |
| | | V_{GS} = 10 V, I _C = 2 A, I _B = 0.67 A, h _{FE} = 3 | | 2.210 | | V | |
| N/ | | V_{GS} = 10 V, I _C = 1 A, I _B = 0.33 A, h _{FE} = 3 | | 0.321 | | | |
| V _{CS(ON)} | | V_{GS} = 10 V, I _C = 0.5 A, I _B = 0.17 A, h _{FE} = 3 | | 0.131 | | | |
| | | V_{GS} = 10 V, I _C = 0.3 A, I _B = 0.06 A, h _{FE} = 5 | | 0.166 | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{BS} = V _{GS} , I _B = 250 μA | | 1.9 | | V | |
| C _{iss} | Input Capacitance $(V_{GS} = V_{CB} = 0)$ | V _{CS} = 25 V, f = 1 MHz | | 470 | | pF | |
| Q _{GS(tot)} | Gate-Source Charge V _{CB} = 0 | V_{GS} = 10 V, I _C = 8 A, V _{CS} = 25 V | | 9 | | nC | |
| | | V _{GS} = 10 V, I _D = 6.3 A | | 21 | | | |
| r _{DS(ON)} | Static Drain-Source On Resistance | V _{GS} = 4.5 V, I _D = 5.5 A | | 26 | | mΩ | |
| 20(011) | UT RESISTATICE | V _{GS} = 10 V, I _D = 6.3 A, T _J = 125°C | | 30 | | | |

Note:

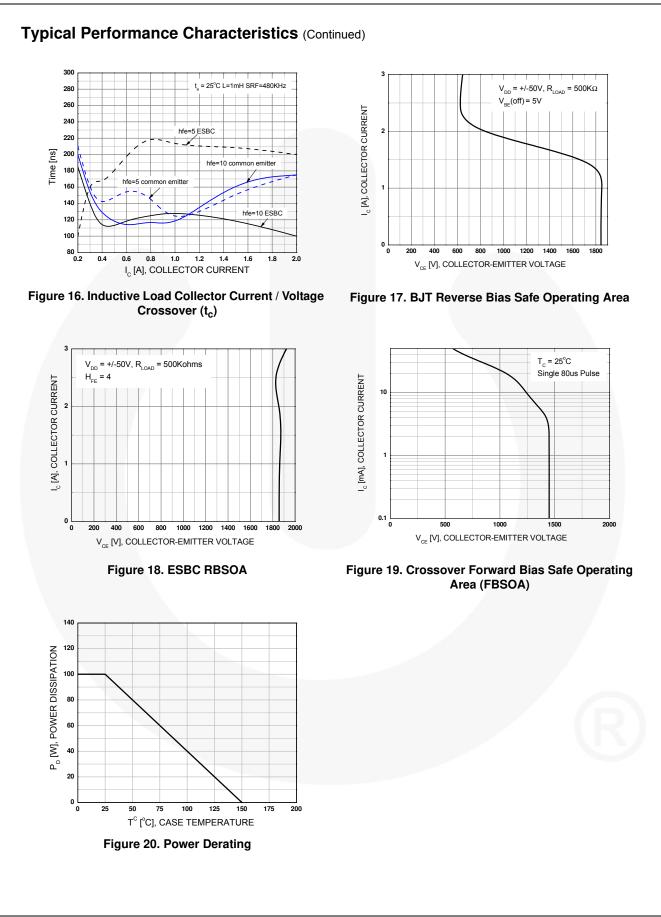
5. Used typical FDC655 MOSFET values in table. Values can vary if other Fairchild MOSFETs are used.

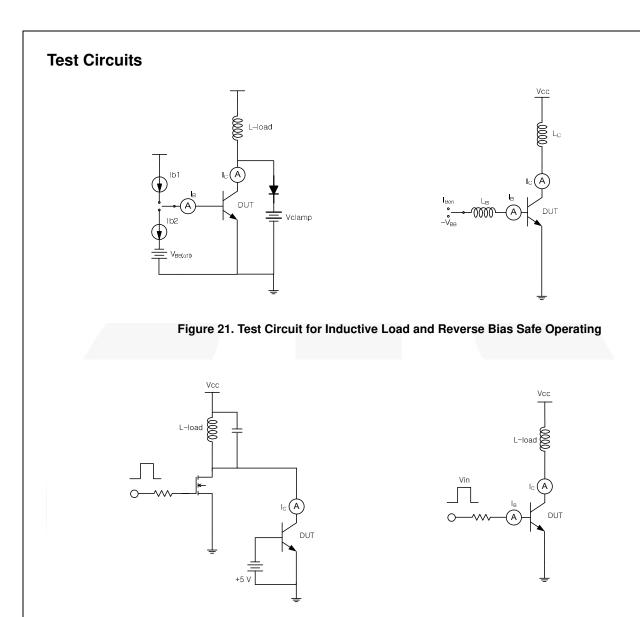


FJBE2150D — ESBC[™] Rated NPN Silicon Transistor

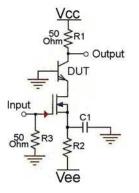


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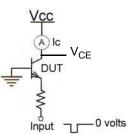


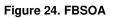












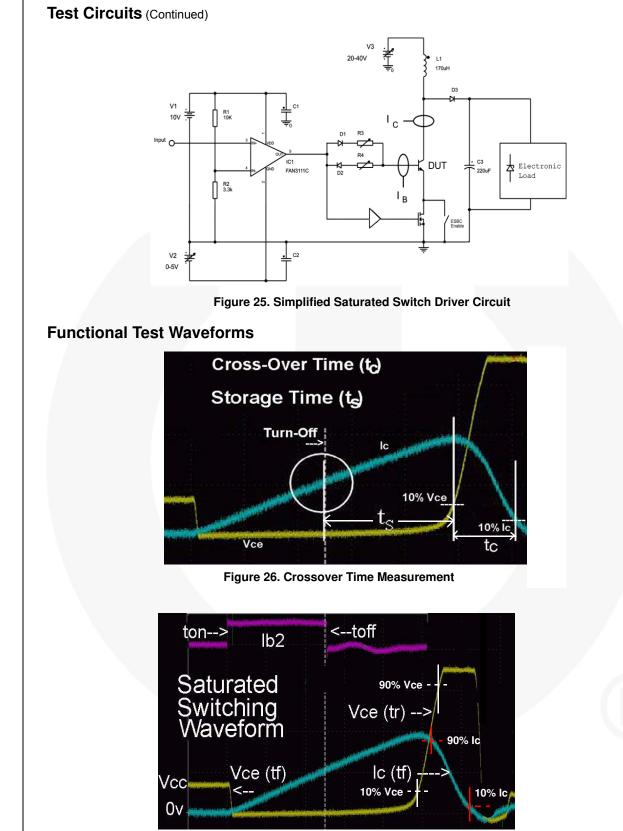
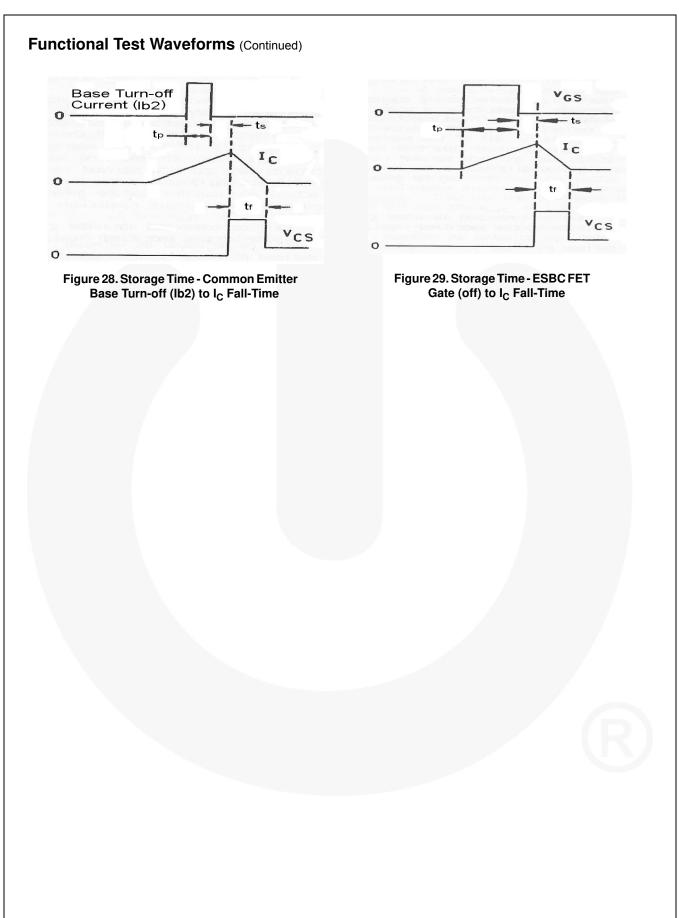
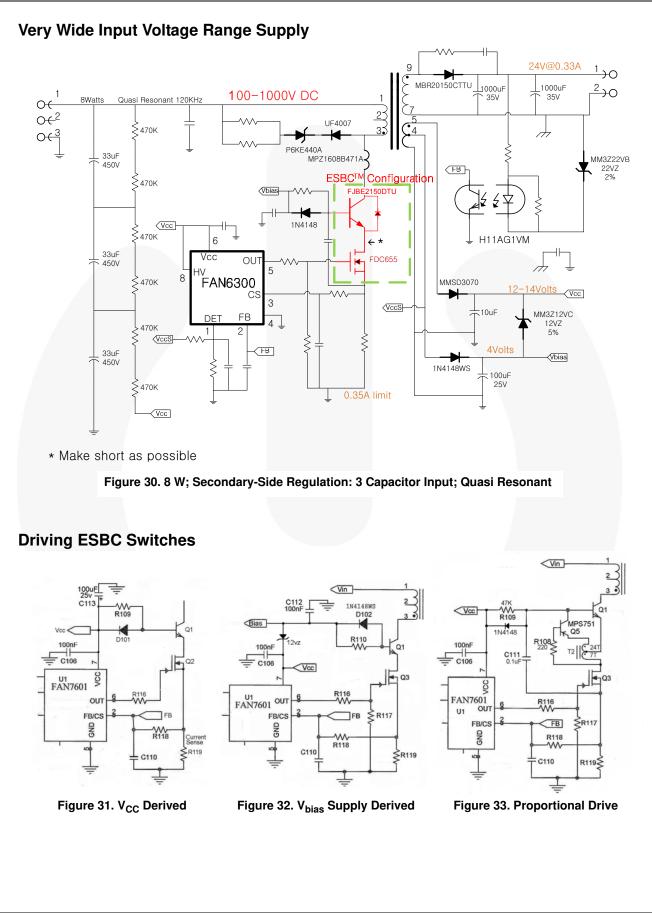
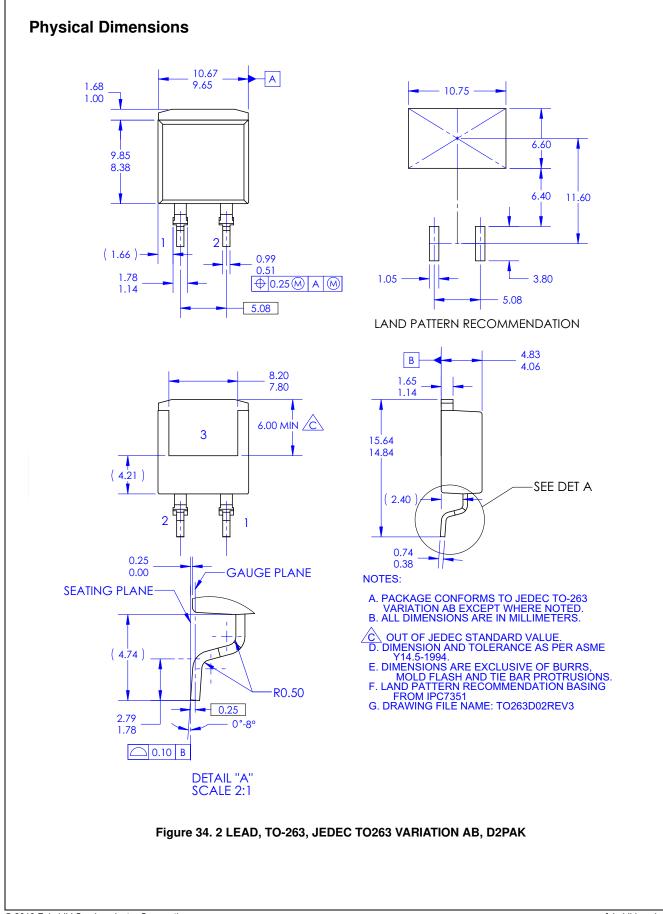


Figure 27. Saturated Switching Waveform











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