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TFT | CHARACTER | UWVD | FSC | SEGMENT | CUSTOM | REPLACEMENT

Graphic Display Module

Part Number

G126FLGFGS164T33XAR

Overview

128x64(80x54), FSTN, Gray background, RGB edge light, Bottom view, Super wide Temp (-30°C ~ +80°C), Transflective (positive), 3.3V LCD, 3.3V LED, Controller=ST7565P, RoHS Compliant



1.Features

- 1. 128X64 dots
- 2. Built-in controller (ST7565P)

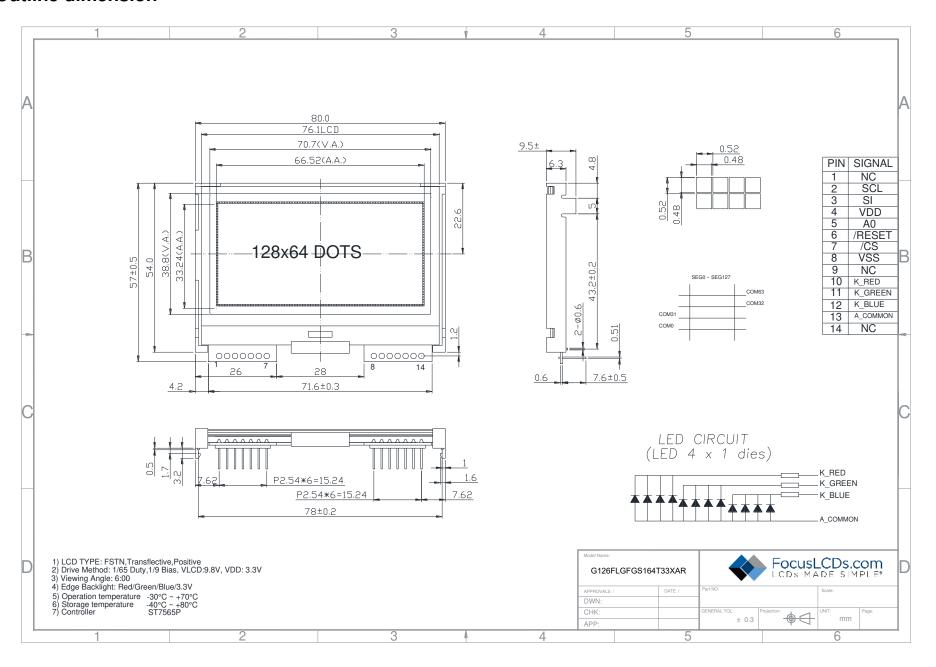
- +3.3V power supply
 1/64 duty cycle;1/9bias
 BKL to be driven by A, K.

LCD type	☑FSTN positiv	е		□FSTN Negative			
	□STN Yellow (Green	□STN	Gray	□STN-Blue		
View direction	☑6 O'clock		□12 C)'clock			
Rear Polarizer	□Reflective		⊠Tran	sflective	□Transmissive		
Backlight Type	☑ LED Edge			□Internal Power	☑3.3V input		
	□LED Array	□CCFL	_	☑External Power	□4.2V input		
Backlight Color	⊠RGB	□Ambe	er □Blue-Green		□Yellow-Green		
Temperature Range	□Normal		☑Wide	9	☐Super Wide		
DC to DC circuit	☑Build-in			□Not Build-in			
El Driver IC	□Build-in			☑Not Build-in			
Touch screen	□With			□Without			
Font type	☑English-Jap	□Engli	sh-Eur	□English-Russian	□other		
	anese	opean					

2. MECHANICAL SPECIFICATIONS

Module size	80.0mm(L)*54.0mm(W)* Max9.5(H)mm
Viewing area	70.7mm(L)*38.8mm(W)
Dots size	0.48mm(L)*0.48mm(W)
Dots pitch	0.52mm(L)*0.52mm(W)
Weight	Approx.

3. Outline dimension



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4. Absolute maximum ratings

Item	Symbol	Standard			Unit
Power voltage	V _{DD} -V _{SS}	0	-	3.5	V
Input voltage	V_{IN}	VSS	-	VDD	V
Operating temperature range	V_{OP}	-30	-	+70	°C
Storage temperature range	V _{ST}	-40	-	+80	C

5.Interface pin description

Pin no.	Symbol	External connection	Function
1	NC		NC
2	SCL	MPU	Serial clock input
3	SI	MPU	Serial data input
4	V_{DD}	Power supply	Signal ground for logic (+3.3V)
5	A0	MPU	Register select signal
6	/RESET	MPU	Controller reset (module reset)
7	/CS	MPU	Used to enter chip select signal
8	Vss	Power supply	Power supply for LCM (GND)
9	NC		
10	K-RED		LED red K
11	K-GREEN	Power supply for	LED green K
12	K-BLUE	BKL	LED blue K
13	A_COMMON		Common cathode
14	NC		NC

6. Optical characteristics

STN type display module (Ta=25°C, VDD=3.3V)

	<u>'</u>					
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing angle	θ	Cr≥2	-60	-	35	dog
	Ф	Ur≶2	-40	-	40	deg
Contrast ratio	Cr		-	6	-	-
Response time (rise)	Tr	-	-	150	250	mo
Response time (fall)	Tr	-	-	150	250	ms



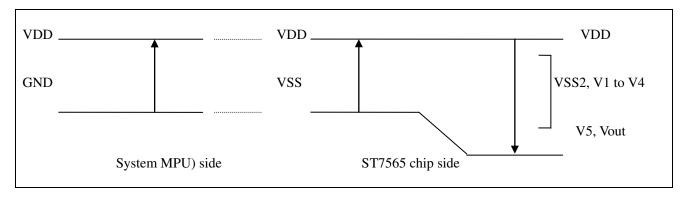
7. Electrical characteristics

DC characteristics

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage for LCD	V_{DD} - V_0	Ta =25°C	-	9.8	-	V
Input voltage	V_{DD}		2.4	ı	3.3	
Supply current	I _{DD}	Ta=25℃, V _{DD} =3.3V	-	1	147	uA
Input leakage current	I _{LKG}		-	1	1.0	uA
"H" level input voltage	VIH		2.2	1	V _{DD}	
"L" level input voltage	VIL	Twice initial value or less	0	ı	0.6	
"H" level output voltage	Vон	LOH=-0.5mA	2.4	ı	-	
"L" level output voltage	Vol	LOH=0.5mA	-	ı	0.4	V
	Red V _F	Ta =25°C If=40mA	1.8	-	2.4	
Forward voltage	Green V _F	Ta =25℃ If=60mA	2.8	1	3.4	
	Blue V _F	Ta =25℃ If=60mA	2.8	-	3.4	

8. Absolute Maximum Ratings (Unless otherwise noted, VSS=0V)

Paran	neter	Symbol	Conditions	Unit
Power Supply Voltage		VDD	-0.3 to $+3.6$	V
Power supply voltage (3) (V	/DD standard)	V5, Vout	-13.0 to +0.3	V
Power supply voltage (4) (V	/DD standard)	V1,V2,V3,V4	V5 to +0.3	V
Input Voltage		Vin	-0.3 to VDD+0.3	V
Output voltage		Vo	-0.3 to VDD+0.3	V
Operating Temp.		Topr	-40 to 80	С
Storage Temp.	Bare chip	Tstr	-40 to +80	C





9. Timing Characteristics The Serial Interface

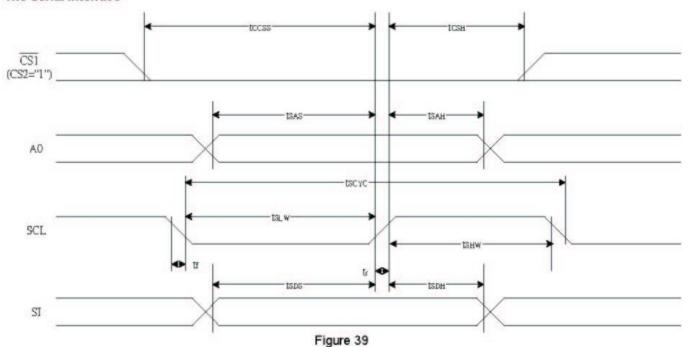


Table 30

(VDD = 3.3V, Ta = -30 to 85°C)

Cianal	Cumbal	Condition	Rat	ing	Units
Signal	Symbol	Condition	Min.	Max.	Units
	Tscyc		50	-	
SCL	Tshw		25	_	1
	TSLW		25	_	
- 40	TSAS		20	-	
A0	Tsah		10	_	ns
C	Tsds		20	-	
SI	TSDH		10	_	1
	Tcss		20	_	
CS	Tosh		40	_	1
	Signal SCL A0 SI CS	Tscyc SCL Tshw TSLW A0 TSAS Tsah SI Tsds TSDH Tcss	Tscyc Tshw TSLW TSAS Tsah Tsds TSDH TCSS Tcs	Signal Symbol Condition Min.	Tscyc 50

Table 31

(VDD = 2.7V, Ta = -30 to 85°C)

Cianal	Cumbal	Condition	Rat	Units	
Signal	Symbol	Condition	Min.	Max.	Units
	Tscyc		100	_	
SCL	Tshw		50	_	
	TSLW		50	_	
40	TSAS		30	-	
	TSAH		20	_	ns
- CI	TSDS		30	_	
si	TSDH		20	_	1
	Toss		30	_	
CS	TCSH		60	_	
	Signal SCL A0 SI CS	Tscyc SCL TSHW TSLW TSAS TSAH TSDS TSDH TCSS TCSS	Tscyc SCL TSHW TSLW A0 TSAS TSAH TSDS TSDH TCSS TCSS	Tecyc 100	Tscyc 100 — SCL TSHW 50 — TSLW 50 — TSLW 50 — A0 TSAS 30 — TSAH 20 — SI TSDS 30 — TSDH 20 — CS TCSS 30 —



10. Table of LCM commands

Command					of the part	nd (Function
1.0000000000000000000000000000000000000	A0	/RD	/WR			D5		D3	D2	D1		100000000000000000000000000000000000000
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Di	ispla	y st	art a	ddre	ess	Sets the display RAM display sta line address
(3) Page address set	0	1	0	1	0	1	1	Pa	ige a	addr	ess	Sets the display RAM page address
(4) Column address set upper bit Column address set lower bit	0	1	0	0	0	0	0	colu	umn ast s	add ignif	cant lress icant lress	Sets the most significant 4 bits of the display RAM column address Sets the least significant 4 bits of the display RAM column address
(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0			1	Writ	e da	ta			Writes to the display RAM
(7) Display data read	1	0	1			-	Rea	d da	ta			Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565F
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	B	oera ode	ting	Select internal power supply operating mode
(17) Vo voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Re	sist atio	or	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1 0	0	0 Ele	0 ctro	0 nic v	0 olur		1 alue	Set the Vo output voltage electronic volume register
(19) Static indicator ON/OFF Static indicator	0	1	0	1 0	0	1	0	1	1		0 1 Mode	0: OFF, 1: ON Set the flashing mode
register set (20) Booster ratio set	0	1	0	1 0	1 0	1	1 0	1	0	0 ste	0 p-up ilue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

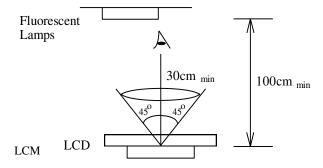


11.QUALITY SPECIFICATIONS

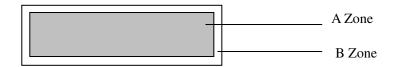
11.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).

B Zone: Non-active display area (outside viewing area).



11.2 Specification of quality assurance AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: * is not including)

Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
Polarizer		Bubble and foreign material	3	
	Soldering	9		
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	



Note on defect classification

No.	Item			Criterior	ו					
1	Short or open circuit		Not allow							
	LC leakage									
	Flickering									
	No display									
	Wrong viewing direction									
	Wrong Back-light									
2	Contrast defect		Re	efer to approva	al sample					
	Background color deviation									
3	Point defect, Black spot, dust	Ç		Point Size	Acceptable Qty.					
	(including Polarizer)	X		φ<0.10 0.10<φ≤0.20	Disregard 3					
				0.20<∮≤0.25	2					
	$\phi = (X+Y)/2$			0.25<∮≤0.30	1					
			L	φ>0.30	nit: mm					
4	Line defect,	<u> </u>								
	Scratch	 		Line	Acceptable Qty.					
	Goraton	←> L	L	W 0.015≥W	Disregard					
		L	3.0≥1	L 0.03≥W	2					
			2.0>1							
			1.0>1	0.1>W 0.05 <w< td=""><td>1 Applied as point defect</td><td></td></w<>	1 Applied as point defect					
					Unit: mm					
5	Rainbow	Not more than tw	o color	changes acro	ess the viewing area.					



No	ltem	Criterion		
6	Chip Remark: X: Length direction Y: Short direction	Acceptable criterion $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		
	Z: Thickness direction t: Glass thickness W: Terminal Width	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		Acceptable criterion $\begin{array}{c cccc} X & Y & Z \\ \hline \leqslant 3 & \leqslant 2 & \leqslant t \\ \hline \text{shall not reach to ITO} \end{array}$		
		Acceptable criterion $\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		



No.	ltem	Criterion			
7	Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$	(1) Pin hole φ < 0.10mm is acceptable.			
		Point Size Acceptable Qty			
8	Back-light	(1) The color of backlight should correspond its specification.			
9	Soldering	(2) Not allow flickering (1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Land Land 50% lead			
10	Wire	 (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable. 			
11*	PCB	(1) Not allow exposed copper wire inside the flat cable.(2) Not allow missing or wrong putting of component.			



No	Item	Criterion		
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$		
13	TAB	1. Position W W W W H H TAB ITO W IS 1/3W H H H IS 1/3H TAB 2 TAB bonding strength test TAB P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)		
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.		



11.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	- No abnormalities in functions and appearance
High temp. Operating	70°C	48	
Low temp. Storage	-30°C	48	
Low temp. Operating	-20°C	48	
Humidity	40°C/ 90%RH	48	
Temp. Cycle	0° C ← 25° C → 50° C (30 min ← 5 min → 30min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not make any modification on the PCB without consulting Focus LCDs
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and



- the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C±10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%.and avoid direct sunlight.