

CERAMIC LVDS MINIATURE SMD OSCILLATOR (3.2 x 2.5 x 1.2mm)

FEATURES:

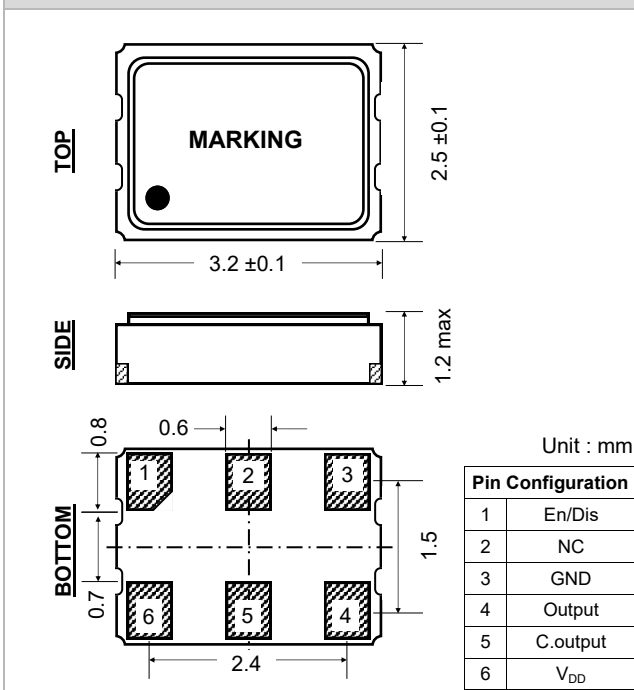
- LVDS MINIATURE SMD
- ULTRA LOW PHASE JITTER PERFORMANCE
- EN/DISABLE PIN AVAILABLE



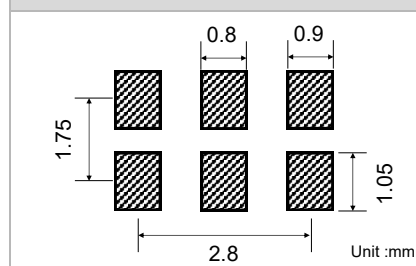
■ SPECIFICATION

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| FREQUENCY RANGE | 13.500MHz ~ 200.000MHz | |
| FREQUENCY STABILITY | ±25ppm ~ ±100ppm (See P/N guide for other options) | |
| OPERATING TEMPERATURE RANGE | -40°C ~ 85°C (See P/N guide for other options) | |
| STORAGE TEMPERATURE RANGE | -55°C ~ 125°C | |
| SUPPLY VOLTAGE ± 5% | 1.8V _{DC} ~ 3.3V _{DC} | |
| SUPPLY CURRENT | 16mA typ. 45mA max. | |
| OUTPUT | LOGIC FAMILY | LVDS |
| | LOAD | 100Ω (Output - Complementary Output) |
| | LEVEL LOGIC '1' (V_{OH}) | 1.4V _{DC} typ. 1.6V _{DC} max. |
| | LEVEL LOGIC '0' (V_{OL}) | 1.1V _{DC} typ. 0.9V _{DC} min. |
| | DIFFERENTIAL OUTPUT VOLTAGE | 0.25V ~ 0.450V, 0.350V typ. |
| | OFFSET VOLTAGE | 1.125V ~ 1.375V, 1.25V typ. |
| | SYMMETRY(DUTY CYCLE) | STD: 40% / 60%, Option: 45% / 55% |
| | RISE AND FALL TIME (Tr/Tf) | 0.2nS typ. 0.8nS max. |
| START-UP TIME | 5mS typ. 10mS max. | |
| STAND-BY VOLTAGE | ENABLE (V_{IH}) | 0.7 x V _{DD} ≤ V _{IH} ≤ V _{DD} or N/C |
| | DISABLE (V_{IL}) | V _{IL} ≤ 0.3 x V _{DD} |
| STAND-BY CURRENT (Pin#1=V_{IL}) | 10μA max. | |
| ENABLE DELAY TIME | 2mS max. | |
| DISABLE DELAY TIME | 200nS max. | |
| AGING @ +25°C ±3°C | ±3.0ppm / 1 st year max. | |
| PHASE JITTER RMS (12kHz to 20MHz) | 1.0pS max. | |

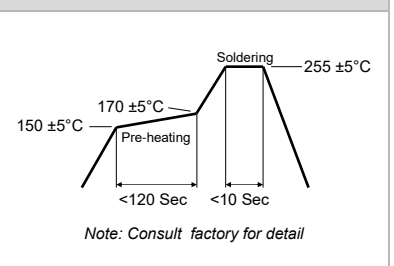
■ PACKAGE DIMENSIONS



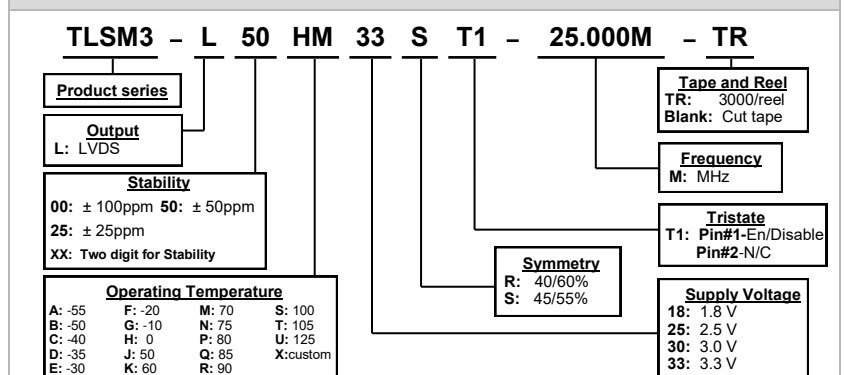
■ SOLDER PATTERN



■ REFLOW PROFILE



■ PART NUMBERING GUIDE



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