

ADJUSTABLE PRECISION SHUNT REGULATOR

■FEATURES

- Adjustable Output Voltage by external resistance
- Wide Safety Operating Boundary Area
- Bipolar Technology

Package SOT-23-5 SOT-89-3

■APPLICATION

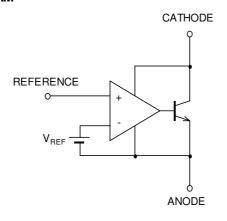
- Industrial Equipment
- Home Electrical Appliance
- Replacement from Zener Diode
- Other

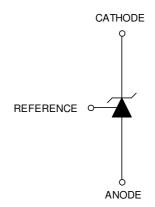
■GENERAL DESCRIPTION

The NJM17431 is adjustable precision shunt regulator. The output voltage may be set to any value between V_{REF} (about 2.5V) and 36V by two external resistors.

The NJM17431 is improved the reference voltage accuracy (\pm 0.8%) and safety operating boundary area connected large capacitance. Therefore, the NJM17431 is suitable for various applications.

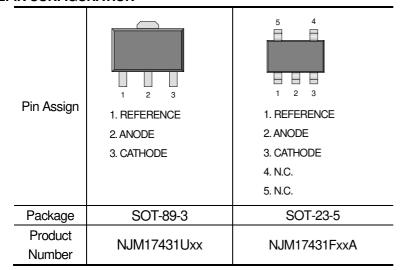
■BLOCK DIAGRAM



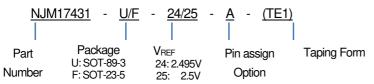




■PIN CONFIGURATION



■PRODUCT NAME INFORMATION



■ORDERING INFORMATION

PRODUCT NAME	PACKAGE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ (pcs)
NJM17431U24	SOT-89-3	Yes	Yes	Sn2Bi	181	61	1,000
NJM17431U25	SOT-89-3	Yes	Yes	Sn2Bi	171	61	1,000
NJM17431F24A	SOT-23-5	Yes	Yes	Sn2Bi	AK5x ("x" is Lot)	15	3,000
NJM17431F25A	SOT-23-5	Yes	Yes	Sn2Bi	AK4x ("x" is Lot)	15	3,000



■ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL		MAXIMUM RATINGS	UNIT	REMARK
Cathode Voltage		V _{KA}	+37 (1)	V	ANODE-CATHODE Pin
Continuous Cathode Voltage		lκ	- 100 to +150	mA	ANODE-CATHODE Pin
Reference Input Current		I _{REF}		mA	-
	P _D	SOT-23-5	480 (2)	mW	
Power Dissipation			650 (3)		
rowei bissipation		SOT-89-3	450 (4)		_
			1300 (5)		
Junction Temperature	Tj _{max}		+150	°C	_
Operating Temperature Range	T _{opr}		- 40 to +125	ç	_
Storage Temperature Range	ure Range T _{stg}		- 50 to +150	°C	_

- (1) Unless specified, all voltage value are with respect to the anode pin.
- (2) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 2Layers)
- (3) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 4Layers),

internal Cu area: 74.2×74.2mm

- (4) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard size, 2Layers)
- (5) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 4Layers)

 (For 4Layers: Applying 74.2×74.2mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

■RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT	REMARK
Cathode Voltage	VKA	V _{REF} to 36	V	ANODE-CATHODE Pin
Cathode Current	lκ	0.5 to 100	mA	ANODE-CATHODE Pin



■ELECTRICAL CHARACTERISTICS

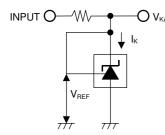
(Unless other noted, IK=10mA, Ta=25°C)

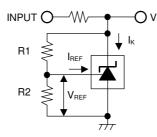
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Potoronoo Voltago	V	\/\/ (C)	2.495V ver.	2.475	2.495	2.515	V
Reference Voltage	V_{REF}	VKA= VREF (6)	2.5V ver.	2.480	2.500	2.520	V
Reference Input Voltage Change Over Temperature	ΔVREF	VKA=VREF (6)		_	8	17	mV
Range	(dev)	$T_a = -40^{\circ}C \text{ to } +85^{\circ}$	o'C				
Reference voltage	ΔV_{REF}	V _{KA} =V _{REF} (6)		_	±30	_	ppm/°C
temperature coefficient	(ppm)	$T_a = -40^{\circ}C \text{ to } +85^{\circ}$	S°C				ррпі, О
Reference Voltage Change	ΔV_{REF}	$\Delta V_{KA}=10V-V_{REF}$	(7)	_	-2.0	-3.7	mV/V
vs. Cathode Voltage Change	ΔV_{KA}	ΔV _{KA} =36V-10V			-1	-2.2	,
Reference Input Current	I _{REF}	R1=10kΩ, R2=∞	(7)	-	1	2.8	μΑ
Reference Input Current Change Over Temperature Range	ΔI _{REF} (dev)	R1=10kΩ, R2= ∞ T _a = - 40°C to +85	` '	-	0.25	0.5	μΑ
Minimum Cathode Current	I _{MIN}	VKA=VREF (6)		-	0.25	0.5	mA
OFF State Cathode Current	loff	VKA=36V, VREF=0	V (8)	-	0.1	1.0	μΑ
Dynamic Impedance	IZKAI	V _{KA} =V _{REF} , I _K =1mA f≤1kHz (6)	to 100mA,	-	0.2	0.5	Ω

The maximum value of "Dynamic Impedance", "Reference Voltage Change" and "Reference Input Current Change" are determined based on sampling evaluation from the initial production lots, and thus not tested in the production test. Therefore, these values are for the reference design purpose only.

- (6): TestCircuitFig.1
- (7): Test CircuitFig.2
- (8): Test Circuit Fig.3

■ TEST CIRCUIT





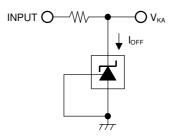


Fig.1. Test Circuit for V_{KA}=V_{REF}

Fig.2. Test Circuit for $V_{\text{KA}} > V_{\text{REF}}$

Fig.3. Test Circuit for IOFF

$$V_{\text{O}} \!\!=\!\! V_{\text{KA}} \!\!=\!\! V_{\text{REF}}$$

$$V_{\text{O}} = V_{\text{KA}} = V_{\text{REF}} \! \left(1 + \frac{R1}{R2} \right) \! + I_{\text{REF}} \! \times \! R1$$



■THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	VALUE		UNIT
		COT 22 F	260 (2)	°C/W
Junction-to-ambient	θја	SOT-23-5	195 (3)	
thermal resistance		SOT-89-3	200 (4)	
			130 (5)	
	ψjt	SOT-23-5	60 (2)	°C/W
Junction-to-Top of package characterization parameter			70 (3)	
			67 (4)	
		SOT-89-3	65 (5)	

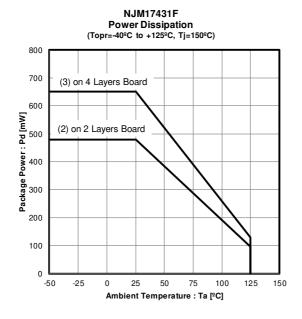
- (2) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 2Layers)
- (3) Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 4Layers),

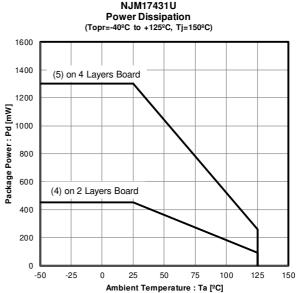
internal Cu area: 74.2×74.2mm

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 (For 4Layers: Applying 74.2×74.2mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

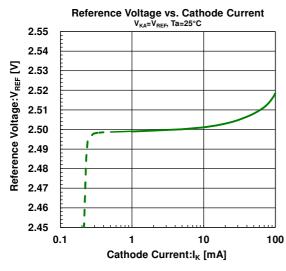
■POWER DISSIPATION vs. AMBIENT TEMPERATURE

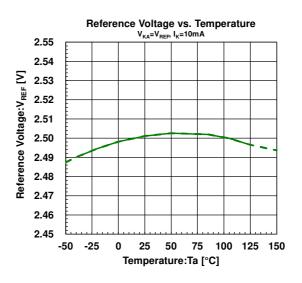


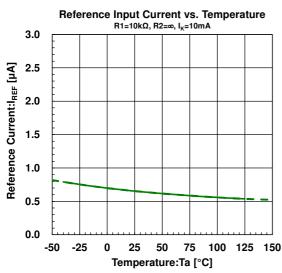


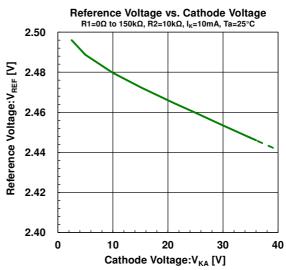


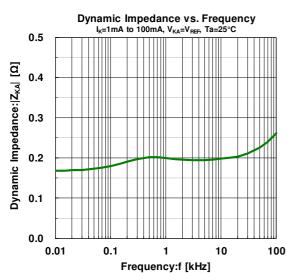
■TYPICAL CHARACTERISTICS





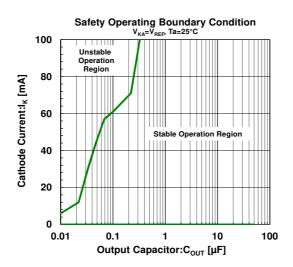




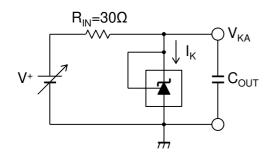




■TYPICAL CHARACTERISTICS



Safety Operating Boundary Condition Test Circuit



Note) Oscillation might occur while operating within the range of safety curve.

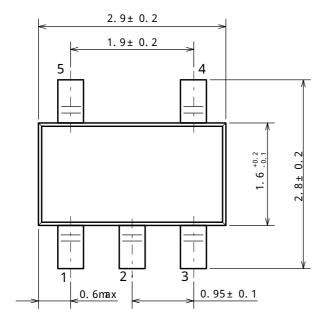
So that, it is necessary to make ample margins by taking considerations of fluctuation of the device.

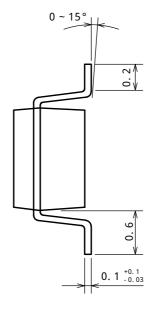


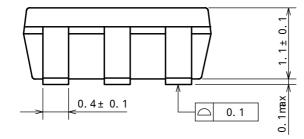
SOT-23-5

Unit: mm

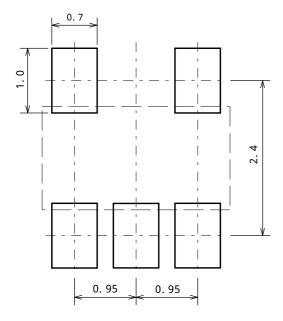
■ PACKAGE DIMENSIONS







■ EXAMPLE OF SOLDER PADS DIMENSIONS

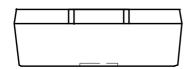


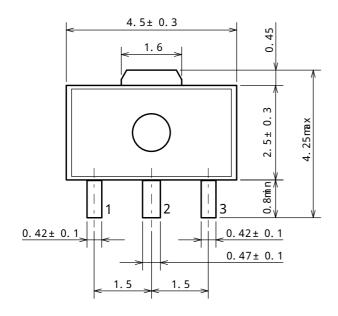


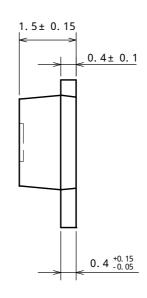
SOT-89-3

Unit: mm

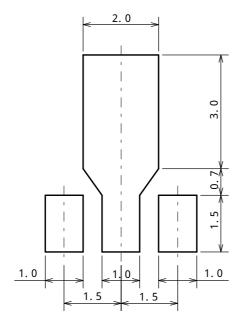
■ PACKAGE DIMENSIONS







■ EXAMPLE OF SOLDER PADS DIMENSIONS



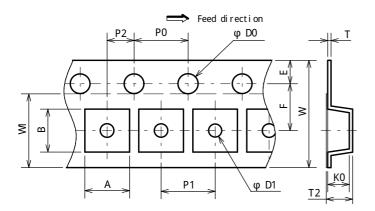


SOT-23-5

■ PACKING SPEC

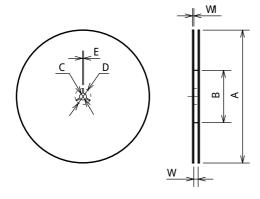
Unit: mm

TAPING DIMENSIONS



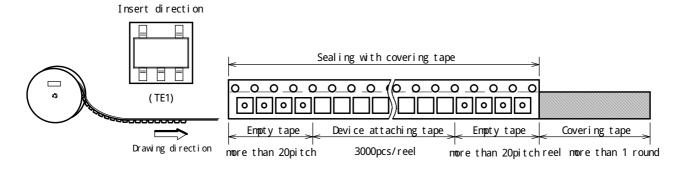
SYMBOL	DI MENSI ON	REMARKS
Α	3. 3± 0. 1	BOTTOM DI MENSI ON
В	3. 2± 0. 1	BOTTOM DI MENSI ON
D0	1. 55	
D1	1. 05	
Е	1. 75± 0. 1	
F	3.5± 0.05	
P0	4. 0± 0. 1	
P1	4. 0± 0. 1	
P2	2. 0± 0. 05	
T	0. 25± 0. 05	
T2	1. 82	
K0	1.5± 0.1	
W	8. 0± 0. 3	
W1	5. 5	THI CKNESS 0. 1MAX

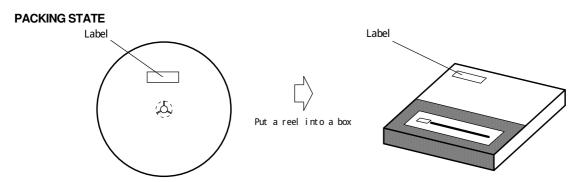
REEL DIMENSIONS



SYMBOL	DI MENSI ON
Α	φ 180± 1
В	φ 60± 1
C	φ 13± 0.2
D	φ 21± 0.8
E	2± 0.5
W	9± 0.5
V1	1. 2± 0. 2

TAPING STATE





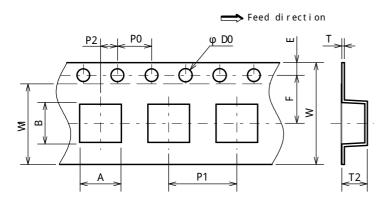


SOT-89-3

■PACKING SPEC

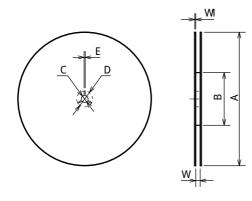
Unit: mm

TAPING DIMENSIONS



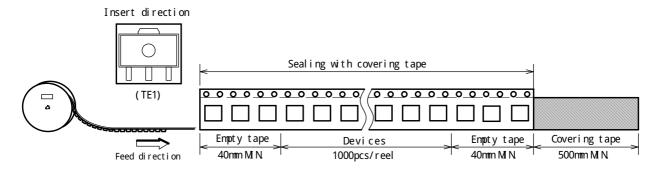
SYMBOL	DI MENSI ON	REMARKS
Α	4. 9± 0. 1	BOTTOM DI MENSI ON
В	4. 5± 0. 1	BOTTOM DI MENSI ON
D0	1. 5 ^{+0. 1}	
E	1. 5± 0. 1	
F	5. 65± 0. 1	
P0	4. 0± 0. 1	
P1	8. 0± 0. 1	
P2	2. 0± 0. 05	
T	0. 3± 0. 05	
T2	2. 0	
W	12. 0± 0. 3	
V1	9. 5	THI CKNESS 0. 1MAX

REEL DIMENSIONS

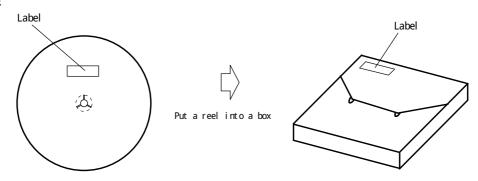


SYMBOL	DI MENSI ON	
Α	φ 180± 1	
В	φ 60± 1	
C	φ 13± 0.2	
D	φ 21± 0.8	
Е	2± 0.5	
W	13± 0.5	
\/1	1 2+ 0 2	

TAPING STATE



PACKING STATE

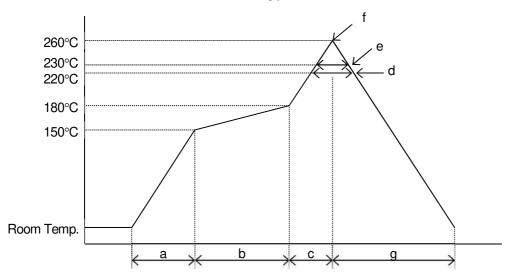




■RECOMMENDED MOUNTING METHOD

INFRARED REFLOW SOLDERING METHOD

* Recommended reflow soldering procedure



a :Temperature ramping rate

: 1 to 4°C /s

b Pre-heating temperature

: 150 to 180°C

time

: 60 to 120s : 1 to 4°C /s

c :Temperature ramp rate d 220°C or higher time

: Shorter than 60s

e 230°C or higher time f Peak temperature

: Shorter than 40s : Lower than 260°C

g :Temperature ramping rate

: 1 to 6°C /s

Defaute ampling fale . 100 C/S

The temperature indicates at the surface of mold package.



■REVISION HYSTORY

Date	Revision	Changes
21.May.2020	Ver.1.0	New Release
16.Sep.2020	Ver.1.1	Added NJM17431F24A
16.Sep.2021	Ver.1.2	Added NJM17431Uxx



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