

Vishay Siliconix

Low Voltage, 1 Ω Single SPDT Analog Switch (1:2 Multiplexer) with Power Down Protection

DESCRIPTION

The DG4157 is a high performance single pole double throw analog switch designed for 1.65 V to 5.5 V operation with single power rail.

Fabricated with high density CMOS technology, the device achieves low on resistance as 1 Ω at 4.5 V power supply and fast switching speed. The - 3 dB bandwidth is typically 117 MHz.

The DG4157 features break before make switch performance, and guarantees logic high control input threshold as low as 1.4 V over the range up to 5.5 V.

It can handle both analog and digital signals and permits signals with amplitudes of up to V_{CC} to be transmitted in either direction.

Power down protection circuit is built in to prevent abnormal current path through signal pins during power down condition.

Each output pin (A, B_0 , or B_1) can withstand greater than 8 kV (human body model).

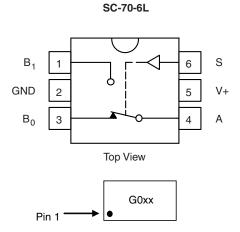
It is available in both SC-70-6 and miniQFN6 packages.

The features make it an ideal part for the switching of audio, video, and data stream.

FEATURES

- Direct cross of industry standard xxx4157
- 1.65 V to 5.5 V operation voltage range
- Guaranteed 1.4 V logic high input threshold at V_{CC} = 5.5 V
- 117 MHz, 3 dB bandwidth
- Low on-resistance
- Power down protection
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

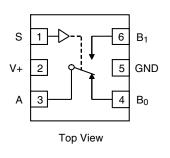
FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



Device Marking: G0xx xx = Date/Lot Traceability Code

TRUTH TABLE					
LOGIC INPUT (S)	FUNCTION				
0	B ₀ Connected to A				
1	B ₁ Connected to A				

miniQFN-6L



Device Marking: Fx x = Date/Lot Traceability Code

ORDERING INFORMATION					
TEMP. RANGE PACKAGE PART NUMBER					
-40 °C to +85 °C	SC-70-6L	DG4157DL-T1-E3			
	miniQFN-6L	DG4157DN-T1-E4			

S15-1745-Rev. G, 27-Jul-15

1 For technical questions, contact: analogswitchtechsupport@vishay.com Document Number: 68800





DG4157

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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	LIMIT	UNIT					
Reference V+ to GND	-0.3 to +6	v					
S, A, B ^a	-0.3 to (V+ + 0.3)	v					
Continuous Current (Any terminal)		± 200					
Peak Current (Pulsed at 1 ms, 10 % duty cycle)	± 400	– mA					
Storage Temperature D Suffix		-65 to +150	°C				
Devuer Dissignation (Deckages) b	SC-70-6L ^c	250					
Power Dissipation (Packages) ^b	miniQFN-6L ^d	160	– mW				

Notes

a. Signals on A, or B or S exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 3.1 mW/°C above 70 °C.

d. Derate 2 mW/°C above 70 °C.

SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS UNLESS OTHERWISE SPECIFIED	TEMP. ^a	LIMITS -40 °C to +85 °C			UNIT
		V + = 3 V, V _{IN} = 0 V or V+ e		MIN. ^b	TYP.°	MAX. ^b	
DC Characteristics							
		$V_{+} = 2.7 V, B_{0} \text{ or } B_{1} = 1.5 V, I_{O} = 100 \text{ mA}$	Room	-	1.7	2.5	
On Resistance	R _{ON}		Full	-	-	3	
On Resistance	non	$V_{+} = 4.5 V$, B_0 or $B_1 = 3.5 V$, $I_0 = 100 mA$	Room	-	0.95	1.2	
		$V + = 4.5 V, B_0 \text{ or } B_1 = 3.5 V, I_0 = 100 \text{ mA}$	Full	-	-	1.4	
On Resistance Flatness		V+ = 2.7 V, B_0 or B_1 = 0.75 V, 1.5 V, I _O = 100 mA	Room	-	0.2	-	Ω
	R _{FLATNESS}	V+ = 4.5 V, B ₀ or B ₁ = 1 V, 3.5 V,	Room	-	0.14	0.3	
		$I_{O} = 100 \text{ mA}$	Full	-	-	0.4	
On Resistance Match	ΔR _{ON}	V+ = 2.7 V, B_0 or B_1 = 1.5 V, I_0 = 100 mA	Room	-	0.04	-	
		$V_{+} = 4.5 V, B_{0} \text{ or } B_{1} = 3.5 V,$	Room	-	0.05	0.12	
		I _O = 100 mA	Full	-	-	0.15	
Quitab Off Lashana Quinant				-2	-	2	
Switch Off Leakage Current	I _{OFF}	V+ = 5.5 V, A = 1 V, 4.5 V	Full	-20	-	20	nA
		B_0 or $B_1 = 4.5$ V, 1 V or floating	Room	-4	-	4	
Switch On Leakage Current	I _{ON}		Full	-40	-	40	
Digital Control	•		•				
Input, High Voltage	V _{INH}	V+ = 2.7 V to 5.5 V	Full	1.4	-	-	v
Input, Low Voltage	V _{INL}	V + = 2.7 V 10 5.5 V	Full	-	-	0.4	v
Input Current	I _{INH} , I _{INL}	$V_{IN} = 0 \text{ or } V+$	Full	-1	-	1	μA
Power Supply	·				•		
Power Supply Range	V+		Full	1.65	-	5.5	V
Quiescent Supply Current	1+	V+ = 5.5 V, V _{IN} = 0 V, 5.5 V	Room	-	0.05	0.5	
	1+	$v_{+} = 5.5 v, v_{ N} = 0 v, 5.5 v$	Full	-	-	1	μA

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SPECIFICATIONS								
	SYMBOL			LIMITS -40 °C to +85 °C			UNIT	
PARAMETER	STMBOL	UNLESS OTHERWISE SPECIFIED V+ = 3 V, V _{IN} = 0 V or V+ ^e	TEMP. ^a	MIN. ^b	TYP. °	MAX. b	UNIT	
AC Characteristics			1					
		$V_{+} = 2.7 V, B_0 \text{ or } B_1 = 1.5 V, R_L = 50 \Omega,$	Room	-	40	55		
Turn-On Time ^d	+	C _L = 35 pF	Full	-	-	60		
	t _{ON}	$V_{+} = 4.5 V$, B_0 or $B_1 = 1.5 V$, $R_L = 50 \Omega$,	Room	-	22	37		
		C _L = 35 pF	Full	-	-	40		
		$V_{+} = 2.7 V, B_{0} \text{ or } B_{1} = 1.5 V, R_{L} = 50 \Omega,$	Room	-	12	27		
Turn-Off Time ^d	+	C _L = 35 pF	Full	-	-	30	ns	
Tum-On Time ³	t _{OFF}	$V_{+} = 4.5 V, B_{0} \text{ or } B_{1} = 1.5 V, R_{L} = 50 \Omega,$	Room	-	8	23		
		C _L = 35 pF	Full	-	-	25		
	t _{ввм}	V+ = 2.7 V, B ₀ = B ₁ = 1.5 V, R _L = 50 Ω, C _L = 35 pF	Deem	1	26	-		
Break-Before-Make Time ^d		$V{+} = 4.5 \text{ V}, \text{B}_{0} = \text{B}_{1} = 1.5 \text{ V}, \text{R}_{L} = 50 \Omega, \\ \text{C}_{L} = 35 \text{pF}$	Room	1	15	-		
Charge Injection d	Q	C_L = 1 nF, R_{GEN} = 0 Ω , V_{GEN} = 0 V	Room	-	50	-	рС	
		R_L = 50 Ω , f = 1 MHz	Room	-	-58	-		
Off Isolation ^d	OIRR	R_L = 50 Ω , f = 10 MHz	Room	-	-31	-	٩D	
Crosstalk ^d	v	R_L = 50 Ω , C_L = 5 pF, f = 1 MHz	Room	-	-63	-	dB	
Grosstaik "	X _{TALK}	R_L = 50 Ω , C_L = 5 pF, f = 10 MHz	NUUIII	-	-36	-		
Bandwidth ^d	BW	R _L = 50 Ω	Room	-	117	-	MHz	
Total Harmonic Distortion ^d	THD	$R_{L}\text{=}$ 600 Ω,V_{IN} = 0.5 V, f = 20 kHz to 20 kHz	Room	-	0.02	-	%	
Capacitance								
BX Port Off Capacitance ^d	C _{B(OFF)}			-	20	-		
A Port On Capacitance ^d	C _{A(ON)}	R_L = 50 Ω , C_L = 5 pF, f = 1 MHz	Room	-	57	-	pF	
Control Pin Capacitance d	CIN			-	5	-		

Notes

a. Room = 25 °C, Full = as determined by the operating suffix.

b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.

c. Typical values are for design aid only, not guaranteed nor subject to production testing.

d. Guarantee by design, nor subjected to production test.

e. V_{IN} = input voltage to perform proper function.

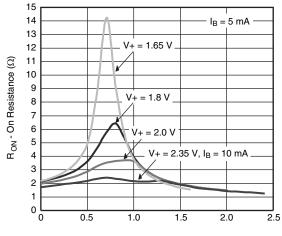
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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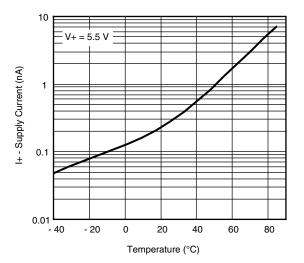
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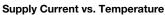
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

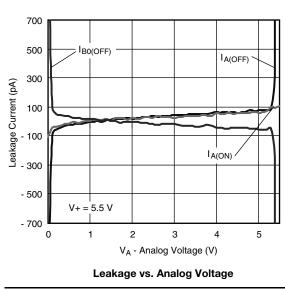


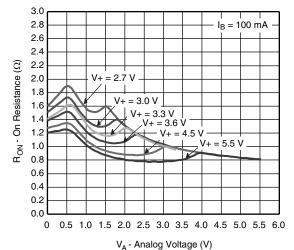
V_A - Analog Voltage (V)

R_{ON} vs. V_A and Supply Voltage

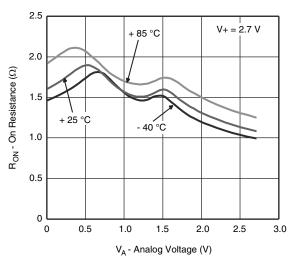




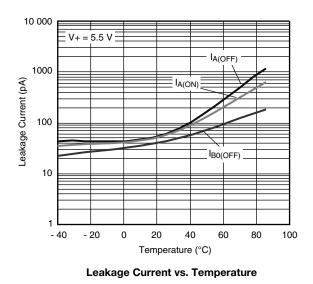




R_{ON} vs. V_A and Supply Voltage



 R_{ON} vs. V_{D} and Temperature



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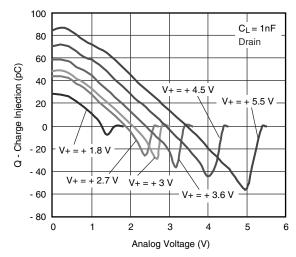
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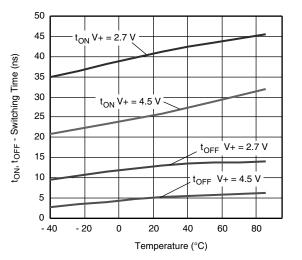
DG4157 Vishay Siliconix



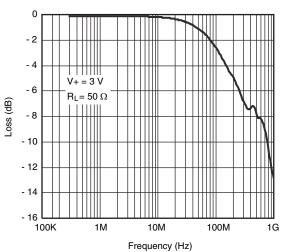
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



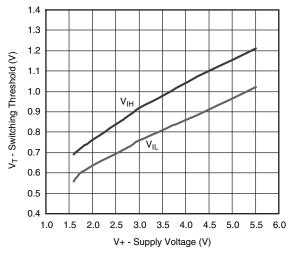
Charge Injection vs. Analog Voltage



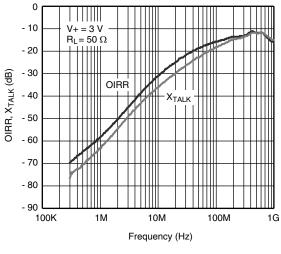
Switching Time vs. Temperature



Insertion Loss vs. Frequency



Switching Threshold vs. Supply Voltage



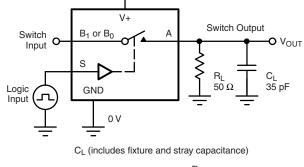
Off-Isolation and Crosstalk vs. Frequency

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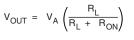
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TEST CIRCUITS



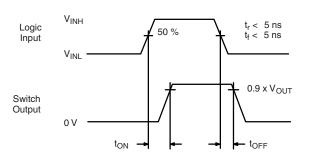
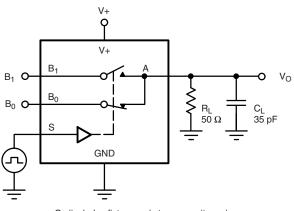
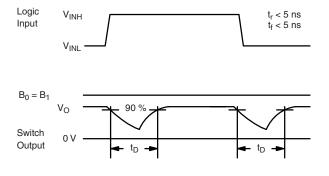


Fig. 1 - Switching Time





CL (includes fixture and stray capacitance)

Fig. 2 - Break-Before-Make Interval

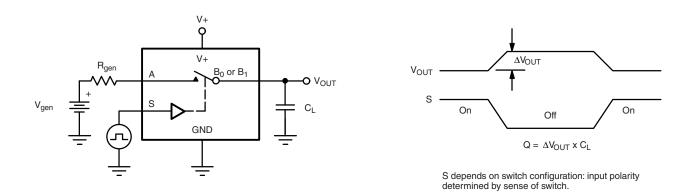


Fig. 3 - Charge Injection

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TEST CIRCUITS

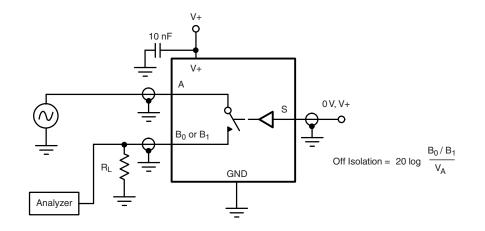


Fig. 4 - Off-Isolation

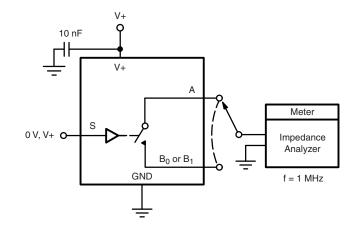


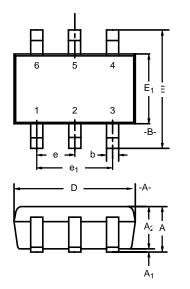
Fig. 5 - Channel Off/On Capacitance

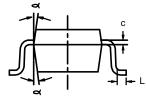
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68800.



Package Information Vishay Siliconix

SC-70: 6-LEADS

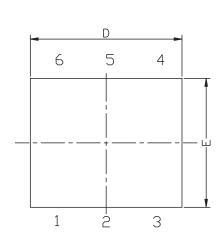


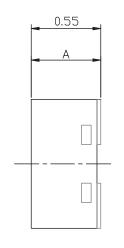


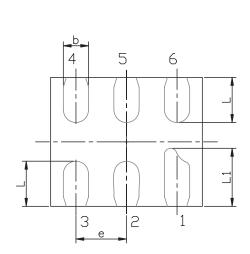
	MIL	LIMET	ERS	INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A ₁	-	-	0.10	-	-	0.004
A ₂	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	0.012	
С	0.10	-	0.25	0.004	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071 0.083 0.0		0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е		0.65BSC			0.026BSC	;
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
م		7°Nom			7°Nom	
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						

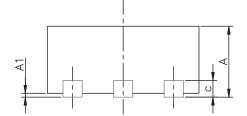


MINI QFN-6L CASE OUTLINE







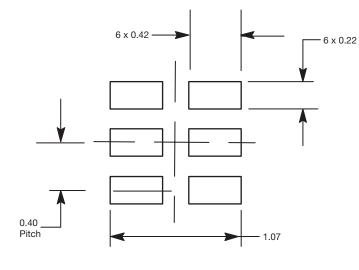


DIM	MILLIMETERS			INCHES			
DIW	MIN.	NAM.	MAX.	MIN.	NAM.	MAX.	
А	0.50	0.55	0.60	0.0197	0.0217	0.0236	
A1	0.00	-	0.05	0.000	-	0.002	
b	0.15	0.20	0.25	0.006	0.008	0.010	
с	0.15 REF			0.006 REF			
D	1.15	1.20	1.25	0.045 0.047		0.049	
E	0.95	1.00	1.05	0.037	0.039	0.041	
е	0.40 BSC				0.016 BSC		
L	0.30	0.35	0.40	0.012	0.014	0.016	
L1	0.40	0.45	0.50	0.016	0.018	0.020	

ECN T-07039-Rev. A, 12-Feb-07	
DWG: 5958	



RECOMMENDED MINIMUM PADS FOR MINI QFN 6L



Mounting Footprint Dimensions in mm



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