

## OSCILLATOR (3.2 x 2.5 x 1.1mm)

### FEATURES:

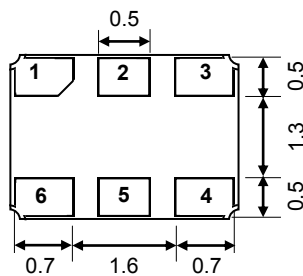
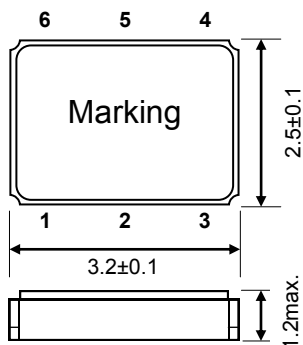
- HIGH STABILITY OVER EXTENDED TEMPERATURE RANGE
- WIDE FREQUENCY RANGE



### ■ SPECIFICATION

FREQUENCY RANGE		2.500MHz ~ 55.000MHz
FREQUENCY STABILITY		±10ppm ~ ±20ppm (overall condition)
OPERATING TEMPERATURE RANGE		STD: -30°C ~ 85°C, Option: -40°C ~ +85°C (See P/N guide for other options)
STORAGE TEMPERATURE RANGE		-55°C ~ 125°C
SUPPLY VOLTAGE ± 5%		+2.5V <sub>DC</sub> ~ +3.3V <sub>DC</sub>
SUPPLY CURRENT (Pin#1 = Open or VIH)		7mA max.
OUTPUT	LOGIC FAMILY	HCMOS
	LOAD	15pF max.
	LEVEL LOGIC '1' (V <sub>OH</sub> )	0.9 x V <sub>DD</sub> min.
	LEVEL LOGIC '0' (V <sub>OL</sub> )	0.1 x V <sub>DD</sub> max.
	SYMMETRY(DUTY CYCLE)	45% / 55% @ 1/2V <sub>DD</sub> level
	RISE AND FALL TIME (Tr/Tf)	5nS max.
	START-UP TIME	10mS max.
STAND-BY VOLTAGE	ENABLE (V <sub>IH</sub> )	V <sub>DD</sub> - 0.5V <sub>DC</sub> min.
	DISABLE (V <sub>IL</sub> )	0.5V <sub>DC</sub> max.
STAND-BY CURRENT (Pin#1 = V <sub>IL</sub> )		10µA max.
ENABLE DELAY TIME		10mS max.
DISABLE DELAY TIME		100nS max.
AGING @ +25°C ±3°C		±3.0ppm / 1 <sup>st</sup> year max.
PHASE JITTER RMS (12kHz ~ 20MHz)		100fS typ. 500fS max.
PHASE NOISE		-130dBc/Hz max. @ 1kHz offset

### ■ PACKAGE DIMENSIONS

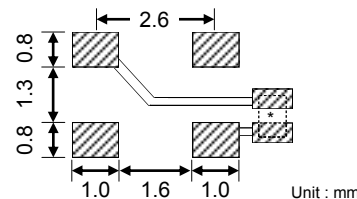


Unit : mm

Pin Configuration	
1	Disable Enable or N.C.
2	N.C.
3	GND
4	Z* Output
5	N. C.
6	V <sub>DD</sub>

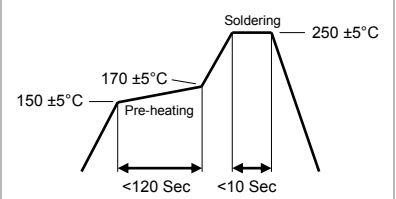
\*Z: high impedance

### ■ SOLDER PATTERN



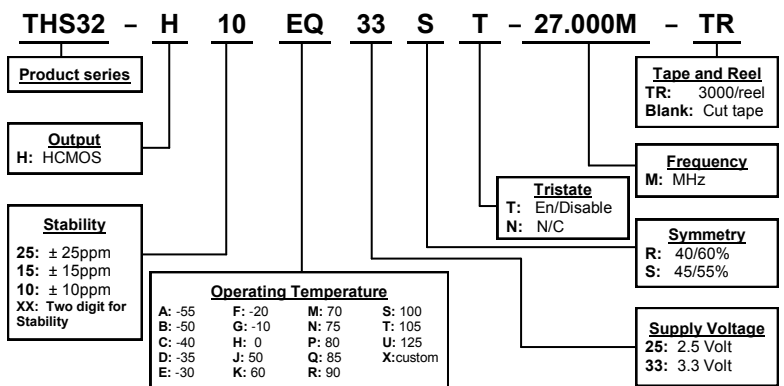
\*0.01µF to 0.1µF bypass capacitor recommended

### ■ REFLOW PROFILE



Note: Consult factory for detail

### ■ PART NUMBERING GUIDE



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