

Transistors

4V Drive Nch MOS FET

RSQ035N03

●Structure

Silicon N-channel MOS FET

●Features

- 1) Low On-resistance.
- 2) Space saving, small surface mount package (TSMT6).

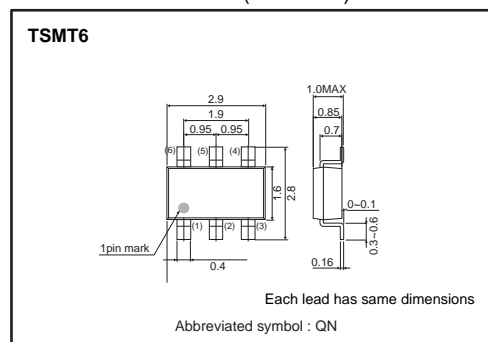
●Applications

Switching

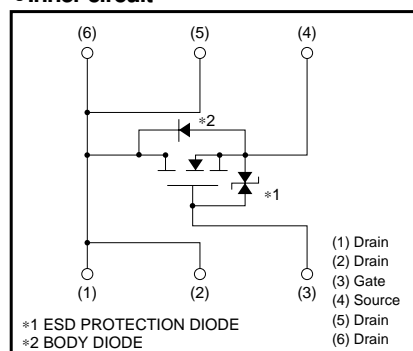
●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ035N03		○

●External dimensions (Unit : mm)



●Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V_{DS}	30	V	
Gate-source voltage	V_{GS}	20	V	
Drain current	Continuous	I_D	± 3.5	A
	Pulsed	I_{DP} *1	± 14	A
Source current (Body diode)	Continuous	I_S	1.0	A
	Pulsed	I_{SP} *1	14	A
Total power dissipation	P_D *2	1.25	W	
Channel temperature	T_{ch}	150	°C	
Range of storage temperature	T_{stg}	-55 to +150	°C	

*1 $P_w \leq 10 \mu s$, Duty cycle $\leq 1\%$
*2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th(ch-a)}$ *	100	°C/W

* Mounted on a ceramic board

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	–	–	10	μA	V _{GS} =20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	–	–	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	–	–	1	μA	V _{DS} = 30V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	1.0	–	2.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state resistance	R _{DS(on)} *	–	44	62	mΩ	I _D = 3.5A, V _{GS} = 10V
		–	60	84	mΩ	I _D = 3.5A, V _{GS} = 4.5V
		–	67	94	mΩ	I _D = 3.5A, V _{GS} = 4V
Forward transfer admittance	Y _{fs} *	2.0	–	–	S	V _{DS} = 10V, I _D = 3.5A
Input capacitance	C _{iss}	–	290	–	pF	V _{DS} = 10V
Output capacitance	C _{oss}	–	85	–	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	–	55	–	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	–	7	–	ns	V _{DD} ≐ 15V
Rise time	t _r *	–	9	–	ns	I _D = 1.75A
Turn-off delay time	t _{d(off)} *	–	24	–	ns	V _{GS} = 10V
Fall time	t _f *	–	6	–	ns	R _L =8.57Ω
Total gate charge	Q _g *	–	5.3	7.4	nC	V _{DD} ≐ 15V V _{GS} = 5V
Gate-source charge	Q _{gs} *	–	1.0	–	nC	I _D = 3.5A
Gate-drain charge	Q _{gd} *	–	1.4	–	nC	R _L = 4.29Ω R _G =10Ω

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD}	–	–	1.2	V	I _S = 1.0A, V _{GS} =0V

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