

4V Drive Nch MOSFET

RSH065N06

●Structure

Silicon N-channel MOSFET

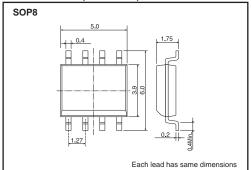
● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).

Application

Switching

●Dimensions (Unit : mm)



Packaging specifications

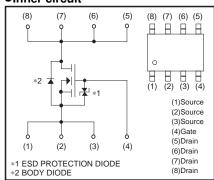
	Package	Taping	
Type	Code	TB	
	Basic ordering unit (pieces)	2500	
RSH065N06	0		

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	60	V	
Gate-source voltage		V _{GSS}	20	V	
Drain current	Continuous	I _D	±6.5	A	
	Pulsed	IDP *1	±26	А	
Source current	Continuous	Is	1.6	А	
(Body diode)	Pulsed	I _{SP} *1	26	А	
Total power dissipatino		P _D *2	2.0	W	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board.

●Inner circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-A) *	62.5	°C/W

^{*} Mounted on a ceramic board.

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●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	_	-	10	μА	Vgs=20V, Vps=0V
Drain-source breakdown voltage	V _(BR) DSS	60	_	_	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	_	-	1	μА	V _{DS} =60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	_	2.5	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	*	_	24	37		ID=6.5A, VGS=10V
	R _{DS} (on)	_	28	44	mΩ	I _D =6.5A, V _{GS} =4.5V
		_	31	48		I _D =6.5A, V _{GS} =4.0V
Forward transfer admittance	Y _{fs} *	4	-	_	S	I _D =6.5A, V _{DS} =10V
Input capacitance	Ciss	_	900	_	pF	V _D s=10V
Output capacitance	Coss	_	200	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	100	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	13	_	ns	I _D =3.3A, V _{DD} ≒30V
Rise time	tr *	_	25	_	ns	Vgs=10V
Turn-off delay time	t _{d (off)} *	_	60	_	ns	R _L =9.1Ω
Fall time	t _f *	_	20	_	ns	R _G =10Ω
Total gate charge	Qg *	_	11	16	nC	I _D =6.5A, V _{DD} ≒30V
Gate-source charge	Q _{gs} *	-	2	-	nC	V _{GS} =5V
Gate-drain charge	Q _{gd} *	_	4	_	nC	$R_L=4.6\Omega$, $R_G=10\Omega$

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	_	_	1.2	V	I _S =1.6A, V _{GS} =0V

^{*}Pulsed

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•Electrical characteristic curves

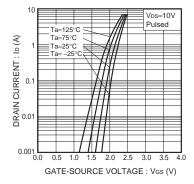


Fig.1 Typical Transfer Characteristics

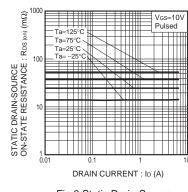


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current(I)

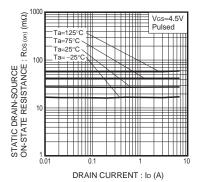


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current(II)

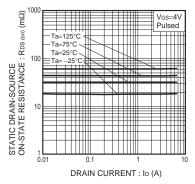


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(III)

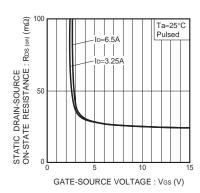


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

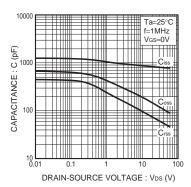


Fig.6 Typical Capacitance vs. Drain-Source Voltage

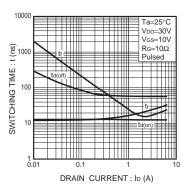


Fig.7 Switching Characteristics

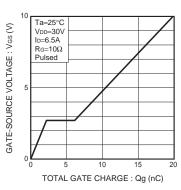


Fig.8 Dynamic Input Characteristics

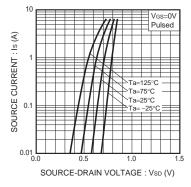


Fig.9 Source Current vs. Source-Drain Voltage

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●Measurement circuit

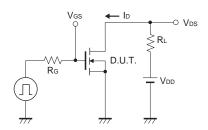


Fig.10 Switching Time Test Circuit

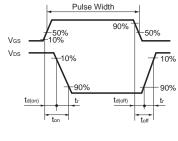


Fig.11 Switching Time Waveforms

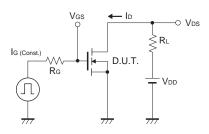


Fig.12 Gate Charge Test Circuit

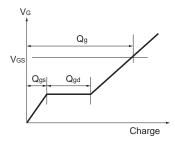


Fig.13 Gate Charge Waveform

Notes

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