

## Dear ATMEL AT88CK201STK customer,

Thank you for selecting the AT88CK201STK evaluation kit. We hope this kit will be a useful platform as you explore the full potential of the CryptoRF Reader chip, AT88RF1354, and the CryptoRF memory products.

The AT88CK201STK is designed to be used with Atmel's JTAGICE mkII debugger, AVR Studio developer's suite and the ATMEL Crypto Evaluation Studio (ACES). This seamless toolchain provides a convenient and powerful environment for generating, debugging and testing your embedded software. For more information on these and other CryptoRF development tools, visit <a href="https://www.atmel.com/cryptokits">www.atmel.com/cryptokits</a>.

#### **IMPORTANT MESSAGE!**

Atmel posts frequent updates to the AT88CK201STK software and documentation on our website. You may download the latest version for free from <a href="https://www.atmel.com/cryptokits">www.atmel.com/cryptokits</a>.

Should you experience problems with this kit, please ensure that you have downloaded and installed the latest software version before contacting an Atmel sales representative.

Thank you for your interest in ATMEL CryptoRF product.

# AT88CK201STK Getting Started Guide

## **Features**

- Installing ACES (ATMEL CRYPTO EVALUATION STUDIO)
- Configuring the board for SPI mode
- · Powering the board
- · Reading a tag

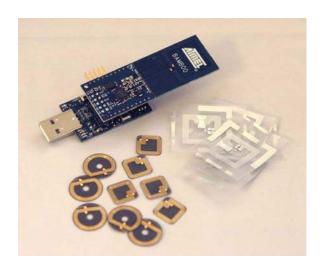
#### 1 Introduction

AT88CK201STK is a CryptoRF reader Starter Kit, which can be used as a reference design and development system for the AT88RF1354 RF reader IC

#### Contents:

- AT88CK201BK modular CryptoRF reader board
- AT88Microbase for demos and development
- Common 10-PIN header for any Atmel STK and EVK
- CryptoRF development library
- JTAG, LED, and buzzer for debug and feedback
- CryptoRF tag assortment and USB cable

Figure 1-1 AT88CK201 Starter Kit





CryptoRF™ AT88RF1354 Reader Board

AT88CK201STK Getting Started Guide



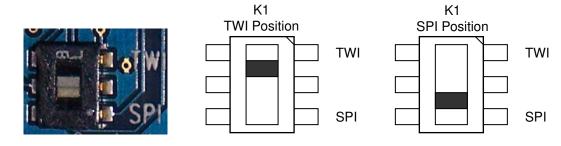


# 1. Installing ACES

Go to www.atmel.com/cryptokits to download and install the latest ACES.

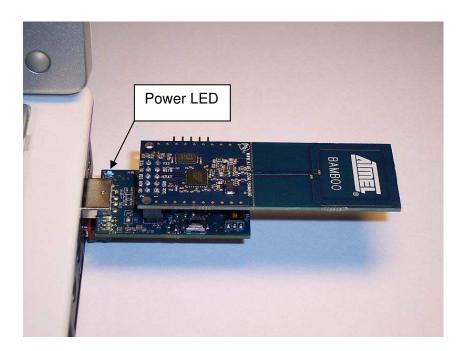
# 2. Configuring the board for SPI mode

Ensure K1 is in SPI mode.



# 3. Powering up the board

The AT88CK201STK is a USB powered device. The AT88Microbase can be directly inserted into the USB port or be used with the 6" USB cable provided.



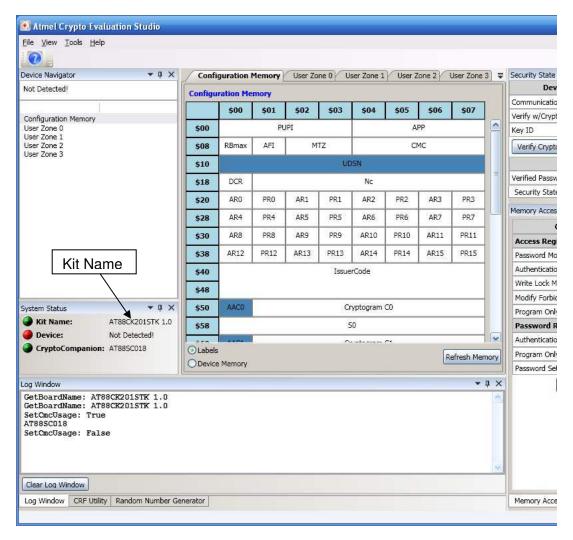
Upon receiving power, the blue power LED "LEDP" will light.

## 4. Reading a tag

- Insert the AT88CK201STK into a USB port (Section 2)
- Launch ACES



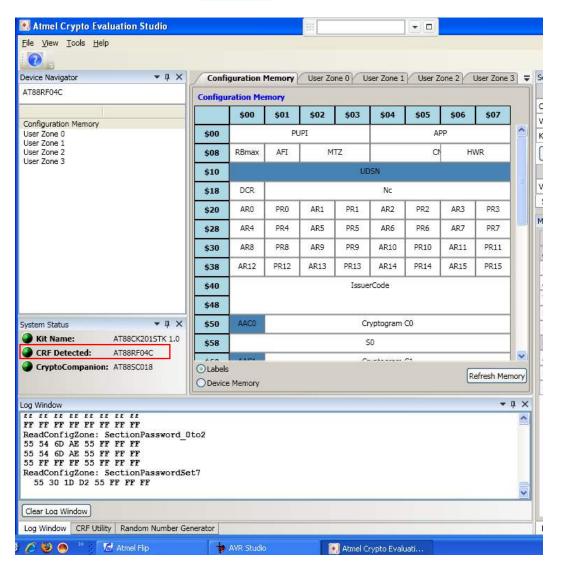
ACES will acknowledge the kit by displaying the kit name.



Place a tag on the antenna portion of the AT88CK201STK.
The "CRF detected" row will now indicate the device number.







- Congratulation, your AT88CK201STK is up and running
- See ACES online Help for additional information

### 5. References and further information

A complete reference design including schematics, Gerber files, BOM, Hardware User Guide, development and demonstration software is conveniently downloadable from the internet at <a href="https://www.atmel.com/cryptokits">www.atmel.com/cryptokits</a>.

#### 6. EVALUATION BOARD/KIT IMPORTANT NOTICE

# 4 Quick Start Guide =

# **CryptoRF Design**

This evaluation board/kit is intended for use for **FURTHER ENGINEERING**, **DEVELOPMENT**, **DEMONSTRATION**, **OR EVAL PURPOSE ONLY**. It is not a finished product and may not (yet) comply with some or any technical or legal requirements that are applicable to finished products, including, without limitations, directives regarding electromagnetic regarding electromagnetic compatibility, recycling (WEEE), FCC, CE or UL (except as may be otherwise noted on the board/kit). Atmel supplied this board/kit "AS IS," without any warranties, with all faults, at the buyer's and further users' sole risk. The user assumes all responsibly and liability for proper and safe handling of goods. Further, the user indemnifies Atmel from claims arising from the handling or use of goods. Due to open construction of the product, it is the user's responsibility to take any and all appropriate precautions with regard to electrostatic discharge and any other technical or legal concerns.

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